**SAMUEL MITCHELL**

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I am a recent 1st Class graduate in BEng in Electrical & Electronic Engineering. I have a lifelong passion for electronics and regularly work on hobby projects using both hardware and software, spanning myriad fields such as signal processing, music, and games. I have worked in multiple successful Engineering team projects, including forming a submission for the Engineering For People Design Challenge, which resulted in an invitation to the national finals.

I am highly interested in RTL design and verification.

**EDUCATION**

**London South Bank University**. BEng Electrical & Electronic Engineering (2020) 1st class honours. *BEng final project “Data Modulation & Demodulation System: Demonstration & Evaluation”*

**EMPLOYMENT**

**Ten Messenger Bags**

*Founder (Feb 2015 – September 2017)*

Design and manufacture of messenger bags and accessories for cyclists and bicycle couriers.

**Self-Employed**

*Bicycle courier (July 2007 – September 2017)*

Ad hoc same-day logistics.

**RELEVANT PROJECTS**

**FPGA Audio Synthesizer:** RTL design and implementation of FPGA core for audio synthesis, utilising DDS techniques and custom SPI module for interfacing with external microcontrollers.

**SKILLS**

**Programming/Hardware languages:** C/C++, Python, MATLAB, Verilog, VHDL

**Software:** Linux, MacOS, Windows, SPICE (Multisim, LTSpice, MacSpice), Quartus, Modelsim

**Hardware:** Oscilloscope, Signal Generator, Spectrum Analyser

**INTERESTS & ACHIEVEMENTS**

**Music**: I have played the drums for 20 years in multiple bands

**Engineering**: LSBU School of Engineering Award for Integrity 2019/20, interested in learning UVM and SystemVerilog