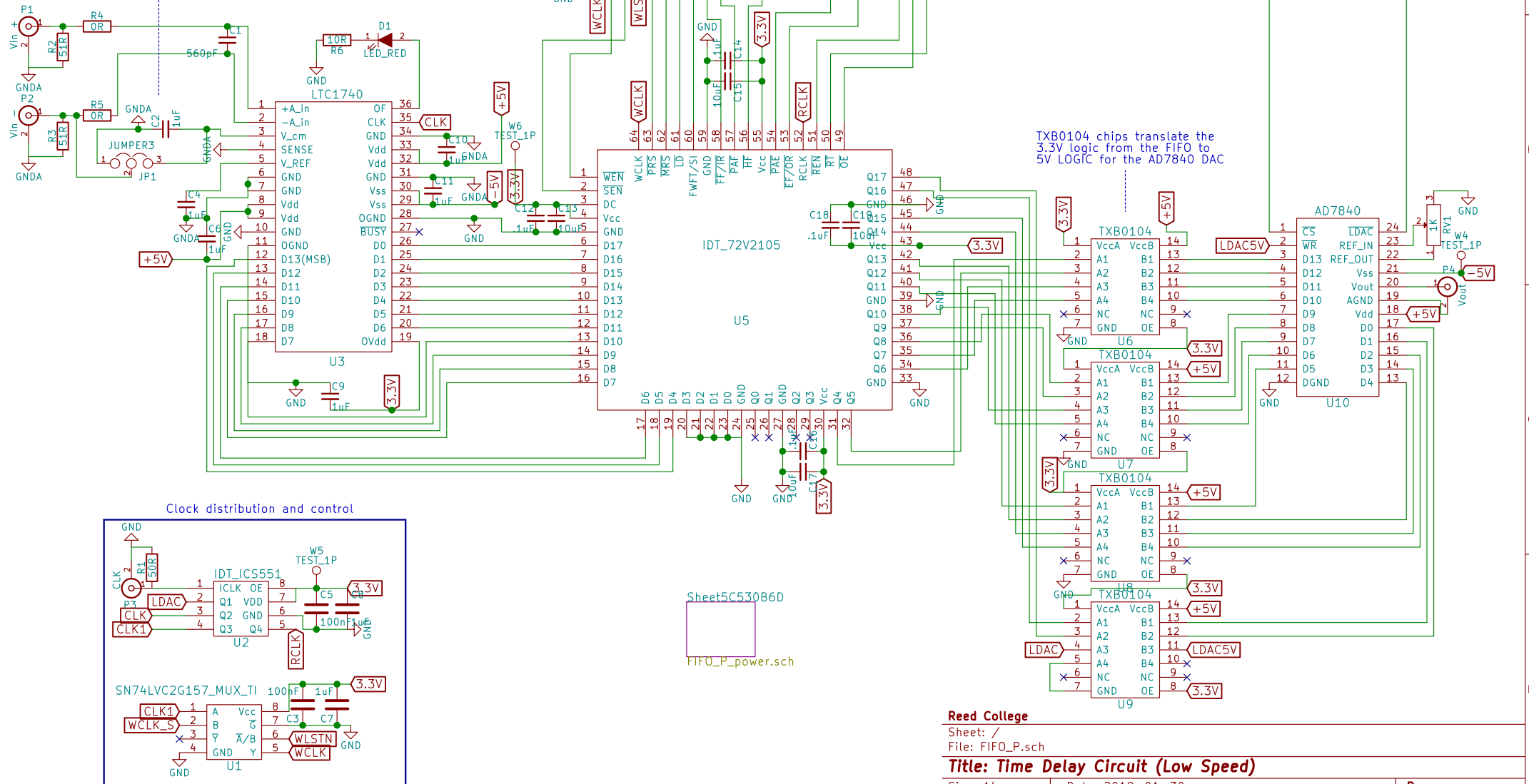


For DC coupled, Dual Supply, non-differential signaling, (i.e. a single signal on a single cable) JP1 connects pins 1 & 2. R4 and C1 create an RC filter to reduce noise.



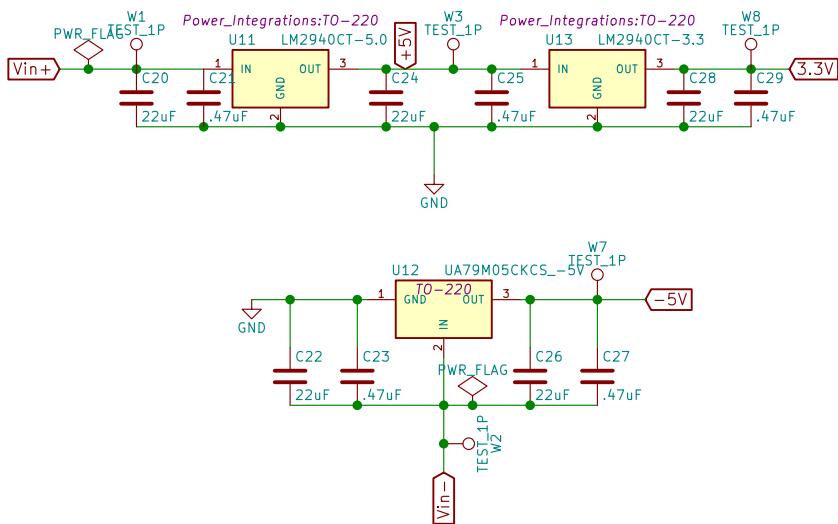
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Sheet: /  
File: FIFO\_P.sch

**Title: Time Delay Circuit (Low Speed)**

Size: A4  
Date: 2019-01-30  
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Rev:  
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A low-noise power supply can improve the accuracy and stability of the circuit.  
This page includes a possible implementation. Using the Suggested chips:

$V_{in+} = 7 \text{ to } 25\text{V}$   
 $V_{in-} = -7 \text{ to } -25\text{V}$

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Sheet: /Sheet5C530B6D/  
File: FIFO\_P\_power.sch

## Title:

Size: A4  
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