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DLD

Lab 3

LT\_SPICE\_NOT\_GATE

1. The Trise is set to 1 second. Thus it takes one second for the pulse voltage to rise to high voltage
2. The voltage jumps all-most (if not completely) vertical from high to low voltage and low to high voltage.

LT\_SPICE\_NAND\_GATE

1. There are 65 level 1, 2, and 3 parameters
2. If Vgd is positive, Cgd is A\*tanh(a\*Vgd)+B. If Vgd is negative, Cgd is C\*atan(a+Vgd)+D