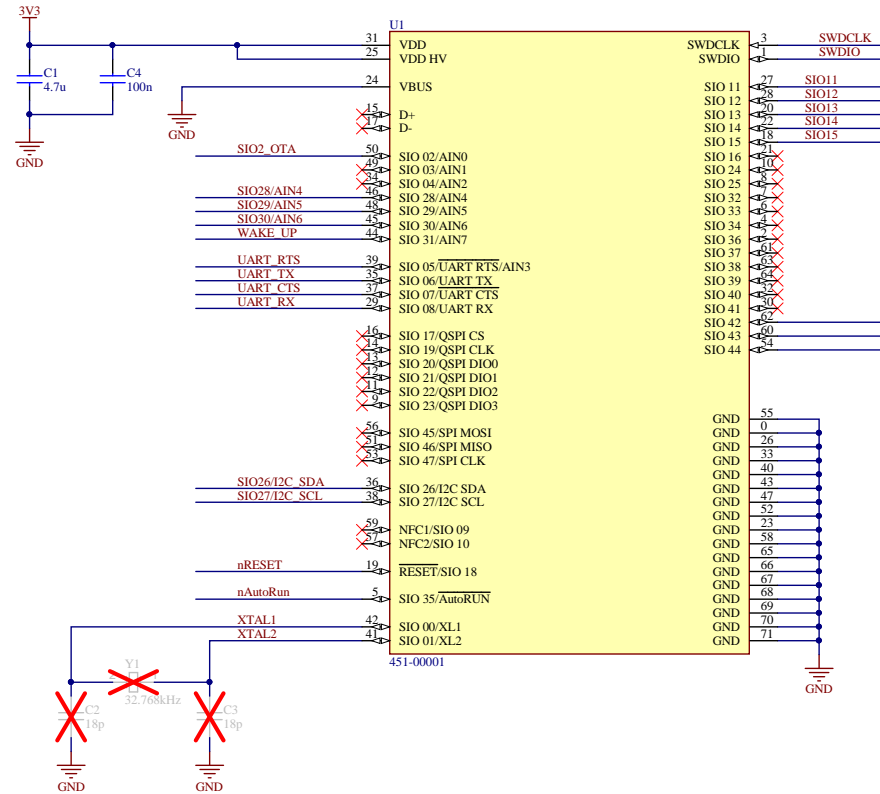
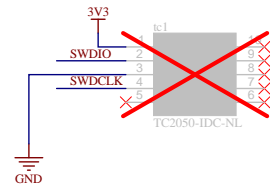
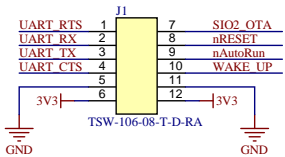


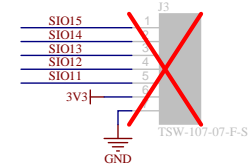
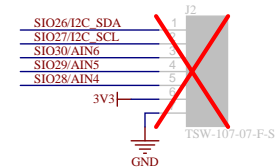
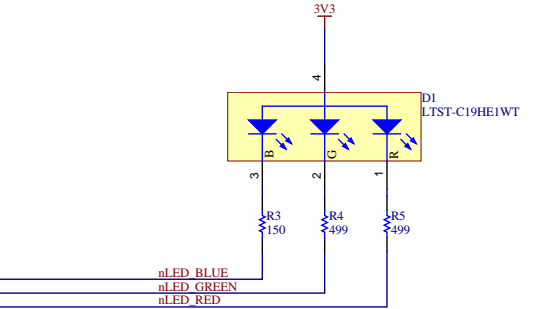
REV. HISTORY

Rev 0.1 - First draft release.
 Rev 0.3 - Added SIO2_OTA signal to support On The Air applications updates.
 Added WAKE_UP signal.
 Rev0.5 Updated symbol for SWD connector to show it is keyed, Updated PMOD connector symbol to be a closer representation of the physical part.
 Rev0.7 Change capacitors for XTAL oscillator from 10pF to 18pF.
 Rev0.9 Added nRESET and SWD connections and 0 Ohms resistors to choose between Reset coming from PMOD or Reset coming from SWD connector.
 Rev0.93 Added two ten position pin stripes for accessibility to extra signals and RGB LED for status indications.
 Rev0.95 Canceled 10 pin headers by 7 pin headers.
 Rev0.96 Changed all capacitors to and resistors to package size 0402(EIA CODE).
 Rev0.97 Changed part number for 1x7 pin headers J2,J3 by TSW-107-07-F-S
 Rev0.98 Changed J2, J3 to DNP (Do not populate)
 Rev0.99 Update note for J1 PMOD connector.
 Rev1.0 Changed series resistor values for RGB LED to improve visibility.

J1 connector pin number assignments per Digilent Pmod Interface Specification 1.2.0 Figure 2



REVISIONS				
REV.	DESCRIPTION	REFERENCE	CHANGED BY	EFF. DATE
			*	*
			*	*
			*	*



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TITLE				
AES-PMOD-NRF-BLE				
DWG SIZE	DWG NO.	REV		
B	AES-PMOD-NRF-BLE	1.0		
NAME	DATE	TIME	SHEET	OF
AES-PMOD-NRF-BLE-G.SchDoc	2019-08-13	12:01:30 PM	1	1