# <u>Lab 4 Parts A + B + C Reference Outputs (bzip2.mtr.gz)</u>

## Part A:

A.bzip2.res

CYCLES : 100000001

CORE\_0\_INST : 100000000 CORE 0 CYCLES : 100000000

CORE 0 IPC : 1.000

MEMSYS\_IFETCH\_ACCESS : 100000000

MEMSYS\_LOAD\_ACCESS : 39627441

MEMSYS\_STORE\_ACCESS : 15885877

MEMSYS\_IFETCH\_AVGDELAY : 0.000

MEMSYS\_LOAD\_AVGDELAY : 0.000

MEMSYS\_STORE\_AVGDELAY : 0.000

DCACHE\_READ\_ACCESS : 39627441
DCACHE\_WRITE\_ACCESS : 15885877
DCACHE\_READ\_MISS : 655444
DCACHE\_WRITE\_MISS : 218653
DCACHE\_READ\_MISS\_PERC : 1.654
DCACHE\_WRITE\_MISS\_PERC : 1.376
DCACHE\_DIRTY\_EVICTS : 325242

### Part B:

## B.S1MB.bzip2.res

CYCLES : 126005161

CORE\_0\_INST : 100000000 CORE\_0\_CYCLES : 126005160 CORE\_0\_IPC : 0.794

MEMSYS\_IFETCH\_ACCESS : 100000000
MEMSYS\_LOAD\_ACCESS : 39627441
MEMSYS\_STORE\_ACCESS : 15885877
MEMSYS\_IFETCH\_AVGDELAY : 1.000
MEMSYS\_LOAD\_AVGDELAY : 1.656
MEMSYS\_STORE\_AVGDELAY : 2.070

ICACHE\_READ\_ACCESS : 100000000
ICACHE\_WRITE\_ACCESS : 0
ICACHE\_READ\_MISS : 222
ICACHE\_WRITE\_MISS : 0
ICACHE\_READ\_MISS\_PERC : 0.000
ICACHE\_WRITE\_MISS\_PERC : 0.000
ICACHE\_WRITE\_MISS\_PERC : 0.000
ICACHE\_DIRTY\_EVICTS : 0

DCACHE\_READ\_ACCESS : 39627441
DCACHE\_WRITE\_ACCESS : 15885877
DCACHE\_READ\_MISS : 655444
DCACHE\_WRITE\_MISS : 218653
DCACHE\_READ\_MISS\_PERC : 1.654
DCACHE\_WRITE\_MISS\_PERC : 1.376
DCACHE\_DIRTY\_EVICTS : 325242

L2CACHE\_READ\_ACCESS : 874319 L2CACHE\_WRITE\_ACCESS : 325242 L2CACHE\_READ\_MISS : 342546 L2CACHE\_WRITE\_MISS : 63029 L2CACHE\_READ\_MISS\_PERC : 39.179 L2CACHE\_WRITE\_MISS\_PERC : 19.379 L2CACHE\_DIRTY\_EVICTS : 183890

DRAM\_READ\_ACCESS : 342546
DRAM\_WRITE\_ACCESS : 183890
DRAM\_READ\_DELAY\_AVG : 100.000
DRAM\_WRITE\_DELAY\_AVG : 100.000

#### Part C

## C.S1MB.CP.bzip2.res

CYCLES	: 126005161

CORE\_0\_INST : 100000000 CORE\_0\_CYCLES : 126005160 CORE\_0\_IPC : 0.794

MEMSYS\_IFETCH\_ACCESS : 100000000
MEMSYS\_LOAD\_ACCESS : 39627441
MEMSYS\_STORE\_ACCESS : 15885877
MEMSYS\_IFETCH\_AVGDELAY : 1.000
MEMSYS\_LOAD\_AVGDELAY : 1.656
MEMSYS\_STORE\_AVGDELAY : 2.070

ICACHE\_READ\_ACCESS : 100000000
ICACHE\_WRITE\_ACCESS : 0
ICACHE\_READ\_MISS : 222
ICACHE\_WRITE\_MISS : 0
ICACHE\_READ\_MISS\_PERC : 0.000
ICACHE\_WRITE\_MISS\_PERC : 0.000
ICACHE\_WRITE\_MISS\_PERC : 0.000
ICACHE\_DIRTY\_EVICTS : 0

DCACHE\_READ\_ACCESS : 39627441
DCACHE\_WRITE\_ACCESS : 15885877
DCACHE\_READ\_MISS : 655444
DCACHE\_WRITE\_MISS : 218653
DCACHE\_READ\_MISS\_PERC : 1.654
DCACHE\_WRITE\_MISS\_PERC : 1.376
DCACHE\_DIRTY\_EVICTS : 325242

L2CACHE\_READ\_ACCESS : 874319 L2CACHE\_WRITE\_ACCESS : 325242 L2CACHE\_READ\_MISS : 342546 L2CACHE\_WRITE\_MISS : 63029 L2CACHE\_READ\_MISS\_PERC : 39.179 L2CACHE\_WRITE\_MISS\_PERC : 19.379 L2CACHE\_DIRTY\_EVICTS : 183890

DRAM\_READ\_ACCESS : 342546 DRAM\_WRITE\_ACCESS : 183890 DRAM\_READ\_DELAY\_AVG : 100.000 DRAM\_WRITE\_DELAY\_AVG : 100.000

## C.S1MB.OP.bzip2.res

CYCLES : 129657001

CORE\_0\_INST : 100000000 CORE\_0\_CYCLES : 129657000 CORE\_0\_IPC : 0.771

MEMSYS\_IFETCH\_ACCESS : 100000000
MEMSYS\_LOAD\_ACCESS : 39627441
MEMSYS\_STORE\_ACCESS : 15885877
MEMSYS\_IFETCH\_AVGDELAY : 1.000
MEMSYS\_LOAD\_AVGDELAY : 1.748
MEMSYS\_STORE\_AVGDELAY : 2.398

ICACHE\_READ\_ACCESS : 100000000
ICACHE\_WRITE\_ACCESS : 0
ICACHE\_READ\_MISS : 222
ICACHE\_WRITE\_MISS : 0
ICACHE\_READ\_MISS\_PERC : 0.000
ICACHE\_WRITE\_MISS\_PERC : 0.000
ICACHE\_DIRTY\_EVICTS : 0

DCACHE\_READ\_ACCESS : 39627441
DCACHE\_WRITE\_ACCESS : 15885877
DCACHE\_READ\_MISS : 655444
DCACHE\_WRITE\_MISS : 218653
DCACHE\_READ\_MISS\_PERC : 1.654
DCACHE\_WRITE\_MISS\_PERC : 1.376
DCACHE\_DIRTY\_EVICTS : 325242

L2CACHE\_READ\_ACCESS : 874319 L2CACHE\_WRITE\_ACCESS : 325242 L2CACHE\_READ\_MISS : 342546 L2CACHE\_WRITE\_MISS : 63029 L2CACHE\_READ\_MISS\_PERC : 39.179 L2CACHE\_WRITE\_MISS\_PERC : 19.379 L2CACHE\_DIRTY\_EVICTS : 183890

DRAM\_READ\_ACCESS : 342546 DRAM\_WRITE\_ACCESS : 183890 DRAM\_READ\_DELAY\_AVG : 125.903 DRAM\_WRITE\_DELAY\_AVG : 144.624