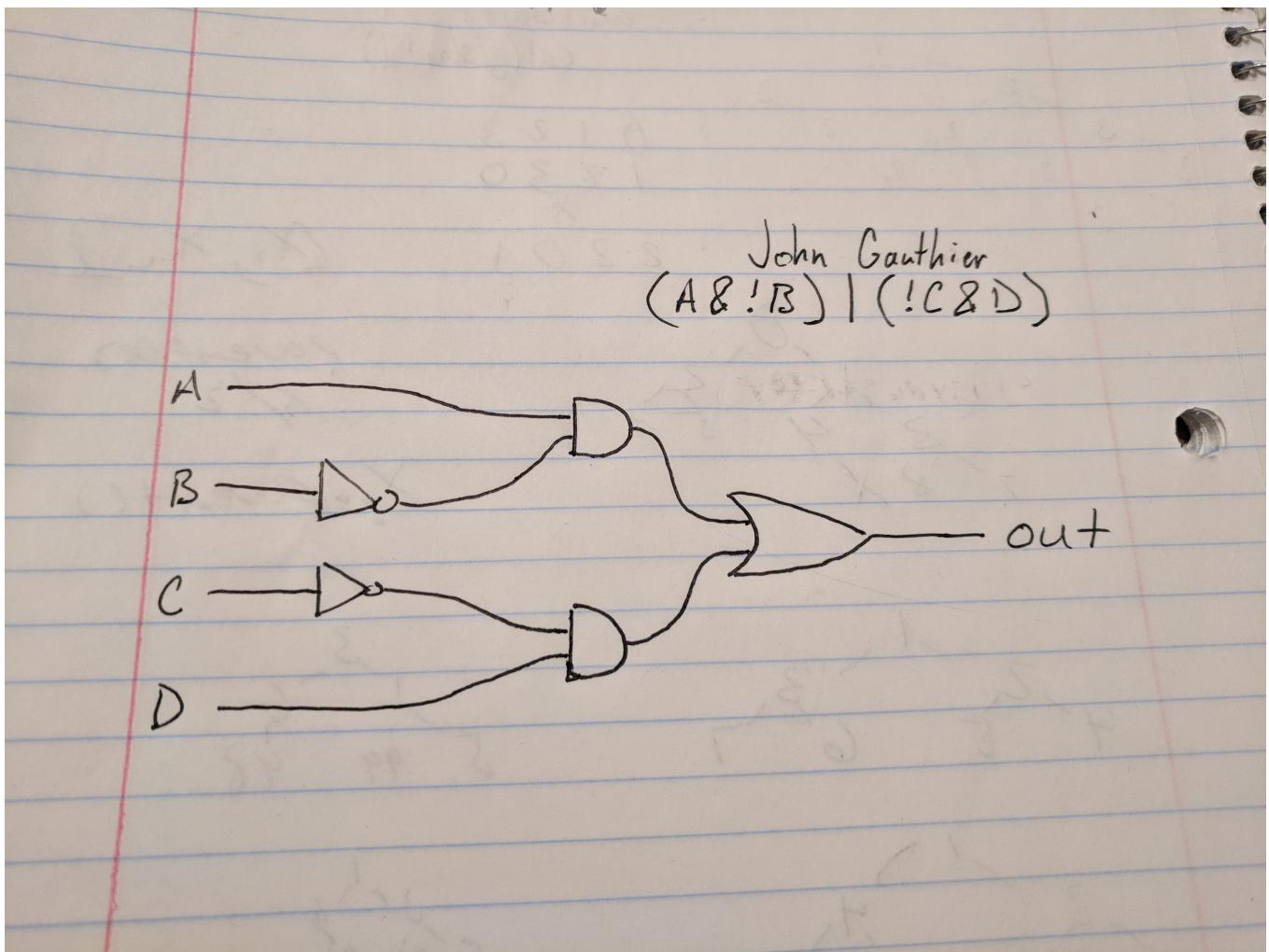


Question 2



# Question 3

iaddl V, rB      |  
  | 0 1 2 3 4 5  
  | C O F I rB V |

Fetch:

icode: ifun  $\leftarrow M_1[PC]$  read instruction byte  
RA: rB  $\leftarrow M_1[PC+1]$  read register byte  
val C  $\leftarrow M_4[PC+2]$  read constant  
val P  $\leftarrow PC + 6$  Compute next PC

Decode:

val B  $\leftarrow R[rB]$  Read value in rB

Execute:

val E  $\leftarrow val B + val C$  Add constant to val B  
Set CC Set condition flags

Memory:

Writeback:

R[rB]  $\leftarrow val E$  Write new value to rB

PC update:

PC  $\leftarrow val P$  update PC