Jeff Bulick

110 Vreeland Avenue – Midland Park, NJ 07432
jgb218@lehigh.edu
(201)-835-6872

OBJECTIVE

To seek a constantly challenging full time entry level position where I can apply my leadership and strong technical background to serve as an invaluable asset that promotes technological innovation and enhances client relations

EDUCATION

Lehigh University, Bethlehem, PA
Bachelor of Science, Computer Engineering; Minor, Economics; Expected May 2018
Major GPA: 3.49
Dean's List: Fall of '14,'15; Spring of '16, '17

Bergen County Academies, Hackensack, NJ
High School Diploma, Academy for Engineering and Design Technology, June 2014
Cumulative GPA: 3.50

RELATED EXPERIENCE

Software Development Intern, KPMG LLP, Montvale, NJ
June – August 2017
- Received immersive training in the development of the firm's tax enterprise systems using the .NET framework, C#, and relational databases through an Agile approach, as a full stack developer
- Served as a liaison between director and the development team by interpreting business requirements and prioritizing the backlog to consistently meet deadlines

Research Fellow, National Science Foundation Smart Spaces REU, Lehigh University
May – August 2016
- Designed a reproducible autonomous mobile robot to be used as a learning platform using ROS as IPC management for a network of onboard sensors to perform efficient odometry in a Linux environment.

Teaching Assistant, Applied Engineering Computer Methods, Lehigh University
January 2016 – January 2017
- Taught fundamentals of programming to students using MATLAB, and then guided them to satisfactory completion of their final projects using a microcontroller to master three design challenges

Lab Assistant, Becton Dickinson R&D, Franklin Lakes, NJ
September 2013 – May 2014
- Received hands on laboratory experience performing physical characteristic data collection on BD's syringes and elastomers

PORTFOLIO

Please visit **www.bulickjeff.com**

PROFICIENCIES

Software: Web Design and Full Stack Applications, Relational Databases, Autonomous Mobile Robotics, Geospatial Analysis, Version Control, Assembly, Visual Studio
Hardware: Verilog, FPGA, Computer Architecture, SPICE, Signals and Systems, Semiconductors, Microcontrollers, Hands on design experience with analog and digital electronic test equipment
Comfortable Languages: C++, C, Java, Frontend languages, SQL

EXTRA-CURRICULAR

Sigma Phi Epsilon, golf, tennis, hiking, weightlifting, racquetball, high school varsity wrestling / football / baseball