

Assignment A1

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Title: Pass I of a two pass assembler

Problem statement: Design suitable data structures and implement pass-I of a two-pass assembler for pseudo machine in Java using object oriented feature. Implementation should consist of a few instructions from each category and few assembler directives.

Objectives:

- Understand the internals of language translators
- Handle tools like LEX and YACC.
- Understand the operating system internals and functionalities with implementation point of view.

s/w Packages and : 64-bit open source Linux
w/w Apparatus used : IntelliJ IDE, JAVA 13 and 15 machines.

Theory:

Assembler is a program which converts assembly language instructions into machine language form. A two pass assembler takes two scans of source code to produce the machine code from assembly language program.

Assembly process consists of following activities:

- Convert mnemonics to their machine language opcode equivalents
- Convert symbolic operands to their machine address
- Translate data constants into internal machine representations.

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- output the object program and provide other information required for linker and loader.

Pass I Tasks:

- Assign addresses to all the statements in the program
- Save the values assigned to all labels for use in Pass II
- Perform processing of assembler directives

Description using set theory:

Let 'S' be set which represents a system

$$S = \{I, O, T, D, \text{succ}, \text{fail}\}$$

Where,

- I = Input
- O = Output
- T = Type (variant I or II)
- D = Data structures

$I = \{SF, MF\}$
 where

SF = Source Code File
 MF = Manemonic Table

$O = \{st, lt, lc\}$

Where,

- st = symbol
- lt = literal
- lc = Intermediate Code File

$$ST = \{N, A\}$$

where N = Name of Symbol
 A = Address of Symbol

$$Lt = \{N, A\}$$

where N = Name of Literal
 A = Address of Literal

T = variant II

$$D = \{Ar, Fl, Sr\}$$

Where,

Ar = Array

Fl = file

Sr = structures

• Test Cases :

Test case	Expected output	Actual Result
Input all valid Mnemonics	Replace the mnemonics with correct opcode	Success
Input the instruction and operands in valid format	Generate Valid intermediate Code format	Success

• Conclusion :-

We were able to parse and tokenize the assembly source code, perform the LC processing, generate the Intermediate file. Successfully.