

# Sensor

## Temperature Digital Powder

2012 Tm / F4 Tm

interface two-wire and SMBus  
allow up and  
compatibility, 2-wire  
3.0V (max)  
1.4V RANGE: SUPPLY

owners. responsible



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ELECTRICAL CHARACTERISTICS
















## SERIAL INTERFACE

The TMP102 operates as a slave device only on the two-wire bus and SMBus. Connections to the bus are made via

This action is accomplished by issuing a slave address byte with the R/W bithed followed by the Pointer Register byte. Nohed data are required. master can then generate a START

## SERIAL BUS ADDRESS

## SLAVE MODE OPERATIONS

Slave Receiver Mode:


## WRITING/READING OPERATION

Slave Transmitter Mode:





TIMING DIAGRAMS







## PACKAGING INFORMATION

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>

## TAPE AND REEL INFORMATION



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