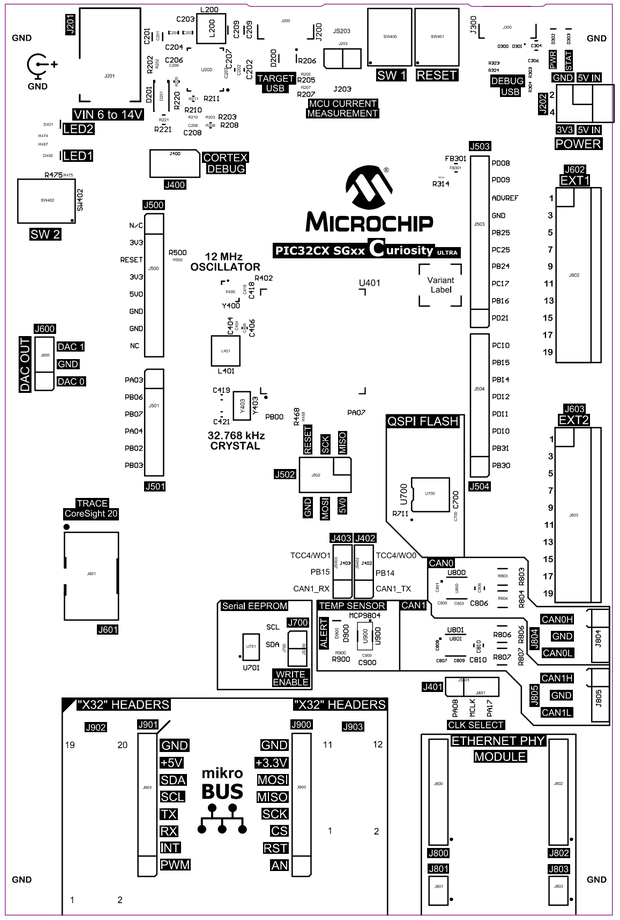
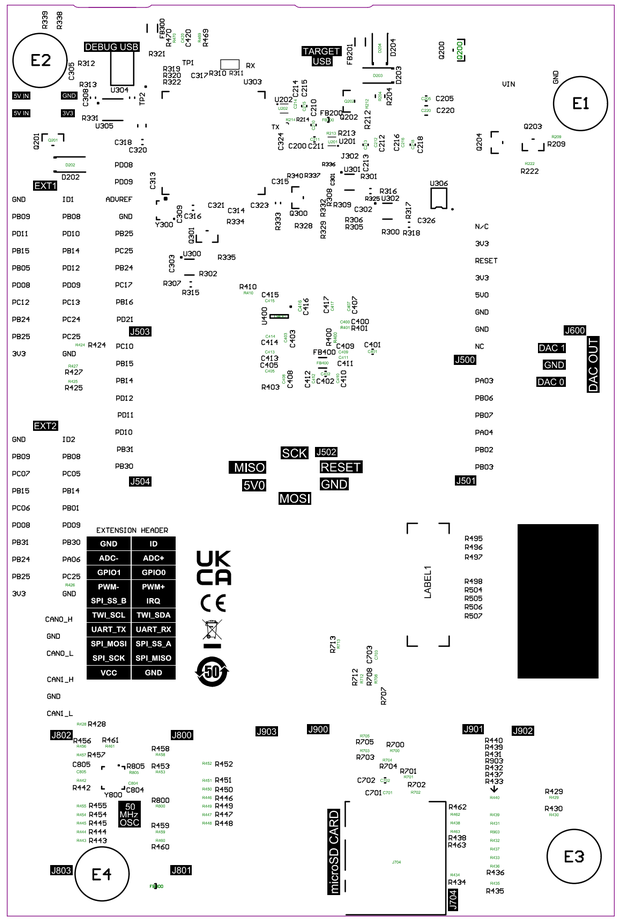


MICROCHIP	ENGINEER: Jerome Angeloni	TITLE: PIC32CX SGxx CURIOSITY ULTRA
	PCB DESIGNER: Ricardo Tejeda	
	DATE: 8/29/2022	DWG NO: 02-00576-D — REV: 5
	FILE NAME: PIC32CX SG_CUltra_R5.PcbDoc	



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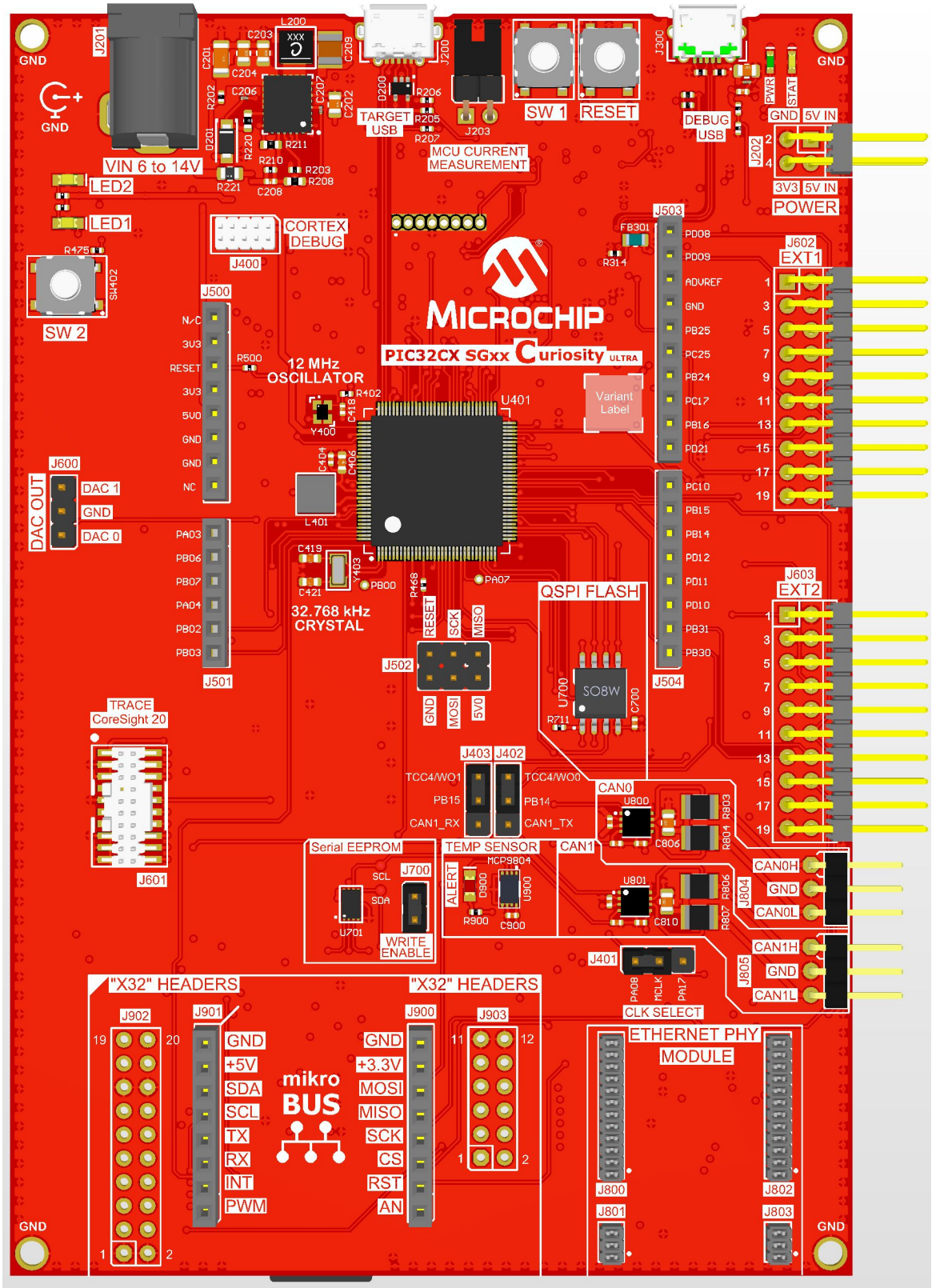
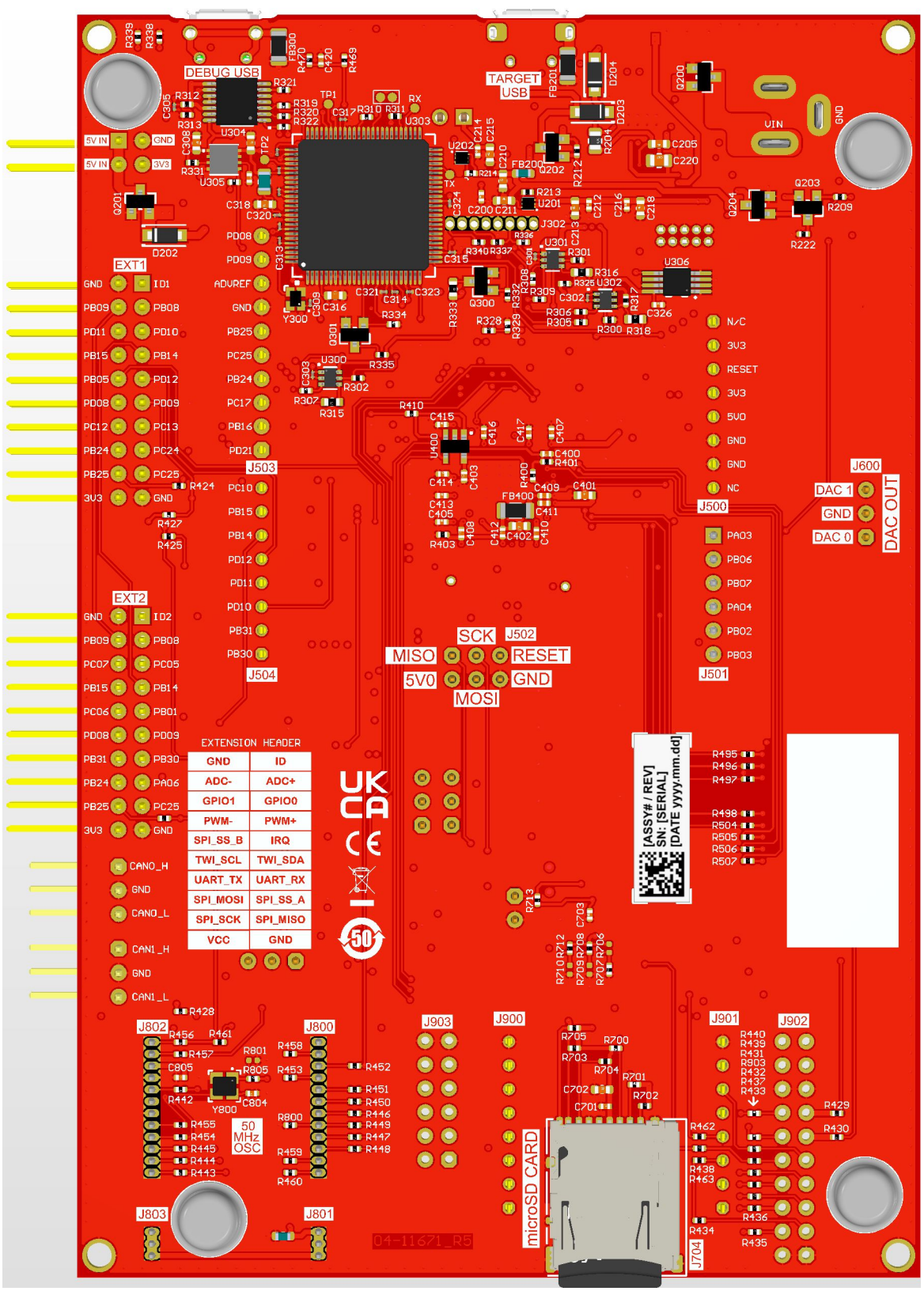
	FILE NAME: PIC32CX SG_CUltra_R5.PcbDoc	


GND
J201
VIN 6 to 14V
LED2
LED1
R475
SW402
SW 2
L200
C203
C204
C206
C201
R202
D201
R220
R221
C207
C202
C209
R211
R210
R203
R208
C208
J200
TARGET USB
D200
R206
R205
R207
J203
MCU CURRENT MEASUREMENT
SW 1
RESET
J300
DEBUG USB
PWR
STAT
GND
5V IN
J202
3V3
POWER
J400
CORTEX DEBUG
J500
N/C
3V3
RESET
5V0
NC
R500
12 MHz OSCILLATOR
Y400
R402
C418
U401
C404
C406
L401
J600
DAC OUT
DAC 1
DAC 0
PA03
PB06
PB07
PA04
PB02
PB03
J501
C419
C421
Y403
PB00
R468
PA07
32.768 kHz CRYSTAL
RESET
SCK
MISO
J502
MOSI
5V0
TRACE CoreSight 20
J601
J403
J402
TCC4/WO1
PB15
CAN1_RX
TCC4/WO0
PB14
CAN1_TX
Serial EEPROM
SCL
SDA
J700
U701
WRITE ENABLE
TEMP SENSOR
MCP9804
ALERT
D900
U900
R900
C900
CAN0
U800
R803
R804
C806
CAN1
U801
R806
R807
C810
J604
CAN0H
GND
CAN0L
J605
CAN1H
GND
CAN1L
J401
PA08
MCLK
PA17
CLK SELECT
"X32" HEADERS
J902
J901
GND
+5V
SDA
SCL
TX
RX
INT
PWM
mikro BUS
"X32" HEADERS
J900
GND
+3.3V
MOSI
MISO
SCK
CS
RST
AN
J903
ETHERNET PHY MODULE
J800
J801
J802
J803
J503
FB301
R314
PD08
PD09
ADVREF
GND
PB25
PC25
PB24
PC17
PB16
PD21
PC10
PB15
PB14
PD12
PD11
PD10
PB31
PB30
J504
J602
EXT1
J603
EXT2
MICROCHIP
PIC32CX SGxx Curiosity ULTRA
Variant Label
U700
SO8W
R711
C700


DEBUG USB
TARGET USB
VIN
GND
EXT1
EXT2
J503
J504
J500
J501
J600
DAC OUT
DAC 1
GND
DAC 0
SCK
J502
MISO
RESET
5V0
GND
MOSI
EXTENSION HEADER
GND ID
ADC- ADC+
GPIO1 GPIO0
PWM- PWM+
SPI_SS_B IRQ
TWI_SCL TWI_SDA
UART_TX UART_RX
SPI_MOSI SPI_SS_A
SPI_SCK SPI_MISO
VCC GND
J802
J800
J803
J801
J903
J900
J901
J902
J704
microSD CARD
50 MHz OSC
[ASSY# / REV]
SN: [SERIAL]
[DATE yyyy.mm.dd]
04-11671_R5