

# Final OSD Disable Patch and Checksum Fix Log

## Patch Operations

- Patched offset 0x04D4: 0x01 → 0x00
- Patched offset 0x0AC8: 0x01 → 0x00
- Patched offset 0x0B02: 0x01 → 0x00
- Patched offset 0x4526: 0x01 → 0x00

*(All four targeted OSD enable writes found and neutralized.)*

## Checksum Calculation

- Partial sum of [0x0000..0x1FFD] (excluding original checksum bytes): 0xC3A1
  - Computed checksum (two's complement of sum): 0x3C5F
  - Updated checksum bytes at **0x1FFE-0x1FFF** to: 5F 3C (little-endian)
  - Final 16-bit sum of entire image (including new checksum): 0x0000 (expected 0x0000 for success)
  - Verification: **PASS** – firmware checksum corrected and valid
  - Patched firmware binary saved to `out/fw_osd_off_crc_fixed.bin`
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