JACOB HLADKY

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OBJECTIVE   An exciting and engaging summer internship

EDUCATION

California Polytechnic State University, San Luis Obispo
Bachelor of Science in **Computer Engineering,** September 2011 – present
GPA: 3.48   Dean's List – 3 quarters

EMPLOYMENT EXPERIENCE

Cisco Systems, Inc.   San Jose, CA   June 2013 – September 2013
Hardware Engineering I Internship
- Part of Global Development Services group
- Assisted in network configuration and troubleshooting
- Developed large-scale virtual machine deployment solution

PROJECTS

LED Cube   September 2013 – present
- Group project to construct a cube out of more than 3000 LEDs, powered by an ATMega microprocessor
- Developed C program to interface with hardware components in cube
- Designed display interface for fine-grained control of cube LEDs

calpolyieee.org   September 2013 – present
- Group project to develop website for Cal Poly IEEE Student Branch
- Developed REST-ful API in python, and front-end framework in javascript for adding user events to website

Hardware bootloader module   Winter 2013
- Final project for class in hardware design and assembly language
- Wrote VHDL module to load arbitrary instructions onto custom CPU architecture without need for hardware re-synthesis
- Used UART VHDL module to interface with computer

RC Car   Spring 2012
- CPES project to control an RC car with an ATMega microprocessor
- Added distance and light sensors and used data to steer car

SKILLS

- Experience in C, Java, JavaScript, and VHDL
- Familiar with revision control, DBMS, and GNU/Linux

INVOLVEMENT

- Institute of Electrical and Electronics Engineers (IEEE)