BSEC Binary Size Information

1. Platform Supported Currently

Platform	Compiler	TYPE
Cortex-ARM	ARMCC	Cortex-M0, M0+, M3, M4, M4_FPU, M7
Cortex-ARM	GCC	Cortex-M0, M0+, M3, M4, M4_FPU, M7
Cortex-A*	GCC	Cortex-A7
AVR_8bit	AVR-GCC	MegaAVR, XMEGA
AVR_32bit	AVR-GCC	32-bit AVR UC3
ESP	xtensa-lx106-elf-gcc	ESP
MSP430	msp430-elf-gcc	MSP430
Mikroe	MikroC-PIC/STM	PIC32MX, PIC32MZ, Cortext M0, M3, M4, M7
IAR	IAR compiler	Cortex-M0, M0+, M3, M4, M4_FPU, M7
Raspberry pi	Arm-linux-gnueabihf-gcc	Pi0-armv6 32bits, pi3-armv8-a 64bits

2. Binary Size on different platform

Platform Type	Compiler	ROM(.text)/ROM(API) in bytes	RAM(.data+.bss) in bytes	File Size*
Cortex-M0	ARMCC	18799 / 3340	1120	226k
Cortex-M0+		18799 / 3340	1120	226k
Cortex-M3		18025 / 3166	1120	222k
Cortex-M4		18025 / 3166	1120	223k
Cortex-M4_FPU		18999 / 3166	1120	230k
Cortex-M7		18041 / 3170	1120	223k
Cortex-M0	GCC	22328 / 3664	1120	90k
Cortex-M0+		22328 / 3664	1120	90k
Cortex-M3		20496 / 3776	1120	86k
Cortex-M4		20508 / 3724	1120	86k
Cortex-M4_FPU		20676 / 3724	1120	82k
Cortex-M7		21060 / 3812	1120	82k
ARMv8		22416 / 3532	1120	89k
Cortex-A7		21316 / 3884	1120	86k
AVR8bit-MegaAVR	AVR-GCC	43123 / 7814	1064	140k
AVR8bit-XMEGA		42337 / 7742	1064	140k
AVR 32bit		23640 / 4484	1460	137k
ESP32	Elf-gcc	23664 / 4127	1120	184k
ESP8266		28401 / 4749	1120	196k

Msp430		34062 / 5530	1070	124k
Cortex-M0	IAR	22134/3596	1120	159k
Cortex-M0+		22134/3596	1120	159k
Cortex-M3		21608/3516	1120	154k
Cortex-M4		21612/3520	1120	154k
Cortex-M4_FPU		22116/3520	1120	157k
Cortex-M7		21612 /3520	1120	154k
Raspberry-pi0	Arm-linux-gnueabihf-gcc	72484/10224	1120	140k
Raspberry-pi3		73068/10172	1120	138k

*Note: ROM/RAM size is basic requirement of BSEC. File size doesn't count.