4.1)	4:1.1' introduce The Interpretation is the second of the
	AND Rd, Rs, Rt Reg [rd] = heg [Rs] AND Reg [Rt]
	Do cote ExtSel BSrc OpSel Memb Regl WBSrc RegDst PCSr
	Docote ExtSel BSrc OpSel Memb Regw WBSrc RegDst PCSr ALU # Reg Fanc no yes ALU rd pc+4
	ALU operation 1 s AMD. Opsel tells the ALU to perform AMD.
	BSrc tooks into the register. WBSrc is set to use the
	octput of ALU Reglot is rd. PC Ser is Pai4 because NIP of lack of jumps.
	ot lack of jumps, (00)010000 Day
	41.2: 1) Get intration address +101 xxxx 10xx
	2) Cal monda from many
	2) Get operands from memory 3) ALU performs AVD ODIDION & UIA :27.1
	Alcher to reposter 12 11/10/14
	4) White to register 12 M (4) Abba
	4.13: Data Memory doesn't produce an output Branch produces an
	4.13: Data Memory doesn't produce an output Branch produces an
	Godd Keyster 2 (corre)
	Six istriction (in the Aryster ix)
	Christofo 218
	to the nominal 10 to cooper of 100. The part is
<i>ψ.</i>	
	I.

4,73	4.7.1: Sign extended: 00000000000000000000000000000000000
	4.7.1: Sign extended: 00000000000000000000000000000000000
- 76	47.2. ALUOP [1-0] 1 00 M 1 1/2 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3
7	Instruction [5-0] = 20 (00/0/00)
	41 771 46410 -001 - 00 - 114 - 41 - 11 - 11
1	4.7.3; New PC: PC+44 and 2/2/2010 OKA - CONDIGORA
	11716 100 100 to 20 00 10 20 20 20 20 20 20 20 20 20 20 20 20 20
	4.7.4: Willed Mox: 2011/1000
	ALU Max : 20 (00010100) 29mg to Not to
9	Mem/AUMux; 2
Production and the state of the	Branch Mux: PC+4 2001/2000 total 400 (1 1811)
	W75: All 72 main mail shape to 0(2
	4.7.5; ALU: -3,00010100 OWN 200200 JIA(E) Add(PC+4)! PC, 4 79/29/31 3/4/(1)
4/3	Alle 1): PC, 4
	Add(Bronds); PC+4,010000(50)
	4.7.6: Read Register 1: (coor)=3 on page 1: to 1 tugtion
	Padd People 2' 20
3	Redd Register 2: 2 (00010)  Write Pafa : 2   ?
	1 de D fo 1 2 1 ?
	Reg Write: 0
	neg with
Name of the Party of the Owner, where the Party of the Pa	



