Intermediate Programming Day 8

Outline

- Separate compilations
- Makefiles
- Header guards
- Review questions

Source code

Separate source files

- Big software projects are typically split among multiple files
- Code accomplishing related tasks is often grouped together (forming a library of functions)
- Different developers may create/edit/test different pieces

Q: How do different files in a software package communicate?

- A: When compiling functions in one file, we need the declarations of functions in the other file
 - * We could include the declarations at the beginning of the file
 - This causes "code bloat" and makes it hard to see what the code is doing
 - ✓ We gather declarations in header (.h) files and then #include the header files
 - A separate source (.c) file will contain definitions for functions declared in the header file
 - Typically, functions defined in file-name.c are declared in a function named file-name.h
 - We use #include <file-name.h> when the header file is part of the general library
 - We use #include "file-name.h" when the header file is ours

```
#include "func.h"
float mult2add(int x, float y)
      return mult2(x) + y;
int mult2(int a)
       return 2*a:
             func.c
```

```
float mult2add( int x , float y ); int mult2( int a );

func.h
```

```
#include <stdio.h>
#include "func.h"
int main(void)
       printf( "%.2f\t", mult2add( 2 , 3.f ) );
       printf( "%d\n" , mult2( 7 ) );
       return 0;
                       main.c
  >> gcc -std=c99 -pedantic -Wall -Wextra main.c func.c
  >> ./a.out
  7.00
         14
  >>
```

Note:

If we do not include the .h file(s), the compiler can try to guess the declaration:

- It can try to guess the types of the function's input from the arguments passed
- It will assume the output is an int
 - This is right for mult2
 - This is wrong for mult2add

```
float mult2add( int x , float y ); int mult2( int a );

func.h
```

```
#include <stdio.h>
// #include "func.h"

int main( void )
{
    printf( "%.2f\t" , mult2add( 2 , 3.f ) );
    printf( "%d\n" , mult2( 7 ) );
    return 0;
}
```

Note:

If we do not include the .h file(s), the compiler can try

func.h

```
#include <stdio.h>
// #include "func.h"

int main( void )
{
    printf( "%.2f\t" , mult2add( 2 , 3.f ) );
    printf( "%d\n" , mult2( 7 ) );
    return O:
```

Note:

If we do not include the .h file(s), the compiler can try

```
#include <stdio.h>
// #include "func.h"

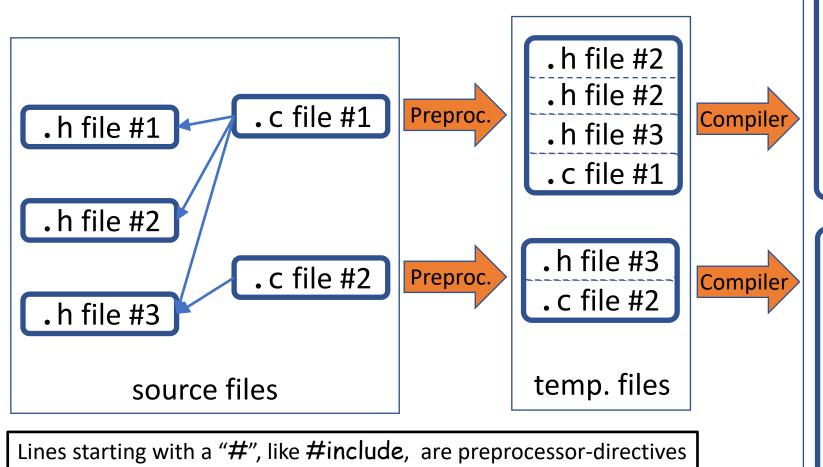
int main( void )
{
    printf( "%.2f\t", mult2add( 2, 3.f));
    printf( "%d\n", mult2( 7));
}
```

The answer is junk because the compiler is reading the 4 float bytes as 4 int bytes

Compiling and linking

- Until now, we've used one gcc command for compilation and linking
 - compiling translates source (.c) files into intermediate object (.o) files
 - linking combines .o files into one executable file called a .out
 (Recall that we can optionally specify the executable name with the -o flag)

Compiling and linking



They are used in the preprocessing phase when constructing the

.o file #1

Linker

. o file #2

a.out executable

source code passed to the compiler

object files

executa

Using header files

When we run gcc, we can do all three steps at once:

- Pre-processing
- Compilation
- Linking

```
#include <stdio.h>
#include "func.h"
int main(void)
       printf( "%.2f\t", mult2add( 2, 3.f));
       printf( "%d\n" , mult2( 7 ) );
       return 0;
                      main.c
  >> gcc -std=c99 -pedantic -Wall -Wextra main.c func.c
```

```
>> gcc -std=c99 -pedantic -Wall -Wextra main.c func.c
>> ./a.out
7.00 14
>>
```

Using header files

When we run gcc, we can do all three steps at once:

- Pre-processing
- Compilation
- Linking

If we modify one of the files, we need to recompile

But we only need to generate new object (.o) files for the modified source files

```
#include <stdio.h>
#include "func.h"
int main(void)
       printf( "hi\n" );
       printf( "%.2f\t", mult2add( 2, 3.f));
       printf( "%d\n" , mult2( 7 ) );
       return 0;
                      main.c
  >> gcc -std=c99 -pedantic -Wall -Wextra main.c func.c
```

```
>> gcc -std=c99 -pedantic -Wall -Wextra main.c func.c
>> ./a.out
hi
7.00 14
>>
```

Using header files

When we run gcc, we can do all three steps at once:

- Pre-processing
- Compilation
- Linking

If we modify one of the files, we need to recompile.

But we only need to generate new object files for the modified source files

```
#include <stdio.h>
#include "func.h"
int main(void)
       printf( "hi\n" );
       printf( "%.2f\t", mult2add( 2, 3.f));
       printf( "%d\n" , mult2( 7 ) );
       return 0;
                      main.c
  >> gcc -std=c99 -pedantic -Wall -Wextra -c main.c
  >> gcc main.o func.o
  >> ./a.out
```

```
hi
7.00
         14
>>
```

We can separately invoke the compiler (with preprocessor) and the linker

Outline

- Separate compilations
- Makefiles
- Header guards
- Review questions

make and Makefiles

- Separately invoking the compiler and linker can be a pain:
 - We need to track dependencies
 - We need to track which files changed since the last time we compiled / linked

make and Makefiles

 make is a tool that helps keep track of which files need to be reprocessed so that those, and only those, are recompiled

- It takes a file containing a list of rules for generating specific files / targets:
 - *Prerequisites*: What targets does this target depend on?
 - *Recipes*: What should be done to generate this target?

make and Makefiles

- make is a tool that helps keep track of which files need to be reprocessed so that those, and only those, are recompiled
- It takes a file containing a list of rules for generating specific files / targets
 - Simplest to name the file Makefile or makefile, otherwise need to run the make command with extra flags (specifying the name of the configuration file)
 - There are strict rules about structure of Makefile, so it's easiest to follow a template and modify
 - Note that tabs and spaces are not equivalent in a Makefile!

Lines in a makefile consist of

```
# Define the compiler and flags
CC=qcc
CFLAGS=-std=c99 -pedantic -Wall -Wextra
# (Default) rule for making the main file
main: main.o func.o
       $(CC) -o main main.o func.o
# Rule for making the main object file
main.o: main.c func.h
       $(CC) $(CFLAGS) -c main.c
# Rule for making the functions object file
func.o: func.c func.h
       $(CC) $(CFLAGS) -c func.c
# Rule for clean-up
clean:
       rm -f *.o
       rm -f main
```

Lines in a makefile consist of:

• Comments, start with a # sign

```
# Define the compiler and flags
CC=qcc
CFLAGS=-std=c99 -pedantic -Wall -Wextra
# (Default) rule for making the main file
main: main.o func.o
       $(CC) -o main main.o func.o
# Rule for making the main object file
main.o: main.c func.h
       $(CC) $(CFLAGS) -c main.c
# Rule for making the functions object file
func.o: func.c func.h
      $(CC) $(CFLAGS) -c func.c
# Rule for clean-up
clean:
       rm -f *.o
       rm -f main
```

Lines in a makefile consist of:

- Comments, start with a # sign
- Definitions, assigned as: constant-name=...

```
# Define the compiler and flags
CC=qcc
CFLAGS=-std=c99 -pedantic -Wall -Wextra
# (Default) rule for making the main file
main: main.o func.o
       $(CC) -o main main.o func.o
# Rule for making the main object file
main.o: main.c func.h
       $(CC) $(CFLAGS) -c main.c
# Rule for making the functions object file
func.o: func.c func.h
       $(CC) $(CFLAGS) -c func.c
# Rule for clean-up
clean:
```

rm -f *.o

rm -f main

Lines in a makefile consist of:

- Comments, start with a # sign
- *Definitions,* assigned as:

```
constant-name=...
and later referenced as:
    $(constant-name)
```

```
# Define the compiler and flags
CC=qcc
CFLAGS=-std=c99 -pedantic -Wall -Wextra
# (Default) rule for making the main file
main: main.o func.o
       $(CC) -o main main.o func.o
# Rule for making the main object file
main.o: main.c func.h
       $(CC) $(CFLAGS) -c main.c
# Rule for making the functions object file
func.o: func.c func.h
      $(CC) $(CFLAGS) -c func.c
# Rule for clean-up
```

clean:

rm -f *.o

rm -f main

Lines in a makefile consist of:

- Comments, start with a # sign
- *Definitions,* assigned as:

```
constant-name=...
and later referenced as:
         $(constant-name)
```

Rules for generating the targets

```
# Define the compiler and flags
CC=qcc
CFLAGS=-std=c99 -pedantic -Wall -Wextra
# (Default) rule for making the main file
main: main.o func.o
       $(CC) -o main main.o func.o
# Rule for making the main object file
main.o: main.c func.h
       $(CC) $(CFLAGS) -c main.c
# Rule for making the functions object file
func.o: func.c func.h
```

\$(CC) \$(CFLAGS) -c func.c

Rule for clean-up

rm -f *.o

rm -f main

clean:

Makefile rules

- Format of a Makefile rule
 - target-name: {dependencies}

```
# Define the compiler and flags
CC=qcc
CFLAGS=-std=c99 -pedantic -Wall -Wextra
# (Default) rule for making the main file
main: main.o func.o
       $(CC) -o main main.o func.o
# Rule for making the main object file
main.o: main.c func.h
       $(CC) $(CFLAGS) -c main.c
# Rule for making the functions object file
func.o: func.c func.h
       $(CC) $(CFLAGS) -c func.c
# Rule for clean-up
clean:
       rm -f *.o
       rm -f main
```

Makefile rules

- Format of a Makefile rule
 - target-name: {dependencies}
 - a set of lines with a tab followed by a command-line instructions

```
# Define the compiler and flags
CC=qcc
CFLAGS=-std=c99 -pedantic -Wall -Wextra
# (Default) rule for making the main file
main: main.o func.o
       $(CC) -o main main.o func.o
# Rule for making the main object file
main.o: main.c func.h
       $(CC) $(CFLAGS) -c main.c
# Rule for making the functions object file
func.o: func.c func.h
       $(CC) $(CFLAGS) -c func.c
# Rule for clean-up
clean:
       rm -t main
```

- Invoke the main tool with the name of the target to build
 - If no target is given, the first is used

```
# Define the compiler and flags
CC=qcc
CFLAGS=-std=c99 -pedantic -Wall -Wextra
# (Default) rule for making the main file
main: main.o func.o
       $(CC) -o main main.o func.o
# Rule for making the main object file
main.o: main.c func.h
       $(CC) $(CFLAGS) -c main.c
# Rule for making the functions object file
func.o: func.c func.h
       $(CC) $(CFLAGS) -c func.c
# Rule for clean-up
clean:
       rm -f *.o
       rm -f main
```

- Invoke the main tool with the name of the target to build
 - If no target is given, the first is used

Examples:

- >> make clean
 - Delete all object files and executable

```
# Define the compiler and flags
CC=qcc
CFLAGS=-std=c99 -pedantic -Wall -Wextra
# (Default) rule for making the main file
main: main.o func.o
       $(CC) -o main main.o func.o
# Rule for making the main object file
main.o: main.c func.h
       $(CC) $(CFLAGS) -c main.c
# Rule for making the functions object file
func.o: func.c func.h
       $(CC) $(CFLAGS) -c func.c
# Rule for clean-up
clean:
```

rm -t main

- Invoke the main tool with the name of the target to build
 - If no target is given, the first is used

Examples:

- >> make func.o
 - Has func.c or func.h changed since the last creation of func.o?
 - Yes: Compile func.c → func.o
 - No: Do nothing

```
# Define the compiler and flags
CC=qcc
CFLAGS=-std=c99 -pedantic -Wall -Wextra
# (Default) rule for making the main file
main: main.o func.o
       $(CC) -o main main.o func.o
# Rule for making the main object file
main.o: main.c func.h
       $(CC) $(CFLAGS) -c main.c
# Rule for making the functions object file
func.o: func.c func.h
       $(CC) $(CFLAGS) -c func.c
# Rule for clean-up
clean:
```

rm -f *.o

rm -f main

- Invoke the main tool with the name of the target to build
 - If no target is given, the first is used

Examples:

- >> make main.o
 - Has main.c or func.h changed since the last creation of main.o?
 - Yes: Compile main.c → main.o
 - No: Do nothing

```
# Define the compiler and flags
CC=qcc
CFLAGS=-std=c99 -pedantic -Wall -Wextra
# (Default) rule for making the main file
main: main.o func.o
       $(CC) -o main main.o func.o
# Rule for making the main object file
main.o: main.c func.h
       $(CC) $(CFLAGS) -c main.c
# Rule for making the functions object file
func.o: func.c func.h
       $(CC) $(CFLAGS) -c func.c
# Rule for clean-up
clean:
       rm -f *.o
       rm -f main
```

- Invoke the main tool with the name of the target to build
 - If no target is given, the first is used

Examples:

- >> make
- >> make main
 - make main.o and func.o
 - Has main.o or func.o changed since the last creation of main?
 - Yes: Link main.o + func.o → main
 - No: Do nothing

```
# Define the compiler and flags
CC=qcc
CFLAGS=-std=c99 -pedantic -Wall -Wextra
# (Default) rule for making the main file
main: main.o func.o
       $(CC) -o main main.o func.o
# Rule for making the main object file
main.o: main.c func.h
       $(CC) $(CFLAGS) -c main.c
# Rule for making the functions object file
func.o: func.c func.h
       $(CC) $(CFLAGS) -c func.c
# Rule for clean-up
clean:
       rm -f *.o
       rm -f main
```

- Invoke the main tool with the name of the target to build
 - If no target is given, the first is used

Examples:

- >> make
- >> make main
 - make main.o and func.o
 - Has main.o or func.o changed since the last creation of main?

```
# Define the compiler and flags
CC=gcc
CFLAGS=-std=c99 -pedantic -Wall -Wextra
```

(Default) rule for making the main file main: main.o func.o
\$(CC) -o main main.o func.o

Rule for making the main object file main.o: main.c func.h \$(CC) \$(CFLAGS) -c main.c

Rule for making the functions object file func.o: func.c func.h \$(CC) \$(CFLAGS) -c func.c

Note:

This means that make can have a cascading effect, with the makeing of one target requiring the makeing of another.

Outline

- Separate compilations
- Makefiles
- Header guards
- Review questions

- You should enclose your header file with a header guard.
 - Preprocessor commands (starts with "#") that ensure that your functions are only declared once

```
#ifndef FUNC_H
#define FUNC_H
float mult2add(int x, float y);
int mult2(int a);
#endif // FUNC_H
func.h
```

- You should enclose your header file with a header guard.
 - Preprocessor commands (starts with "#") that ensure that your functions are only declared once
 - If the pre-processor variable FUNC_H has not been defined

```
#ifndef FUNC_H
#define FUNC_H
float mult2add(int x, float y);
int mult2(int a);
#endif // FUNC_H
func.h
```

- You should enclose your header file with a header guard.
 - Preprocessor commands (starts with "#") that ensure that your functions are only declared once
 - If the pre-processor variable FUNC_H has not been defined
 - Then define it and declare the functions

```
#ifndef FUNC_H
#define FUNC_H
float mult2add(int x, float y);
int mult2(int a);
#endif // FUNC_H
func.h
```

- You should enclose your header file with a header guard.
 - Preprocessor commands (starts with "#") that ensure that your functions are only declared once
 - If the pre-processor variable FUNC_H has not been defined
 - Then define it and declare the functions
 - Otherwise, don't do anything

```
#ifndef FUNC_H
#define FUNC_H
float mult2add( int x , float y );
int mult2( int a );
#endif // FUNC_H
func.h
```

Example:

• The first time we **#include** func.h, **FUNC_H** is undefined, so we define it and include the declarations.

```
#include <stdio.h>
#include "func.h"
#include "func.h"
int main( void )
{
    printf( "%.2f\t" , mult2add( 2 , 3.f ) );
    printf( "%d\n" , mult2( 7 ) );
    return 0;
}
```

```
#include "func.h"
float mult2add(int x, float y)
       return mult2(x) + y;
int mult2(int a)
       return 2*a:
             func.c
```

```
#ifndef FUNC_H
#define FUNC_H
float mult2add(int x , float y );
int mult2(int a );
#endif // FUNC_H
func.h
```

Example:

The second time we #include func.h,
 FUNC_H is defined, so the declarations are ignored.

```
#include <stdio.h>
#include "func.h"
#include "func.h"
int main( void )
{
    printf( "%.2f\t" , mult2add( 2 , 3.f ) );
    printf( "%d\n" , mult2( 7 ) );
    return 0;
}
```

```
#include "func.h"
float mult2add(int x, float y)
       return mult2(x) + y;
int mult2(int a)
       return 2*a:
             func.c
```

```
#ifndef FUNC_H
#define FUNC_H
float mult2add(int x , float y );
int mult2(int a );
#endif // FUNC_H
func.h
```

Example:

• If we don't have a header guard, the compiler doesn't mind. Yet. (As we include more complex C constructs in header files, it will.)

```
#include <stdio.h>
#include "func.h"
#include "func.h"
int main( void )
{
        printf( "%.2f\t" , mult2add( 2 , 3.f ) );
        printf( "%d\n" , mult2( 7 ) );
        return 0;
}
```

```
#include "func.h"
float mult2add(int x, float y)
       return mult2(x) + y;
int mult2(int a)
       return 2*a:
             func.c
```

```
float mult2add( int x , float y );
int mult2( int a );

func.h
```

Outline

- Separate compilations
- Makefiles
- Header guards
- Review questions

1. Why do we need header guards?

2. What is the difference between compiling and linking?

3. What compiler flag do we use to create object files and what extension do those files have?

4. What is a target in a Makefile?

5. What are the advantages of using Makfiles?

Exercise 3-2

• Website -> Course Materials -> Ex3-2