

Figure 1: i_D versus V_{SD} for PMOS with $V_{SG}=2.5\mathrm{V}$

One would hope to acquire similar results in figure (1) for the PMOS as are obtained for the NMOS. However, the results are drastically different. Reliable data for the $V_{GS}=5\mathrm{V}$ case could not be acquired.