# CE3005: Computer Networks
Part I: Tutorial – 2
(Fall 2016)

1. A 64kpbs leased line connects two bank branches and is used for transferring secure bank information. HDLC protocol with 3-bit sequence number is deployed. Determine the minimum window size required to ensure that the throughput is at least 32 Kbps. Assume that the average packet size is 80 bytes, the signal propagation delay between the two sites is 10 *ms*, and the probability of error is negligible. (Hint: *flow control*)

2. In the figure below, frames are generated at node A and sent to node C through node B.
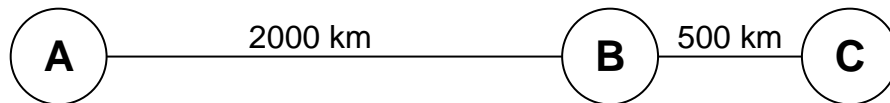
   Determine the minimum transmission rate required between nodes B and C so that the buffers of node B are not flooded, based on the following:

   (a) The data rate between A and B is 100 *Kbps*.
   (b) The propagation delay is 10 *μsec/km* for both links.
   (c) The lines are full duplex between the nodes.
   (d) All data frames are 1000 bit long; ACK frames are separate frames of negligible length.
   (e) Between A and B, a sliding-window protocol with a window size of 3 is used, and each frame is acknowledged individually.
   (f) Between B and C, a stop and wait is used.
   (g) There is no error, and the processing delay at the nodes is negligible.

(Hint: *outgoing rate from B should be at least the same as its incoming rate*)

3. Consider a communication link between two cities of City *S* and City *D*. The frame transmission rate on the link is 100 *kbps* and the frame length is 25 bytes. The distance between City *S* and City *D* is 5 *km* and the propagation delay 3 *ms/km*. The communication link suffers from an average frame error probability of 0.2 and we adopt a Selective-Reject ARQ mechanism between City *S* and City *D* for reliable communication.

   a) If the window size is 10, compute the link utilization from City S to City D.

   b) Compute the minimum window size and the corresponding number of bits reserved for frame sequence in the header, in order to maximize the link utilization between City S and City D.
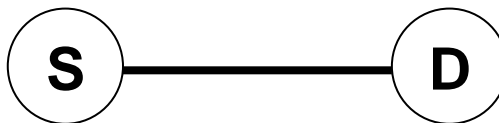
(Hint: *ARQ scheme has window size limit*)

4. Complete the HDLC time sequence diagram by adding the send and receive sequence numbers to the information and supervisory frames. Also include the next few frames that are sent by A and B to completely recover from the error if any. Assume that 3 bits are allocated for sequence numbering.

Diagram (a):

A → B: I(,)
A → B: I(,)
B → A: RR(1)
B → A: I(,)
A → B: I(,)
A → B: I(,)
A → B: I(,)
B → A: I(0,4)
A → B: I(,)

(a)

Diagram (b):

A → B: I(,)
A → B: I(,)
B → A: I(,)
B → A: I(,) → X
A → B: I(6,4)
A → B: I(,)
B → A: SREJ( )
B → A: I(,)

(b)