

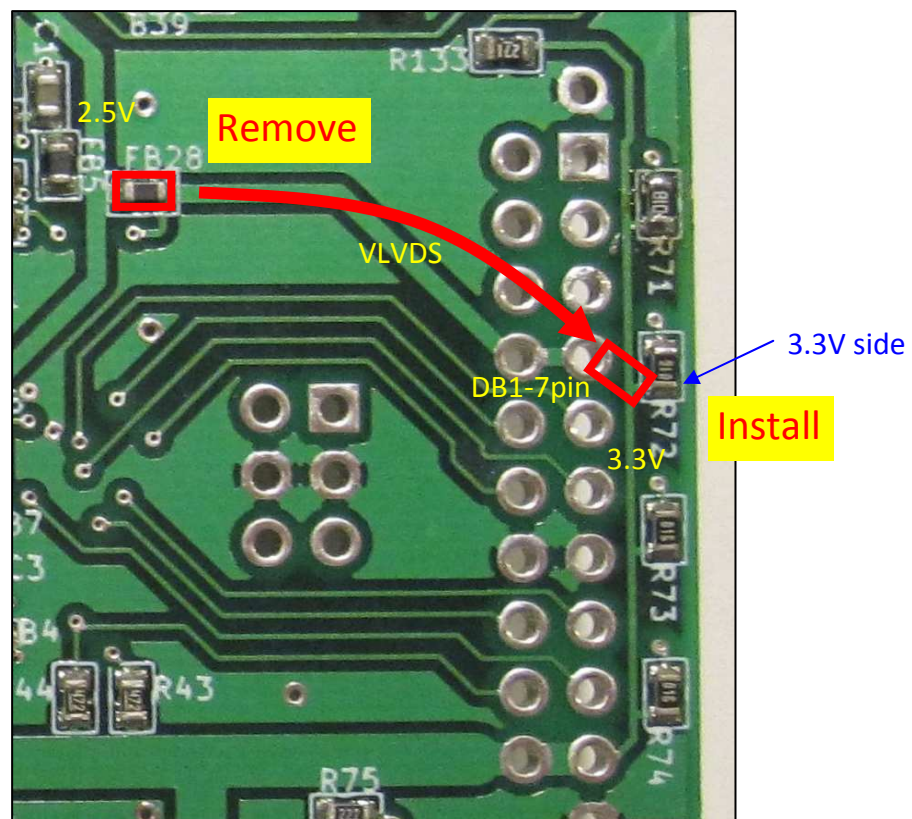
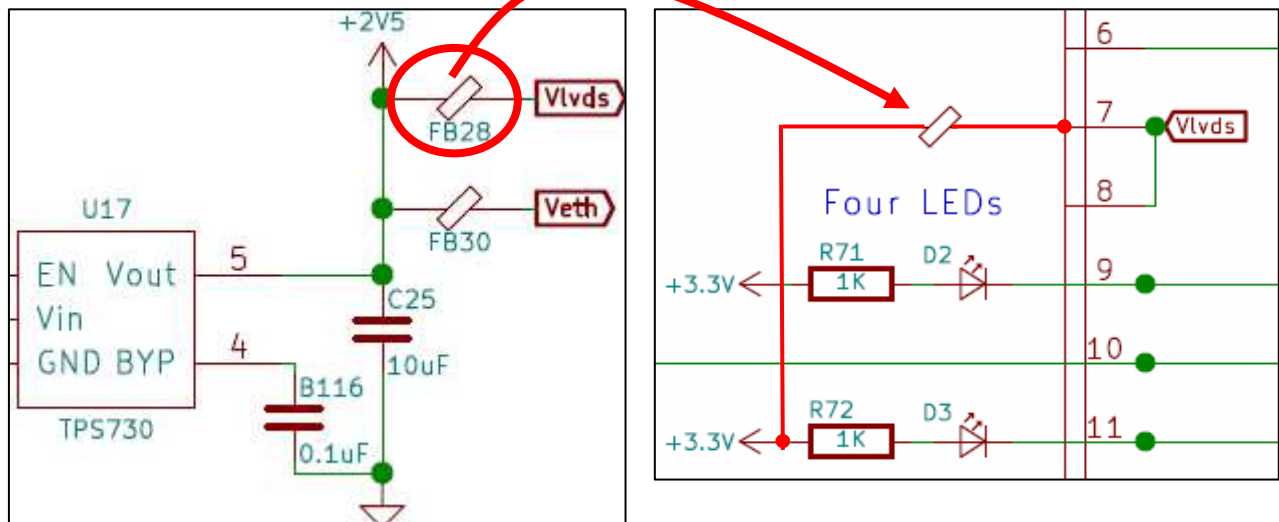
Installation guide "CW Keyer I/F and AK4951 audio codec board" for HL2b5 or later

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1. Change FPGA IO Bank5 voltage to match AKM4951 IO voltage (3.3V).
Supply 3.3V to V_{LVDS} instead of 2.5V.

1-1. Remove FB28 from HL2.

1-2. Install the removed Ferrite Bead between DB1-7pin and R72 (3.3V side).



2. Connect "CW Keyer I/F and AK4951 audio codec board" to HL2.

HL2b5 or later		codec board	
signal name	connector Pin #		connector Pin #
DIN	DB1-2	→	CN5-7
DOT	DB1-3	←	CN4-2
PTT	DB1-4	←	CN4-3
DASH	DB1-5	←	CN4-6
PDN	DB1-6	→	CN4-7
GND	DB1-13		CN4-1
	DB1-14		CN4-8
SCL1	DB1-16	→	DB1-3
SDA1	DB1-18	→	DB1-2
+3.3V	DB1-19		CN4-4
	DB1-20		CN4-5
BCK	DB12-1	→	CN5-2
LRCK	DB12-2	→	CN5-3
DOUT	DB12-6	←	CN5-6

