

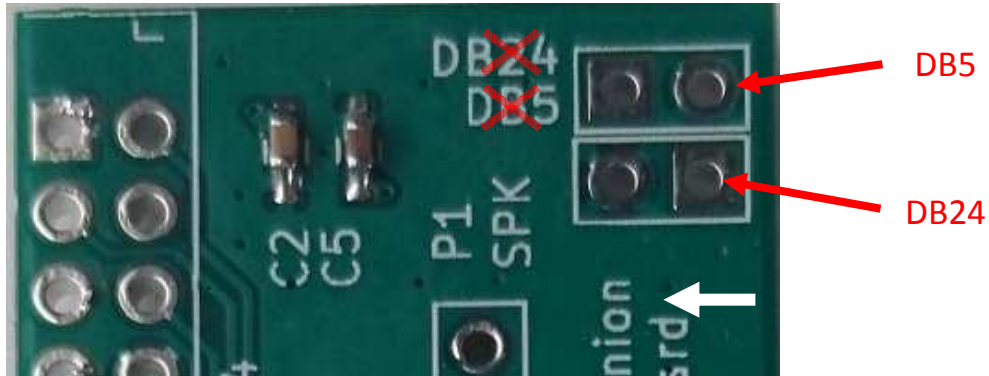
## Keyer I/F & AK4951 v2.0.1 PCB Errata

17th Aug, 2017  
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There is two known errata.

### 1) Wrong Silk

The silks of DB5 and DB24 are swapped



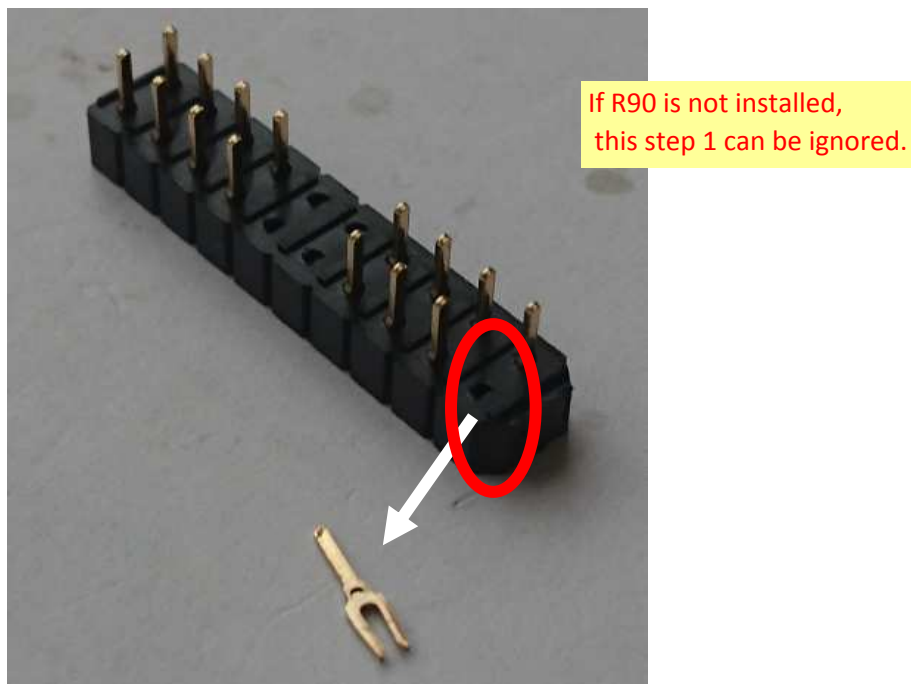
### 2) Wrong FPGA signal assignment

FPGA Pin89 (CN5-7) cannot be assigned as Output port.

#### Counter measure

FPGA Pin76 (DB24-1) is connected to AK4951 Pin11(SDTI) instead.

Step1. Pull CN5-7 contact pin out.



Step2. connect DB24-1 with CN5-7

