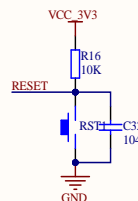
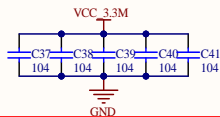
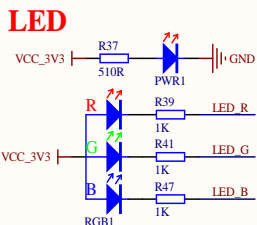


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KEY

The diagram shows a 3x3 keypad matrix circuit. The columns are labeled R38, WK UP, and WK UP1, with resistors of 10K. The rows are labeled KEY2, KEY1, and KEY0, with resistors of 10K. The circuit is connected to GND and VCC_3V3. A 3x3 grid of switches is shown, with each switch connected to a row and a column line. The bottom-right switch is highlighted with a blue circle.

TEMP&HUMI SENSOR

INFRARED EMISSION

The diagram shows an infrared emission circuit. A 3V3 supply (VCC_3V3) is connected to a 336 ohm resistor (R36). The other end of R36 is connected to a 104 capacitor (C61) to ground and a 51 ohm resistor (S1R). The other end of S1R is connected to an IR1 LED. The LED is connected to the base of an 8050 NPN transistor (Q3). The base is also connected to a 1K resistor (R44) to ground. The emitter of Q3 is connected to ground. The collector of Q3 is connected to a 46 ohm resistor (R46) and a 10K resistor (R46) to ground. The output of the collector is labeled EMISSION.

ALS&PS SENSOR

SPI_FLASH

INFRARED RECEPTION

The diagram illustrates the wiring for an infrared receiver module (IR2) to a 3V3 power supply. The module's VCC pin (pin 3) is connected to the 3V3 supply through a 4.7K pull-down resistor (R52). The GND pin (pin 2) is connected to ground. The DATA pin (pin 1) is connected to the 3V3 supply through a 104 capacitor (C66). The module is labeled 'RECEPTION'.

ATK MODULE

VCC 5V

GND

C72

104

6

5

4

3

2

1

GBC TX

GBC RX

GBC KEY

GBC LED

VCC

GND

TXD

RXD

KEY

LED

ATK MODULE

MOTOR

The diagram shows a motor control circuit. A yellow box labeled 'MOTOR' has three pins: 1 (V+), 2 (V-), and 3 (GND). Pin 1 is connected to the VCC_3V3 supply. Pin 2 is connected to the OA pin of a yellow box labeled 'U12'. Pin 3 is connected to GND. The OA pin of U12 is also connected to the VCC pin of a yellow box labeled 'TC214B'. The VCC pin of TC214B is connected to the VCC_3V3 supply. The GND pin of TC214B is connected to GND. A 10R resistor (R60) is connected between VCC_3V3 and the C74 pin of TC214B. The C74 pin is connected to the C75 pin, which is connected to the OB pin of TC214B. The OB pin is connected to the GND pin of TC214B.

AXIS SENSOR

The circuit diagram shows an ICM-20608 sensor module connected to a microcontroller. The microcontroller pins are labeled C68, SCL1, CS, VCC_3V3, R63, U1, INT, and GND. The sensor chip has pins VDDIO, SCL/SCLK, SDI/SDO, CS, AD0/SDO, PSYNC, INT, VDD, RESV, REGOUT, GNSD, RESV, RESV, RESV, RESV, and RESV. The sensor is powered by a 3V3 supply through a capacitor C69 (10nF) and connected to ground through capacitors C71 (10nF) and C73 (474). The serial communication is via I2C using SCL1 and SDI/SDO pins.

POWER

USB DAP & USB OTG

USB DAP

VBUS(A4+B9)
VBUS(B4+A9)

CC1(A5)
CC2(B5)

SBU1(A8)
SBU2(B8)

D-1(A6)
D-2(B6)
D-1(A7)
D-2(B7)

GND(A1+B12)
GND(B1+A12)

SH1
SH2
SH3
SH4

USB TYPE-C

USB_DAP

2 11

4 10

9 3

6 8

7

5

1 12

SH1
SH2
SH3
SH4

VCC_USB

R30 10R

R32 1.5K

R35 10R

DAP_USB_D+

DAP_USB_D-

S8050

VCC_DAP

Q2

R28 10K

R29 100R

R33 96K

VCC 5V

GND

USB_RENUM6

USB OTG

VBUS(A4+B9)
VBUS(B4+A9)

CC1(A5)
CC2(B5)

SBU1(A8)
SBU2(B8)

D-1(A6)
D-2(B6)
D-1(A7)
D-2(B7)

GND(A1+B12)
GND(B1+A12)

SH1
SH2
SH3
SH4

USB TYPE-C

USB_OTG

2 11

4 10

9 3

6 8

7

5

1 12

SH1
SH2
SH3
SH4

VCC_USB

R48 10R

R49 10R

USB_D+

USB_D-

GND

TF_CARD

SD CS R54 5K

SPI1_MOSI R55 5K

SPI1_SCK R56 5K

SPI1_MISO R57 5K

VCC_3V3

1 SD CS

2 CS/DAT0

3 SPI1_MOSI

4 CMD

5 SPI1_SCK

6 VDD

7 SPI1_MISO

8 DAT0/DO

DAT1/IRQ

VCC_3V3


C70

104

Title

5V & 3.3V

The image shows two circuit diagrams for voltage dividers. The left diagram is for a 5V output, labeled 'VOUT1'. It consists of a 10k resistor in series with a 1k resistor, connected to a 5V supply (VCC_5V) and ground (GND). The output voltage is taken from the node between the two resistors. The right diagram is for a 3.3V output, labeled 'VOUT2'. It consists of a 10k resistor in series with a 1k resistor, connected to a 3.3V supply (VCC_3V3) and ground (GND). The output voltage is taken from the node between the two resistors.

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