Manuscript number**: CJECE-OA-2025-Jul-160.R1**

Title**:** **Design and Implementation of a Low-Power Memristor-Based Piccolo-80 Lightweight Encryption Algorithm Using VTM Logic Gates**

**The authors have thoroughly addressed all comments raised in the initial review. The revised manuscript demonstrates significant improvements in clarity, technical accuracy, and presentation quality. All previously identified issues have been satisfactorily resolved.**