

# Jia Jun Lee

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## Education

### Bachelor Degree of Electrical & Electronic Engineering (E&E)

2016 – 2020

Universiti Sains Malaysia (USM), Nibong Tebal, Penang, Malaysia

First Class Bachelor Degree Honour with CGPA Grade of 3.96 / 4.00

## Skills

**Programming/Scripting:** Python

**VCS:** Git/Bitbucket

**Robotic Process Automation:** UiPath

**Project Management:** JIRA/Confluence

**Database/SQL:** Snowflake, MSSQL

**Containerization/CI/CD:** Docker/Jenkins

**Data Analytics:** Tableau/Power BI

**Credential Management:** Vault

**Web Development:** React/NextJS/TypeScript/Tailwind CSS

**Control Systems:** MES/SPC/RMS

## Experiences

### Senior New Product Introduction (NPI) Engineer @ Micron Technology, Penang, Malaysia

Sep'2020 – Present

- Prototyped, developed, qualified and ramped both Client/Enterprise SSD Products from NPI to High-Volume-Manufacturing (HVM), with 100% NPI qualification success rate.
- Enabled Micron Penang with full-fledged capabilities by ramping the first Low-Temp Solder SSD, first E3.S/U.2 Rigid-Flex Complex Form Factor SSDs, and first Gen 5 SSD with high capacity/performance.
- Improved Micron's Capacity Flexibility by extending SSD qualification into Micron Batu Kawan, Penang as dual site qualification in parallel with Micron Prai, Penang.
- Developed multiple improvement strategies to smoothen NPI executions to achieve on-time sample delivery, cost reduction, minimize quality deviations and improve overall efficiency.

## Projects and Achievements

### Automated MAM BOM Creator

Micron Technology

- Effectively terminated the traditional manual, time-consuming, and error-prone aspects of BOM creation.
- Vertical efficiency improvement with high-speed robot executions, enabling ramp with \$23703 savings per annum.

### Automated Readiness Checker

Micron Technology

- Early-shift-left detection project, designed to streamline and provide overview on MAM/RMS Readiness, enabling engineers to resolve issues before it impacts production with \$26880 savings per annum.

### Script-Automated SMT Pick & Place Recipe Creation

Micron Technology

- Leveraged algorithm of script-automated XML modification from master recipe, to generate non-proto / SWR recipes with improved recipe preparation time and license cost saved.

### Developed Automated Recipe vs BOM Checker

Micron Technology

- Minimized quality events with scrap cost reduction by introducing Automated Recipe vs BOM Checking Algorithm, to ensure components mounted are correct on its corresponding designators.

### Automated Reject Guardian

Micron Technology

- Smoothen Engineer Investigation Workflow by script automating JIRA issue creation for every drive rejected with details populated, connected to Machine Data for 1st-level story-telling of the issue.