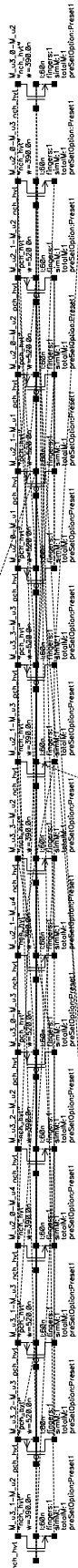


VDD

VSS



A2
A1

Z