PAR Laboratory Assignment Lab 5: Geometric (data) decomposition using implicit tasks: heat diffusion equation

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Spring 2021-22



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Note:

• All files necessary to do this laboratory assignment are available in a compressed tar file available from the following location: /scratch/nas/1/par0/sessions/lab5.tar.gz. Copy it to your home directory in boada.ac.upc.edu and uncompress it with this command line: "tar -zxvf lab5.tar.gz".

Before starting this laboratory assignment ...

Before going to the labroom to start this laboratory assignment, we strongly recommend that you take a look at this section and try to solve the simple questions we propose to you. This will help to better face your last programming assignment in OpenMP: data decomposition for solving the heat diffusion equation.

1.1 Some data decomposition strategies

The tasking model in OpenMP making use of explicit tasks that we have used in the two previous laboratory assignments is very versatile, allowing to express a wide range of dynamic task decompositions, both iterative (Lab 3) and recursive (Lab 4); however the programmer has no control on data locality. In this laboratory assignment you will explore the last decomposition strategy we study in this course: data decomposition making use of **implicit tasks**. With this strategy the computation that each implicit task has to perform is determined by the data it has to access, either read or write. This may have clear benefits in terms of data locality exploitation because each thread will always execute implicit tasks that access to the same data, whenever possible. Data decompositions can be Geometric or Recursive. In this lab we will focus our attention on the Geometric ones that are applied to n-dimensional matrices (including vectors).

Assume the code shown in Figure 1.1 for which we want to write a parallel code ensuring that each thread will compute the elements of matrix C that are stored in its own memory.

Figure 1.1: Simple example performing matrix sum.

Assuming that P is the number of processors and that $thread_k$ is executed on processor P_k , the left part of Figure 1.2 shows a possible distribution for the matrices by rows, so that the memory associated to each processor has a block of consecutive $N \div P$ rows of matrices A, B and C. On the right we show a parallel implementation using implicit tasks for the loop that follows the so called *Owner-computes Rule*: each processor is responsible for the computations on the elements that are allocated in its main memory. Observe that, based on the value returned by intrinsic omp_get_thread_num(), each thread can determine the subset of iterations of the i loop to execute (range between i_start and i_end); since

columns are not distributed, each thread executes all iterations of the j loop. For simplicity, the code in Figure 1.2 assumes that the number of elements N is a multiple of the number of threads executing the parallel region; think how it would change if this condition does not hold (think about it, trying to maximise load balancing).

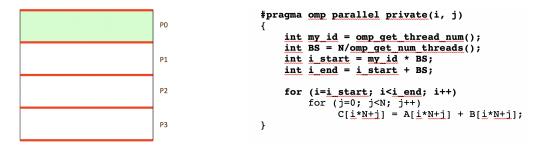


Figure 1.2: Left: geometric block data decomposition by rows. Right: parallelisation using implicit tasks.

Similarly, Figures 1.3 and 1.4 show two different data decomposition strategies and the code associated, always using implicit tasks, that follows the owner computes rule. In Figure 1.3 each processor also has $N \div P$ rows but now distributed in a cyclic way, starting with row 0 assigned to thread 0. Observe that the loop \mathbf{i} now traverses the iteration space starting from the identifier of the thread executing the implicit task, jumping as many iterations as threads in the parallel region until reaching N.

Figure 1.3: Left: geometric cyclic data decomposition by rows. Right: parallelisation using implicit tasks.

In Figure 1.4 each processor has blocks of BS = 2 consecutive columns assigned in a cyclic way, starting with the first block assigned to thread 0. Observe that now the loop that is decomposed is the j loop since it is the one used to access the columns. Do you understand how the loop is transformed in order to follow the owner-computes rule?

Figure 1.4: Left: geometric block-cyclic data decomposition by columns. Right: parallelisation using implicit tasks, assuming $N\%(BS \times howmany) == 0$.

Considering that two–dimensional matrices are stored in memory by rows, which should be the value (or values) for BS to avoid false sharing when writing elements C[i*N+j]?

Sequential heat diffusion program and analysis with Tareador

In this laboratory assignment you will work on the parallelisation of a sequential code that simulates the diffusion of heat in a solid body using two different solvers for the heat equation (Jacobi and Gauss-Seidel). Each solver has different numerical properties which are not relevant for the purposes of this laboratory assignment; we use them because they show different parallel behaviours. In any case, you should be familiar with the two solvers since we have been using them quite extensively in the course.

Take a look at the the source code of heat.c (where the solver is invoked) and solver.c (where the solvers are coded). You will soon realise that both solvers use the same function solve. The difference is that Jacobi uses a temporary matrix to store the new computed matrix (param.uhelp) while Gauss-Seidel directly updates the same matrix (param.u). Notice that function solve is iteratively invoked inside a while loop that iterates while two different conditions are met: 1) the maximum number of iterations param.maxiter is not reached; and 2) the value returned by the solver is larger than param.residual. And also that at each iteration of the while loop Jacobi needs to copy the newly computed matrix into the original one in order to repeat the process, by simply invoking funtion copy_mat also defined inside solver.c.

The picture in Figure 2.1 shows the resulting heat diffusion when two heat sources are placed in the borders of the 2D solid (one in the upper left corner and the other in the middle of the lower border). The program is executed with a configuration file (test.dat) that specifies the number of heat sources, their position, size and temperature. The program also accepts several execution arguments: -n: maximum number of simulation steps or iterations; -s: the size of the body (resolution); -r: the residual value that stops the algorithm; -a: the solver to be used; and -o the output file (an image similar to the one shown in Figure 2.1 in portable pixmap file format, showing a gradient from red (hot) to dark blue (cold)). The execution of the program reports the execution time and performance measurements.

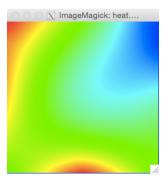


Figure 2.1: Image representing the temperature in each point of the 2D solid body

- 1. Compile the sequential version of the program using "make heat" and execute the binary generated using the Jacobi solver: "./heat test.dat -a 0 -o heat-jacobi.ppm". The execution reports the execution time (in seconds), the number of floating point operations (Flop) performed, the average number of floating point operations performed per second (Flop/s), the residual and the number of simulation steps performed to reach that residual. Visualise the image file generated with an image viewer (e.g. "display heat-jacobi.ppm"); keep this file in your directory to check later the correctness of the parallel versions you will program.
- 2. Change the solver from Jacobi to Gauss-Seidel by simply re-executing with "./heat test.dat -a 1 -o heat-gauss.ppm". Observe the differences with the previous execution. Note: the images generated when using the two solvers are slightly different (you can check this by applying diff to the two image files generated). Again, keep the .ppm file generated to check later the correctness of the parallel versions you will program.

Once you understand the code, you will use *Tareador* to analyse the task graphs generated when using the two different solvers. We already provide you with an initial coarse-grain task definition ready to be compiled ("make heat-tareador") that you will refine later.

- 1. Take a look at the instrumentation performed inside heat-tareador.c in order to identify the parallel tasks that are initially proposed. The two solvers and an auxiliary function copy_mat are identified as tasks in Tareador. Compile with the appropriate make target and execute with ./run-tareador.sh; the script has to receive the name of the executable and the solver to be used (0 for Jacobi or 1 for Gauss-Seidel). The script internally specifies a very small test case which performs a few iterations on a very small body. Is there any parallelism that can be exploited at this granularity level?
- 2. We assume that the answer to the previous question was not affirmative. Let's explore a finer granularity for both solvers. Open the solver-tareador.c file and take a closer look at the implementation of function solve. Notice that the function divides the computation of the 2-dimensional matrix unew using u in blocks, each block computing a subset of rows and columns. the lower and upper bounds in each dimension are computed (i_start, i_end, j_start and j_end) based on the size of the matrices that are used. Important: This is the granularity level we want you to explore for the tasks: one task per block. Make sure you understand the macros that are defined in the same file and how they are used to implement the blocking transformation.
- 3. Change the original *Tareador* instrumentation to reflect the new proposed task granularity. Compile again, execute and analyze the task graphs that are generated when using both *Jacobi* and *Gauss-Seidel*.
 - (a) Which variable is causing the serialisation of all the tasks? Use the *Dataview* option in *Tareador* to identify it.
 - (b) In order to emulate the effect of protecting the dependences caused by this variable, you can use the tareador_disable_object and tareador_enable_object calls, already introduced in the code as comments. With these calls you are telling to *Tareador* to filter the dependences caused by the variable indicated as object. Uncomment them, recompile and execute. Are you obtaining more parallelism? How will you protect the access to this variable in your OpenMP implementation? Simulate the execution when using 4 processors and extract your conclusions. Is there any other part of the code that can also be parallelised?. If so, modify again the instrumentation to parallelise it.

Important: Please, refer to section 3.1 to make sure you have everything you need to include in the deliverable for this part of the laboratory assignment (task dependence graphs for both solvers and other information provided by *Tareador*, *Paraver* windows to support your explanations, ...).

Parallelisation of the heat equation solvers

3.1 Jacobi solver

In this section you will first parallelise the sequential code for the heat equation code considering the use of the *Jacobi* solver, using the **implicit tasks** generated in **#pragma omp parallel**¹, following a geometric block data decomposition by rows, as shown in Figure 3.1 for 4 threads running on 4 processors.

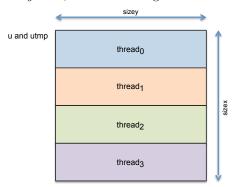


Figure 3.1: Geometric (data) decomposition for matrix u (and utmp) by rows, for 4 threads.

With this strategy the computation that the implicit task executed each thread has to perform is determined by the data it has to access, either read or write. This may have clear benefits in terms of data locality exploitation because each thread will always execute implicit tasks that access to the same data, whenever possible. How could we parallelise the sequential code for function solve to ensure that each processor computes its assigned elements?

- 1. To start with, take a look at the initial parallelisation proposed in solver-omp.c for function solve. Try to understand how the proposed parallelisation follows the data decomposition strategy mentioned above. Complete the parallel code so that it honors the dependences that you have discovered when applying the Jacobi solver. Compile using make heat-omp and submit its execution to the queue using the submit-omp.sh script, specifying the binary file, the use of the Jacobi solver and 8 threads (sbatch submit-omp.sh heat-omp 0 8). Validate the parallelisation by visually inspecting the image generated and making a diff with the file generated with the original sequential version (if not, you should not continue with the next steps).
- 2. Once validated, submit the execution of the submit-strong-omp.sh script to obtain the scalability plot for different number of processors (1 to 12), specifying with an argument the solver to be used

¹Important: You can not make use of other pragmas to create explicit tasks or distribute the work among the implicit tasks. You can use OpenMP intrinsic functions.

- (0). In order to do a better analysis of scalability, this script is already modified to run with a larger problem size (-s 1022) but less simulation steps (-n 1000). Is the scalability that is obtained with this initial parallelisation appropriate?
- 3. In order to understand what is happening, instrument the execution of the binary with Extrae by submitting the submit-extrae.sh script with the same arguments as submit-omp.sh. If you look at the script you will see that the instrumented execution just performs 100 iterations on a problem with the original resolution (hot surface size) as the one used before. Reason about the behaviour of the parallel execution.
- 4. Is there any serious serialisation in your parallel execution? Parallelise other parts of the code in order to improve the efficiency of your parallel code. Compile the new version and submit its execution to the queue using the submit-omp.sh script, specifying the binary file, the use of the Jacobi solver and 8 threads. Is the execution time reduced? Make sure the new parallel version still generates correct results.
- 5. Once validated, instrument its parallel execution with *Extrae* in order to see the new parallel behaviour. Has the execution time for the invocations to function solve changed? Why the new code that you have parallelised makes the difference in the performance results?
- 6. Finally, use the submit-strong-omp.sh script to queue the execution of heat-omp and analyse the scalability of the parallelisation for different number of processors (1 to 12); do not forget to specify with an argument the solver to be used (0). Reason about the scalability that is obtained.

Important: Please, refer to section 3.2 to make sure you have everything you need to include in the deliverable for this part of the laboratory assignment (scalability plots when using the *Jacobi* solver, *Paraver* windows to support your explanations, ...).

3.2 Gauss-Seidel solver

Once the parallelization of the solver for Jacobi is appropriate, you should continue the parallelization process considering the dependences that appear when the Gauss-Seidel is used (that you discovered using Tareador in the previous chapter). The parallelisation should follow the same $geometric\ block\ by\ rows\ data\ decomposition$ that is shown in Figure 3.1, and again ONLY making use of the implicit tasks in parallel regions. Note: before continuing, we suggest you take a look at the explanation in $Annex\ 1$ to make sure you understand how to express ordering constraints among (implicit) tasks using shared variables and the $memory\ consistency$ problem that may occur.

- 1. Parallelise the Gauss-Seidel solver, introducing the necessary synchronisation among implicit tasks and data sharing constraints. In case you need to know inside function solve which solver has to be applied (i.e. Jacobi or Gauss-Seidel), you just need to check if u==unew, which will return true for Gauss-Seidel. The number of blocks in the i dimension should be determined by the geometric data decomposition (i.e. the number of threads or implicit tasks in the parallel region); for your first implementation we suggest to use the same number of blocks in the j dimension (i.e. nblocksj = nblocksj). Later you will explore different numbers of blocks in the j dimension.
- 2. Compile using make heat-omp and submit the execution of the binary using the submit-omp.sh script to validate the parallelisation (by visually inspecting the image generated and making a diff with the file generated with the original sequential version). Don't forget to specify the Gauss-Seidel solver when submitting the script (1) as well as the number of processors to use.
- 3. Instrument with Extrae by submitting the submit-extrae.sh script and visualize the traces generated for the parallel execution. Does the parallel behaviour match your expectations? Once the parallelisation is correct, use the submit-strong-omp.sh script to queue the execution and analyse the scalability of the parallelisation; do not forget to specify with an argument the solver to be used (1).

- 4. In order to exploit more parallelism in the execution of the solver, you should change the number of blocks in the j dimension. Why? Changing the number of blocks in the j dimension changes the ratio between computation and synchronisation, why? In order to do this change in your code, main is already prepared to receive an argument in the command-line execution (-u value) that is stored in global variable userparam. Modify your code accordingly to make use of this value and change the number of blocks in the j dimension without recompiling the code for each new value to test. You could either use the value in userparam as a multiplicative factor (i.e. nblocksj=userparam * nblocksi) or directly as the number of blocks (i.e. nblocksj=userparam). Compile and execute with different values and check that it has the expected effect.
- 5. Use the provided submit-userparam-omp.sh script to explore a range of values for this argument (please set the range of values as appropriate depending on the use of userparam). The only argument for this script is the number of threads to be used for the exploration. For the execution with 4 and 8 threads plot how the execution time varies and explain the plot that is obtained. You can also execute with 2 and 12 threads to verify your conclusions.

Important: Please, refer to section 3.3 to make sure you have everything you need to include in the deliverable for this part of the laboratory assignment (execution time and scalability plots when using the *Gauss-Seidel* solver, *Paraver* windows to support your explanations, ...).

3.3 Optional exercises

Several optional exercises are proposed with the objective of comparing the task and data decompositions in terms of the effects data locality has on performance.

Optional 1: Implement the *Jacobi* solver using explicit tasks and following an *iterative task decomposition*. Compare the performance results that are obtained with the ones obtained with the *data decomposition strategy*. Instrument the executions with *Extrae* and take a look at the execution time of solve instances.

Optional 2: Implement the *Gauss-Seidel* solver using explicit tasks and task dependences, following an *iterative task decomposition*. Compare the performance results that are obtained with the ones obtained with the *data decomposition strategy*.

4

Deliverable

You have to write a short document that presents the main results and conclusions that you have obtained when doing this assignment. Only PDF format for this document will be accepted.

- The document should have an appropriate structure, including, at least, the following sections: Introduction, Task decomposition strategies, Implementation in OpenMP and performance analysis, and Conclusions. The document should also include a front cover (assignment title, course, semester, students names, the identifier of the group, date, ... and a table of contents).
- Important: You DON'T have to include in the document all the steps you have followed during the laboratory sessions, ONLY the main results and conclusions derived from them. In the following subsections we highlight the aspects that you should included in the document. Your explanations should be based on the relevant execution timelines and other *Paraver* windows as well as scalability plots generated by the execution scripts.
- You also have to deliver the complete C source codes for Tareador instrumentation and all the OpenMP parallelisation strategies that you have done. Your professor should be able to re-execute the parallel codes based on the files you deliver. DON'T include code in the document unless strictly necessary, and in that case, only the fragment modified with respect to the original one.

Your professor will open the assignment in *Atenea* and set the appropriate dates for the delivery. You will have to deliver TWO files, one with the document in PDF format and one compressed file (tgz, .gz or .zip) with the requested source codes.

As you know, this course contributes to the **transversal competence** "Tercera llengua". Deliver your material in English if you want this competence to be evaluated. Please refer to the "Rubrics for the third language competence evaluation" document to know the Rubric that will be used.

4.1 Analysis of task granularities and dependences

Explain the original task dependence graph that is obtained as well as the new graphs that you obtained for *Jacobi* and *Gauss-Seidel*, reasoning about the causes of the data dependences that appear and how will you protect them in your parallel OpenMP code. Include the timelines with the simulated execution for 4 processors to support your explanations about potential scalability.

4.2 OpenMP parallelization and execution analysis: Jacobi

Describe how did you address any detected performance bottleneck (serialisation, load balancing, ...) that you observed when parallelizing the code using the Jacobi solver. You should include captures of Paraver windows to justify your explanations and the differences observed in the execution. Finally you should analyse the speed-up (strong scalability) plots that have been obtained for the different numbers of processors, reasoning about the performance that is obtained.

4.3 OpenMP parallelization and execution analysis: Gauss-Seidel

Describe how did you enforce the dependences that appear when using the *Gauss-Seidel* solver, focusing on the mechanism used to guarantee the proper synchronization between implicit tasks. Analyse the speed–up (strong scalability) plot that has been obtained for the different numbers of processors, reasoning about the performance that is obtained and including captures of Paraver windows to justify your explanations. Finally include your conclusions about the optimum value for the ratio computation/synchronization in the parallelization of this solver for 4 and 8 threads.

4.4 Optionals

If you have done the optional part in this laboratory assignment, please include and comment in your report what have you done, the relevant portions of the code, performance plots, or *Paraver* windows that have been obtained.

Annex 1: Creating your own synchronisation objects

The implicit tasks used to express parallelisation strategies based on data decomposition can not synchronise themselves using task dependences (i.e. depend clauses can not be applied to implicit tasks). For this reason in this laboratory assignment you will also implement your own synchronisation objects to implement some sort of task ordering constraints. These synchronisation objects will be implemented using shared variables for which one has to ensure that all accesses (reads and writes) to them always access to memory. This is what is called the *memory consistency problem*: usually the compiler tries to optimise memory accesses by placing variables in registers, and then only read/write from/to memory at certain points in the parallel program, usually at synchronisation points.

For example consider the simple producer-consumer code shown in Figure 5.1, using implicit tasks. Notice that all implicit tasks can do their *computation* A part in parallel; however, the instance of the implicit task executed by myid cannot execute *computation* B until the previous thread has executed it (in other words, the execution of *computation* B has to be done in an ordered way). Only the instance of the implicit task executed by thread 0 can do the execution of *computation* B initially. This is controlled by vector next with as many positions as threads (i.e. instances of the implicit task): if position myid is 0, then the implicit task will wait in the while loop; once it is set to 1 by the implicit task myid-1, the implicit task will be allowed to continue.

```
int next[P];
...
next[0] = 1;
for (int i = 1; i < P; i++) next[i] = 0;
...
#pragma omp parallel num_threads(P)
{
   int myid = omp_get_thread_num();
   // computation A
   while (next[myid] == 0); // wait to advance
   // computation B
   if (myid < P-1) next[myid+1]++;
}</pre>
```

 $Figure \ 5.1: \ Simple \ producer-consumer \ code.$

Although the code seems to be correct, it has memory consistency problems. To ensure that each task always accesses to the last value of the shared variable next, the programmer has to introduce some sort of data sharing/synchronisation construct. The preferred one is atomic, which can have three different clauses: read, write and update. Clauses read and write are used to express that memory accesses are always served by main memory. The resulting code is shown in Figure 5.2. Inside the do

while construct we are ensuring that the element of vector next is read from memory by adding the atomic read; in order to ensure that the element of vector next is updated in memory we need to add atomic write.

```
int next[P];
next[0] = 1;
for (int i = 1; i < P; i++) next[i] = 0;</pre>
#pragma omp parallel num_threads(P)
    int myid = omp_get_thread_num();
    // computation A
    do {
        #pragma omp atomic read
        tmp = next[myid];
    } while (tmp == 0); // wait to advance
    // computation B
    if (myid < P-1) {
        #pragma omp atomic write
        next[myid+1] = 1;
    }
}
```

Figure 5.2: Simple producer-consumer code making use of atomic pragmas to guarantee memory consistency.

How would you change the code above if we introduce an iterative loop that repeats the execution of computation A and computation B several times, always ensuring the same execution order constraints? Figure 5.3 shows the code to be completed in order to ensure the appropriate execution ordering.

```
int next[P];
...
next[0] = ...;
for (int i = 1; i < P; i++) next[i] = ...;
...
#pragma omp parallel num_threads(P)
{
   int myid = omp_get_thread_num();
   for (int iters = 0; iters < num_iters; iters++) {
        // computation A

        do { ... } while ( ... ); // wait to advance
        // computation B

        if (myid < P-1) next[myid+1] = ...;
   }
}</pre>
```

Figure 5.3: Second version of simple producer-consumer code.

Question: Can the access to vector **next** cause false sharing? If you answered yes, how would you solve the problem?