

**GRAPHENE-COMPLEX OXIDE
HETEROSTRUCTURE**

by

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Bachelor of Science, Hong Kong Baptist University, 2011

Submitted to the Graduate Faculty of
the Department of Physics in partial fulfillment
of the requirements for the degree of
Doctor of Philosophy

University of Pittsburgh

2019

UNIVERSITY OF PITTSBURGH
DEPARTMENT OF PHYSICS AND ASTRONOMY

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TABLE OF CONTENTS

PREFACE	xvii■
I. INTRODUCTION	1
A. Motivation	1
B. Introduction to LAO and STO	1
1. 2DEG on LAO/STO interface	3
2. Metal-insulator transition and critical thickness	5
3. Superconductivity	5
4. Magnetism	5
5. Electron waveguide on LAO/STO interface	5
C. Graphene	5
1. Dirac fermion	7
2. Quantum Hall effect	7
II. EXPERIMENTAL METHODS	8
A. Sample growth	8
1. LAO/STO growth	8
a. Pre-growth treatment	8
b. PLD deposition	9
2. Graphene growth	9
a. Copper substrate preparation	13
b. CVD growth	18
B. LAO/STO sample processing	20
1. Photolithography	21

a.	Spin-coating	21
b.	UV exposure	22
c.	Developing	26
2.	Ion milling	27
3.	Deposition	30
4.	Lift-off	30
5.	Oxygen plasma cleaning	32
C.	Atomic force microscope	32
1.	Working principle	33
2.	Contact mode	35
3.	AC mode	36
4.	Non-contact mode	37
5.	Magnetic force microscopy	38
6.	Piezoresponse force microscopy	39
7.	LAO/STO nano-device c-AFM lithography	41
D.	Electric signal measurement	44
1.	Lock-in amplifier	46
2.	Virtual lock-in	46
3.	IV-curve measurement	46
4.	Probe station	46
E.	Graphene/LAO/STO device fabrication	46
1.	Overview of the Hyflon transfer method	49
2.	Hyflon solution preparation and spin-coating	51
3.	Graphene preparation, copper etching and wet transfer	51
4.	Graphene/LAO/STO sample pattern	54
5.	Photolithography and etching	55
	a. One-step etching	55
	b. Two-step etching	58
6.	Hyflon removal	59
	a. High temperature method	63

b. Low temperature method	63
7. AFM cleaning of graphene	63
8. Graphene/LAO/STO nano-scale device lithography	66
9. c-AFM lithography and PFM imaging	66
10. More about the c-AFM writing conditions on graphene and damage thresholds	68
III. GRAPHENE P-N JUNCTION EDGE-STATE ENGINEERING	71
A. Transport measurement in graphene	71
1. Charge-neutrality point	71
2. Temperature dependence	71
3. Carrier densities and mobilities	71
4. Quantum Hall effect in graphene	71
B. p-n junction on graphene/LAO/STO	71
1. Graphene edge channels and p-n junctions	71
2. Fabricating p-n junction with c-AFM lithography	71
3. Measurement and results	71
IV. GRAPHENE/LAO/STO SUPERLATTICE DEVICE	72
A. Graphene band structure engineering with superlattice	72
B. c-AFM lithography of graphene/LAO/STO superlattice	72
1. Measurement and results	72
V. MAGNETO-OPTICAL KERR EFFECT ON LAO/STO INTERFACE	73
A. Magnetism on LAO/STO interface	73
B. MFM measurement	73
C. Optical setup	73
D. Results	73
VI. OUTLOOK	74
VII. CONCLUSIONS	75
BIBLIOGRAPHY	76

LIST OF TABLES

1	Spin-coating conditions for AZ4210 and AZ4110	22
2	UV exposure and developing conditions for AZ4210 and AZ4110	27
3	Comparison of etching methods.	28
4	One-step RIE etching recipe.	59
5	Two-step etching photolithography recipe. First layer of AZ4210 is used to protect the canvas against the aggressive RIE for contamination removal. Second layer of AZ4110 is for graphene Hall bar protection, and only need to resist mild oxygen plasma etching.	62
6	Two-step etching recipe. Although it seems that the barrel etcher is running at higher power, the structure of the instrument make the etching much less invasive compared to RIE.	62
7	Graphene cleaning recipes on Asylum MFP3D and Cypher.	65
8	c-AFM writing and erasing parameters on graphene/LAO/STO samples.	70

LIST OF FIGURES

- | | | |
|---|--|---|
| 1 | Ferroelastic transition of STO. (a) At $T = 300$ K STO has cubic perovskite structure. (b) At $T = 105$ K, the adjacent oxygen octahedrons would rotate in opposite directions and cause ferroelastic transition in STO. Domains will be formed for different orientations of unit cells. Adapted from [?]. | 2 |
| 2 | The band structure of bulk STO. (a) The conductive bands derive from the $3d$ orbitals of Ti. The degeneracy of the orbitals is broken by the octahedral cage of oxygen atoms, and the energy levels are grouped into e_g and t_{2g} . (b) The hopping of d_{xy} , d_{xy} and d_{xy} are much easier along in the plane directions, mediated by the $2p$ orbitals of oxygen atoms. Adapted from [?]. | 3 |
| 3 | Polar catastrophe mechanism of interface conductivity of LAO/STO. (a) AlO_2 and LaO are not charge neutral and a non-zero potential is built up when LAO is grown on TiO_2 terminated STO. (c) Electrons are transferred from the top surface to neutralize the potential and therefore a layer of 2DEG is formed. (b) and (d) similar mechanism can explain the formation of hole gas on the interface. Adapted from [?]. | 4 |

4	(a) When LAO is over the critical thickness of 4 unit cells, the interface is conductive, otherwise the interface is insulating. (b) When LAO is only slightly below the critical thickness (e.g. 3 unit cells), the conductivity on the interface is tunable with an external voltage applied to the backside. The as-grown sample is insulating. When +100 V is applied, the interface becomes conductive. When the voltage is removed, the conductivity still persists. When -100 V is applied, the interface becomes insulating again. The conductivity can be cycled with alternating voltages. Adapted from [?].	6
5	PLD epitaxial growth. The PLD chamber is back-filled with oxygen to the target pressure. A beam of pulsed deep-UV excimer laser is focused onto the LAO target. LAO is ablated off and a plume of plasma extends towards the heated substrate on top, and condensed into atomic layer films. RHEED signal is used to monitor the thickness of LAO.	10
6	AFM image of the sample after PLD growth and RHEED signal for PLD. (a) shows is the AFM Image. The stripes are the crystal terraces, with $h \approx 4\text{\AA}$. Inset of a is the raw diffraction signal of RHEED. (b) is the RHEED signal oscillation during the film epitaxial growth. Each cycle indicates the completion of one unit cell. Adapted from [?].	11
7	Graphene CVD growth. At a temperature $T \approx 1000^\circ\text{C}$, methane molecules will react with copper surface and leave carbon atom on the surface. Carbon atom will self-assemble into graphene due to lattice-match between graphene and copper super-cell. Hydrogen is used to etch away the smaller domain so that the final single layer graphene domains is as large as possible. If the hydrogen is too much, the larger domain will be etched as well. Ratio of methane and hydrogen has to be carefully controlled.	13
8	Diamond turning machine at work. (a) Copper is fixed onto a turning spindle with vacuum. (b) Mineral spirit is sprayed to the tool and cutting spot, to cool down the surface and blow away the debris.	14
9	(a) Diamond cutting tool. (b) Magnified image of the tool tip. The radius of curvature $r = 1.5 \text{ mm}$	15

- 10 (a) The copper foil after DTM thinning and polishing. The final thickness is
about 100 μm . (b) The AFM image of the copper foil surface. The roughness
is within 2 nm. Some nanoscale particles can be seen. They will serve as
nucleation centers for graphene CVD growth. Reducing the density of surface
contaminants will reduce the density of nucleation centers and enlarge the
domain size. The vertical grooves are left by the dents on the diamond tool for
each revolution. The spacing between grooves is the same as the approaching
speed of the tool towards the center. The copper is annealed at $T = 1050^\circ\text{C}$
for another time before growth starts and the surface will reconstruct, possibly
remove those grooves. 16
- 11 Procedure of cutting copper rods into 100 μm foils for graphene growth. High
purity OFHC copper rods are purchased from McMaster Carr. The rod is
first annealed at 1050 $^\circ\text{C}$ for 24 h so that the polycrystalline domains merge
into larger domains. Then the rod is cut into 2 mm thick copper discs in the
machine shop. The discs are cleaned with DTM and then annealed again to
“heal” the domains broken by the mechanical cutting. Domain re-formation
would cause surface corrugation, therefore the copper disc surface is cleaned
with DTM and then cut into 100 μm foils and ready for graphene CVD growth. 17
- 12 Dark field mode microscope images of CVD graphene on copper substrates. (a)
The copper substrate is partially covered with graphene. Hexagonal shape sin-
gle domain graphene can be observed. Growth was terminated before graphene
fully covered the substrate. (b) Graphene covers the entire copper substrate.
Copper domains of two different orientations can be observed. 19
- 13 Mask for photolithography. The dark parts are transparent, while the bright
parts are covered with chromium. Sample will be exposed where UV is not
blocked by chromium. The figure shows the electrodes of a canvas for writing.
The pedal shape area to be exposed will be etched away on the sample, and
filled with titanium and gold to make contact with the LAO/STO interface. . 23

14	Suss UV mask aligner. The mercury lamp generates broad band light from arc. The light is reflected by an ellipsoidal mirror and focused on a cold light mirror, where the UV is selected. The exposure time and dose is controlled by a shutter. The UV is collected by a condenser lens and diffraction reduction optics for collimation and resolution enhancement. Finally, the UV is shed on the sample with a folding mirror and front lens. The sample is in close contact with a mask, where chromium is partially removed. UV exposes the photoresist on sample and print the mask pattern on it.	24
15	Heidelberg direct laser writer. The sample is illuminated by red light from a diode, and realtime image of sample surface can be monitored with a CCD. UV with $\lambda = 405$ nm is generated from a laser diode. The laser is expanded and filtered, and focused on the sample surface with a high-NA objective. The sample is fixed on a piezo-driven stage. The CCD image, laser power, sample movement and layer alignment are all controlled with computer, so that the laser path and dose control is fully automatic.	25
16	Exposed and developed sample. The bright regions are free of photoresist while the rest of the sample is still covered.	26
17	Simplified view of ion mill. The argon ions are generated from the discharge chamber on the right hand side. Electrons are extracted from the heated cathode and ionize Ar molecules before they reach the chamber, which works as an anode cup. A magnetic field is applied through a solenoid to increase electrons' path. Ar^+ are accelerated and aligned by the grids to 500 eV. Electrons from the neutralizer are mixed with the Ar^+ plasma flow so that the collimation can be maintained. Sample is tilted by an angle of 22.5°.	29
18	E-beam evaporator. The electron beam is generated from an electron gun on the bottom. The beam is deviated and shaped by magnetic field, towards the material. Them material is loaded in a crucible and heated up to melting point. The vapor of the material is thrown towards the sample on top. Deposition time is controlled by a shutter.	31

19	The schematics of AFM. A laser is reflected from the top surface of the AFM tip, and collected by a quad detector. A small amount of deformation would cause the center of laser spot to move on the quad detector, and measured as a change of differential voltage.	34
20	The interaction between tip and the sample surface. At long distance the interaction zero. When the tip moves towards the sample, the interaction is attractive. As the tip gets closer, the interaction would switch from attraction to repulsion, and gets larger and larger the tip moves closer.	35
21	AFM contact mode. The tip is pressed to the sample surface by the piezoelectric actuator. The deformation of the tip is monitored by the quad detector. The voltage of the piezoelectric actuator is controlled with a feedback loop with the deflection voltage on the quad detector.	36
22	AFM AC mode. Left: the tip is driven at a frequency close to the resonance frequency. When the resonance frequency shifts as the interaction between the tip and the sample changes, the Change of oscillation amplitude is monitored by the quad detector and sent to the computer.	37
23	AFM Contact mode. The tip is kept at a distance from the sample so that the interaction is always attractive. The tip does not have direct contact with the sample during the scanning.	38
24	MFM mode. The first scan is close to the sample surface, so that the topographical information can be obtained. In the second scan the tip is lifted to a constant distance from the sample surface, so that the Van der Waals interaction is negligible and only the long-ranged magnetic interaction is measured.	40
25	PFM measurement. The external electric field is applied on the sample through a conductive tip. The sample deforms and slightly bend the tip, and the deformation signal is monitored with a quad detector (not shown). If the sample contract in the direction of the field, the deformation signal is in phase with the driving voltage (above); if the sample elongate in the direction of the field, the signal would be out of phase with the driving voltage (below). . . .	42

26 AFM lithography on LAO/STO. (a)(b) The AFM is set in contact mode. A positive bias voltage is applied to the tip. A trace of proton is left on the path of AFM tip, and 2DEG is formed underneath the path. Conductivity of the two interface electrodes are monitored, and a conductance jump will be observed once a closed loop is formed. (c) A negative voltage can remove the proton and cut the nanowire previously written.	43
27 LAO/STO sample pattern for c-AFM lithography. The sample size is 5 mm × 5 mm. Canvas are 20 μm squares.	45
28 CVD graphene transfer and patterning with PMMA. (a) Graphene is grown on copper with CVD. (b) PMMA is spin-coated onto graphene surface. (c) Copper is etched chemically, while graphene floats on the liquid surface. (d) Graphene is transferred onto LAO/STO, using PMMA as a supporting layer. (d) PMMA is patterned with deep-UV lithography. (f) Graphene is etched with oxygen plasma and then PMMA is removed with organic solvent.	47
29 AFM phase image of graphene on LAO/STO, transferred with PMMA. The circular region is graphene. Particles can be seen outside graphene, on LAO/STO surface. Other features are the metal electrodes for graphene and interface contact.	48
30 Hyflon transfer and patterning procedure. (a) Graphene is grown with CVD on a copper substrate. (b) Hyflon in spin-coated on graphene. (c) A layer of photoresist is coated on Hyflon as a supporting layer for wet-transfer. (d) Copper substrate is etched with ammonium persulphate; graphene is rinsed in DI-water for several times. (e) Graphene is transferred onto LAO/STO surface. (f) Photoresist for transfer assistance is removed; another layer of photoresist is coated. (g) Standard UV photolithography. (h) Oxygen plasma etching on the excessive graphene. (i) Photoresist removal with acetone. (j) Hyflon removal with FC-40. (k) Residue particle cleaning with AFM.	50

31	(a) The graphene with copper foil is cut into squares. The graphene single domains can be seen. (b) copper foil with CVD graphene is fixed onto a silicon wafer as a spin-coating carrier. The edges of the copper foils are carefully sealed with Kapton tape to prevent Hyflon solution leaking into the space between copper and silicon substrate. (c) After the copper is fully etched by ammonium persulfate, the graphene flake with Hyflon and photoresist floats on the surface of the liquid. (d) Graphene transferred onto pre-patterned LAO/STO. Liquid between the graphene LAO is evaporated in an oven or on a hot plate.	52
32	(a) Without copper substrate backside cleaning, the graphene transferred onto LAO/STO has contaminant trapped. (b) The graphene on LAO/STO is much cleaner if the backside is cleaned.	53
33	Patterns for graphene/LAO/STO. Two sets of electrodes are patterned separately, for interface and graphene contact.	56
34	Contaminants can be introduced by wet transfer. The source can be photoresist, copper etchant, or liquid trapped in the bubble formed from transferring. (a) Contaminants can be seen after graphene transfer. Only the graphene inside the red circles (zoomed-in image in Figure 35) need to be protected with photoresist, and rest of the graphene can be removed along with the contaminants by RIE procedure. (b) Sample surface is clean after RIE procedure. . .	57
35	The graphene area of interest on pre-patterned LAO/STO sample. The patterns are for graphene and LAO/STO interface contact.	58
36	Two-step graphene etching. (a) A square shape region is protected by AZ4210 photoresist. Outside of the square region the sample is etched with an aggressive RIE process to remove the contaminants introduced by wet-transfer. The figure shows the graphene with Hyflon. Photoresist has been washed off. (b) A second step of photolithography is performed and a Hall-bar shape region is protected by AZ4110 photoresist. Outside of the Hall-bar the Hyflon and graphene is etched away with a mild oxygen plasma barrel etching recipe, and the LAO/STO interface is still writable. The figure shows the graphene Hall-bar covered with Hyflon.	60

37	Two-step graphene etching. (a) AZ4210 is spin-coated on Hyflon. (b) The photoresist is patterned into square-shape protection layer for the canvas. (c) An aggressive RIE process is used to fully remove the contaminants outside the canvas. (a) to (c) might be repeated if necessary. (d) AZ4110 is spin-coated. (e) Hall bar shape is patterned, to protect the Hyflon and graphene on the regions of interest. (f) A weak oxygen plasma etching step is used to remove the unwanted Hyflon and graphene. (g) AZ110 is washed off by solvents. (h) Hyflon is removed by FC-40.	61
38	AFM cleaning of graphene. (a) The organ Hall bar region is the graphene, after Hyflon removal in FC-40. Particles on graphene can be observed. (b) Zoomed-in image of the square region in (a). The center has been scanned in AFM contact mode. Particles are pushed to the top and right edges. Terraces of LAO/STO and wrinkles can be observed.	64
39	STM image of graphene on LAO/STO after AFM cleaning.	66
40	(a) c-AFM lithography of graphene/LAO/STO nanowire. (b) PFM measurement setup. (c) PFM image of the nanowire on LAO/STO interface, underneath the graphene. The inset shows the sudden jump of conductance after the two interface electrodes are connected.	67
41	Damages on graphene from c-AFM lithography. On the AFM paths the graphene is oxidized and show contrast on the phase image.	69

PREFACE

I want to thank everyone that made this happened.

I. INTRODUCTION

A. MOTIVATION

B. INTRODUCTION TO LAO AND STO

Both LAO and STO have perovskite structure at room temperature. Lattice constant of LAO is $a = 3.791\text{\AA}$ [?], while lattice constant of STO is $a = 3.905\text{\AA}$. In each cubic unit cell of STO, the Ti atom is enclosed in octahedral cage of oxygen, and the octahedron is in the center of a cubic skeleton of Sr atoms (Figure 1). At $T = 105$ K, STO would go through a phase transition and oxygen octahedron pairs would rotate against each other so that the bonding lengths of Sr-O and Ti-O are optimized[?]. This ferroelastic transition would break the cubic symmetry and form tetragonal domains in STO, from different orientations of the tetragonal unit cells. At even lower temperature, the crystal symmetry may transit to orthorhombic and triclinic, but the effects on the property of STO are less significant than the tetragonal phase transition at $T = 105$ K.

At room temperature the STO is paraelectric, with a large dielectric constant ($\epsilon_r \sim 300$). When the temperature is lower than $T = 105$ K, the paraelectricity still persists. Although the elongation of unit cell in low temperature develops a double-well potential for Ti atoms, with potential minima at the oxygen at opposite sides of the the elongation axis, the Ti atoms do not displace towards the minima due to the quantum tunneling of oxygen atoms[?] between the minima. STO is one of the few materials that exhibit such *quantum paraelectricity*[?]. As a result, the dielectric constant of STO increases rapidly with decreased temperature. The increase is also cut off by quantum tunneling, leaves $\epsilon_r \sim 20,000$ [?]. This is especially important for gating experiment at low temperature, and more details will be

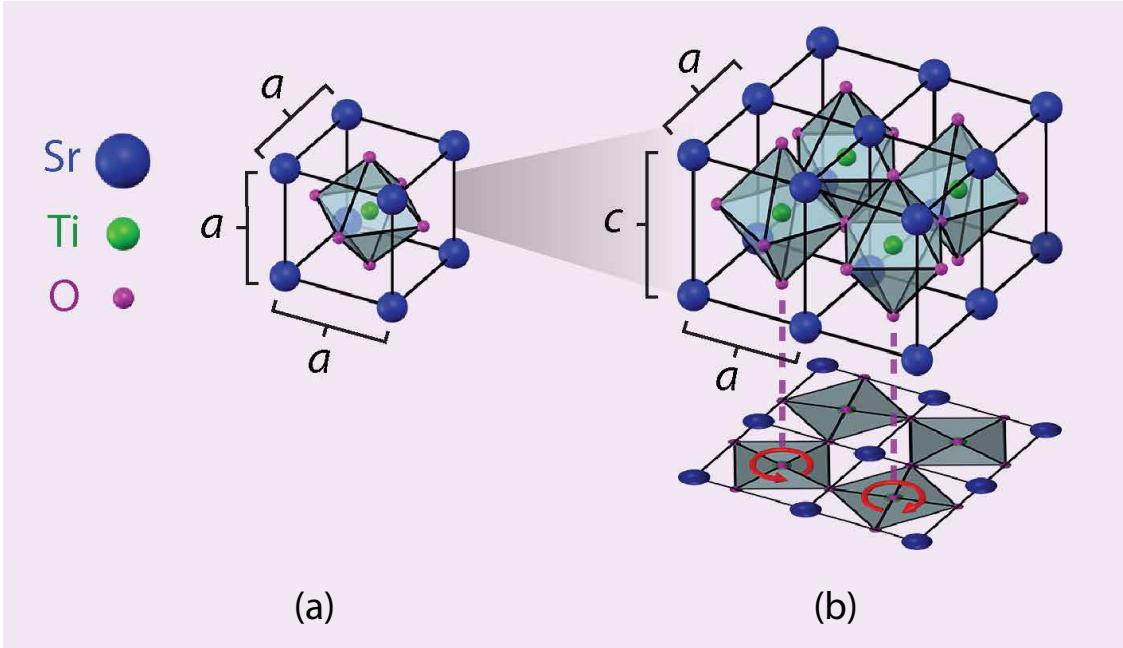


Figure 1: Ferroelastic transition of STO. (a) At $T = 300$ K STO has cubic perovskite structure. (b) At $T = 105$ K, the adjacent oxygen octahedrons would rotate in opposite directions and cause ferroelastic transition in STO. Domains will be formed for different orientations of unit cells. Adapted from [?].

discussed in the following chapters.

The band structure of STO is quite complex. As shown in Figure 2, the conduction band is derived from the $3d$ titanium orbitals and the valence band is from the $2p$ oxygen orbitals. The degeneracy of the five $3d$ orbitals is broken by the crystal structure of STO, and then they are grouped into high energy bands e_g (from $d_{3z^2-r^2}$ and $d_{x^2-y^2}$), and low energy bands t_{2g} (from d_{xy} , d_{yz} and d_{xz}). Ti d_{xy} , d_{yz} and d_{xz} orbitals are coupled to neighboring identical orbitals through the $2p$ orbitals of oxygen atoms in between. Therefore, the hopping matrix elements for in-plane are much greater than than for out-of-plane.

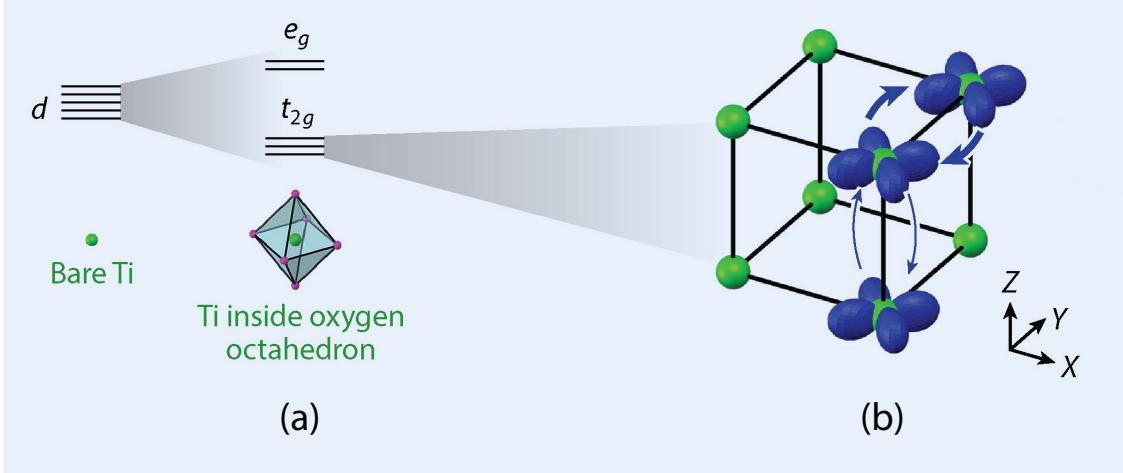


Figure 2: The band structure of bulk STO. (a) The conductive bands derive from the $3d$ orbitals of Ti. The degeneracy of the orbitals is broken by the octahedral cage of oxygen atoms, and the energy levels are grouped into e_g and t_{2g} . (b) The hopping of d_{xy} , d_{xy} and d_{xy} are much easier along in the plane directions, mediated by the $2p$ orbitals of oxygen atoms. Adapted from [?].

1. 2DEG on LAO/STO interface

Although STO is a band insulator, when LAO is grown epitaxially (the small lattice mismatch of 3% makes it possible) on TiO_2 terminated STO, the interface of LAO and STO becomes conductive and a layer of 2DEG is formed, first demonstrated by Ohtomo and Hwang in 2004[?]. The origin of interface 2DEG is still under debate. There are several different theories about the formation of 2DEG on the LAO/STO interface: polar catastrophe[?], oxygen vacancy[?] and interfacial cation intermixing[?].

The most widely accepted explanation is the polar catastrophe. Unlike the $\text{Ti}^{4+}\text{O}_2^{2-}$ and $\text{Sr}^{2+}\text{O}^{2-}$ planes in STO which are charge neutral, the $\text{La}^{3+}\text{O}^{2-}$ and $\text{Al}^{3+}\text{O}_2^{2-}$ layer are polar, with a net charge of $+e$ and $-e$ respectively. When LAO is grown on TiO_2 terminated STO, the polarity will generate a positive electric field, that builds up as the thickness goes up, as shown in Fig 3(a). To avoid the divergence of potential, the electrons are transferred

from the surface to the interface and form 2DEG (Fig 3(c)). When LAO is grown on SrO terminated STO, a layer of hole gas will be formed in similar mechanism, as observed by Lee in 2018 ??.

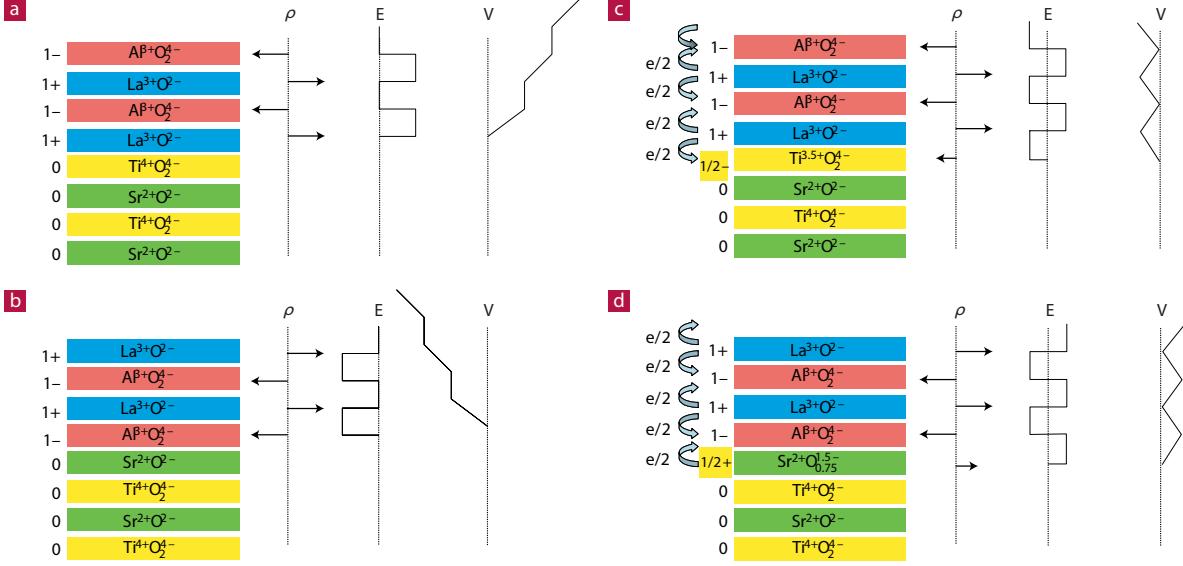


Figure 3: Polar catastrophe mechanism of interface conductivity of LAO/STO. (a) AlO_2 and LaO are not charge neutral and a non-zero potential is built up when LAO is grown on TiO_2 terminated STO. (c) Electrons are transferred from the top surface to neutralize the potential and therefore a layer of 2DEG is formed. (b) and (d) similar mechanism can explain the formation of hole gas on the interface. Adapted from [?].

However, there is a large discrepancy between the carrier density proposed by the polar catastrophe mechanism and measurement. In the polar catastrophe picture, each unit cell will donate half an electron, and result in carrier density of $3.2 \times 10^{14} \text{ cm}^{-2}$, while the observed values are in the order of 10^{13} cm^{-2} . Also, the formation of 2DEG on amorphous LAO on STO is contradicting to the polar catastrophe model.

Oxygen vacancy is another possible explanation of the origin of 2DEG[?], especially it is considered to be the source of conductivity of bulk STO[?]. Growth of LAO/STO sample under different oxygen partial pressure also shows that the carrier density is correlated to

P_{O_2} , therefore the oxygen vacancies can at least partially explain the formation of 2DEG. The magnetism is also considered related to the oxygen vacancies in STO, which will be discussed in future sections. There are also TEM[?] and XRD[?] evidences support that cation intermixing across the interface can also explain the formation of 2DEG. The exchange of La^{3+} and Sr^{2+} provides extra electrons in the STO. However, this cannot explain the formation of hole gas formation with LAO grown on SrO terminated STO. So far, the polar catastrophe is the most widely accepted explanation of 2DEG in LAO/STO.

2. Metal-insulator transition and critical thickness

In 2006, Thiel et al[?] reported that the interface of LAO/STO shows metal-insulator transition when the LAO is thicker than 4 unit cells. In Figure 4(a), when the thickness of LAO is over 4 u.c., the interface is conductive, otherwise the interface is insulating and the conductivity is below measurement limit. When LAO is slightly below the critical thickness (e.g. 3 u.c.), the conductivity on the interface is tunable, with a voltage applied to the backside of the sample. As Figure 4(b) demonstrates, when $V = +100$ V is applied, the interface becomes conductive. When the positive voltage is removed, the resistance is partially restored, but the interface is still conductive. However when $V = -100$ V is applied, the interface becomes insulating again. Similar phenomenon can be observed if the voltage is applied locally through a conductive AFM tip. More details are discussed in future chapters.

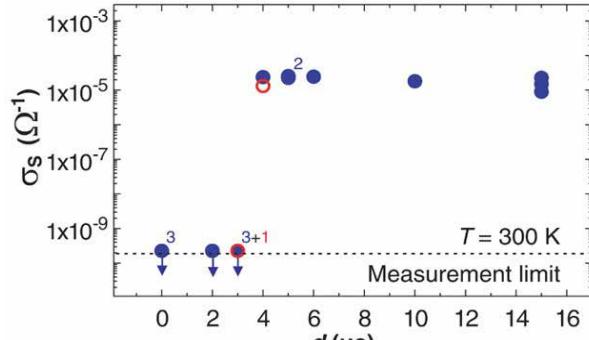
The mechanism of the interface tunability when LAO is slightly thinner than the critical thickness can be explained with the “water-cycle” mechanism.

3. Superconductivity

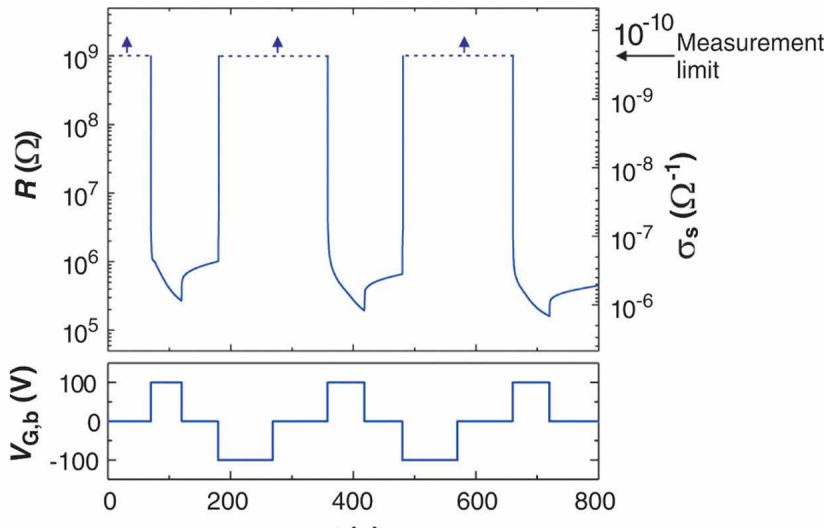
4. Magnetism

C. GRAPHENE

Graphene is another two dimensional material.



(a)



(b)

Figure 4: (a) When LAO is over the critical thickness of 4 unit cells, the interface is conductive, otherwise the interface is insulating. (b) When LAO is only slightly below the critical thickness (e.g. 3 unit cells), the conductivity on the interface is tunable with an external voltage applied to the backside. The as-grown sample is insulating. When +100 V is applied, the interface becomes conductive. When the voltage is removed, the conductivity still persists. When -100 V is applied, the interface becomes insulating again. The conductivity can be cycled with alternating voltages. Adapted from [?].

1. Dirac fermion
2. Quantum Hall effect

II. EXPERIMENTAL METHODS

A. SAMPLE GROWTH

1. LAO/STO growth

The properties of LAO/STO interfaces is extremely sensitive to growth conditions such as substrate temperature, background oxygen pressure, annealing conditions, etc[]]. The samples in my experiments are grown with pulsed laser deposition (PLD) by our collaborators at University of Wisconsin-Madison.

a. Pre-growth treatment STO (001) substrates are purchased from commercial crystal suppliers. The substrates have a carefully controlled mis-cut angle $< 0.1^\circ$, so that the width atomic terraces on STO surface is about 500nm (Figure 6(a)). It has been reported that the terraces on LAO/STO surface can affect the properties of the LAO/STO interface[], therefore I would like to place the nano-devices on a single terrace. If the terraces are too wide however, the LAO cannot grow evenly on STO surface during the PLD process due to the high activation energy for atomic migrations, and end up forming holes on the sample (FIG. []).

Before PLD growth, the STO (001) substrates are etched with buffered HF acid to remove SrO and become TiO_2 terminated. Then they are annealed at 1000°C for several hours, so that the surface will reconstruct and form crystal terraces[]. Then the samples are etched in HF acid for a second time for a clean surface to start with.

b. PLD deposition In PLD deposition, KrF excimer laser ($\lambda = 248$ nm) pulses are focused on a LAO target and plumes of target material are deposited on the pre-heated STO substrates. Two growth conditions are used for LAO/STO samples: (a) STO substrate is heated at $T_{\text{STO}} = 550^\circ\text{C}$ and chamber background oxygen pressure $P(O_2)$ is maintained at 1×10^{-3} mbar, and sample is annealed in 1 atm of O_2 after growth. (b) STO substrate is heated at $T_{\text{STO}} = 780^\circ\text{C}$ and chamber background oxygen pressure $P(O_2) = 7.5 \times 10^{-5}$ mbar; sample is annealed at $T_{\text{STO}} = 600^\circ\text{C}$, $P(O_2) = 300$ mbar for 1 hour after growth, or the so called “Augsburg condition”. Samples grown in condition (a) are used for nano-scale device lithography (i.e. the sub-critical thickness 3.4 uc LAO/STO samples), while condition (b) favors the formation of oxygen vacancies and sample grown in such conditions are used for magnetism experiments[].

Figure 5 illustrates PLD epitaxial growth of LAO. The sample is loaded in an ultra-high vacuum chamber, backed filled with oxygen to the target pressure. Deep UV excimer laser is focused on the LAO crystal target through an optical window on the chamber. The target material is ablated off by the laser and forms a plasma plume. The STO substrate is heated up and placed on top of the target. As the plume expands, target material would condense on the substrate and forms atomic-thin films epitaxially. The high temperature of the substrate facilitates crystallization and epitaxial growth of the target material.

During the PLD growth, the reflection high-energy electron diffraction (RHEED) is used for *in-situ* monitoring the thickness of epitaxial layers. A beam of electron is generated from a source and reflected from the substrate as the film growth. Electron diffraction signal is collected by a detector on the other side (Figure 6(a) inset). The intensity of the diffraction oscillates as a function of film thickness. The thickness of LAO can be precisely controlled by counting the cycles of RHEED signal (Figure 6(b)).

2. Graphene growth

The two most popular methods to obtain single-layer/few-layer graphene are mechanical exfoliation and chemical vapor deposition (CVD).

The mechanical exfoliation isolate single layer graphene directly with adhesive tapes.

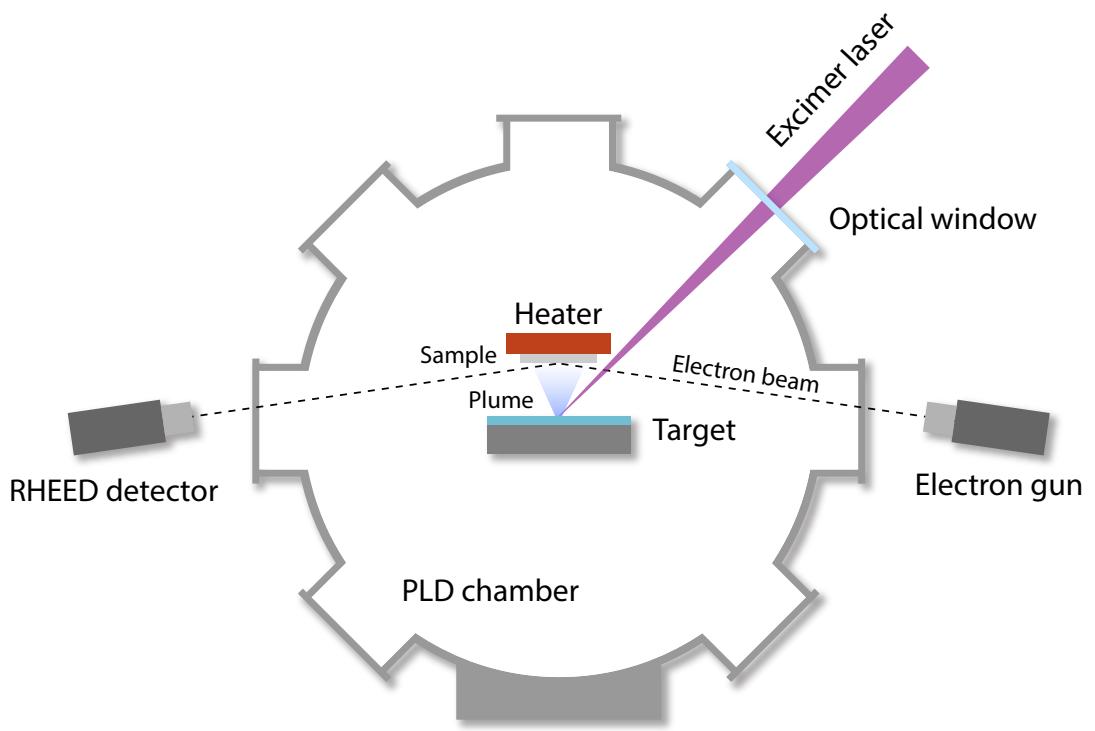
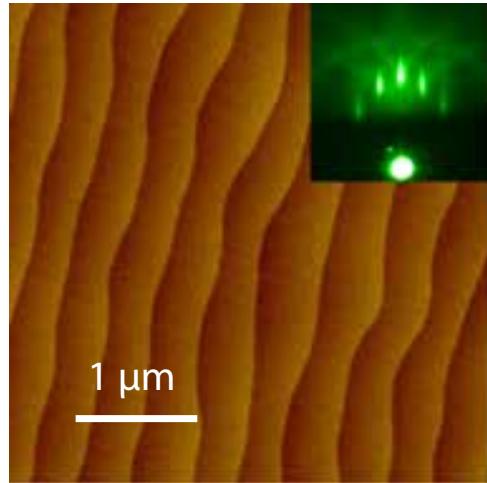
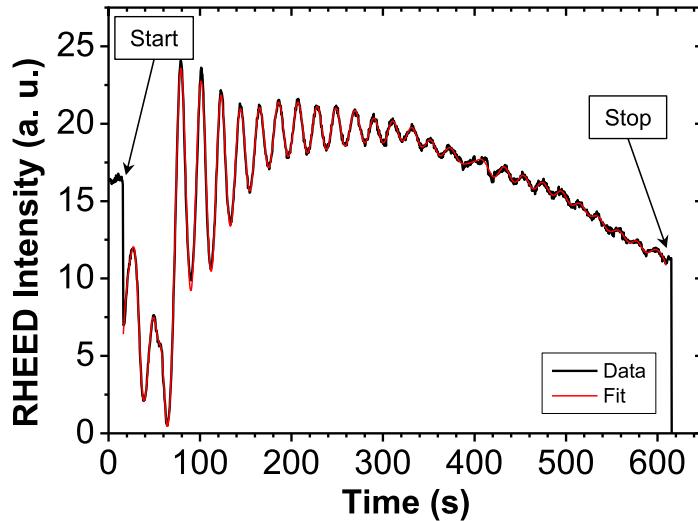


Figure 5: PLD epitaxial growth. The PLD chamber is back-filled with oxygen to the target pressure. A beam of pulsed deep-UV excimer laser is focused onto the LAO target. LAO is ablated off and a plume of plasma extends towards the heated substrate on top, and condensed into atomic layer films. RHEED signal is used to monitor the thickness of LAO.



(a)



(b)

Figure 6: AFM image of the sample after PLD growth and RHEED signal for PLD. (a) shows is the AFM Image. The stripes are the crystal terraces, with $h \approx 4\text{\AA}$. Inset of a is the raw diffraction sigal of RHEED. (b) is the RHEED signal oscillation during the film epitaxial growth. Each cycle indicates the completion of one unit cell. Adapted from [?].

Graphene is a type of Van der Waals material, which means that the carbon atoms within a layer are tightly bonded with covalent bond while the layers are bonded by the much weaker Van der Waals force. Adhesive tapes can easily separate graphene layers. After multiple exfoliation steps, single layer graphene flakes can be separated and identified^[1]. One key aspect of the exfoliation method is choosing a proper substrate so that single layer graphene can be easily identified with optical microscopes. Although graphene has the highest absorption coefficient in the visible light regime^[2], spotting a single layer graphene flake on a transparent substrate is still challenging, and is always assisted with other characterization methods such as Raman spectroscopy^[3] to ensure the graphene is single layer. The most commonly used substrate is silicon wafer with 400 nm thick silicon oxide coating. The graphene on SiO₂ surface modifies the interference of visible light between the top and bottom layer of the coating, and make graphene layers distinguishable^[4].

CVD is another method to obtain single layer graphene. Graphene flake from mechanical exfoliation is mostly a few micrometers or tens of micrometers; the size and shape cannot be well controlled. CVD on the other hand, can grow continuous graphene of wafer sizes^[5], and then etch it into any desired shapes through post processing.

The CVD process uses gaseous organic molecules (methane, ethylene, etc) as carbon source, and graphene lattice-matching crystal such as SiC, Ni and Cu as substrate. In high temperature (about 1000 °C), the metal surface has highly reactive and can serve as a catalyst, so the gas molecules will react with the surface and carbon atoms are detached. At the high temperature, the carbon atoms will self-assemble into graphene. Over the entire process the substrate is in continuous gas flow so the gaseous byproducts are flushed away. Hydrogen is used to assist the growth. Without hydrogen molecules, graphene will grow simultaneously on the entire surface and form many small domains. Hydrogen will etch away the smaller domains while the larger domains are growing^[6] until the domains meet or the process is terminated. If the hydrogen portion is too high however, the larger graphene domains will also be etched. CVD is a dynamic process of etching and growth. The ratio of methane and hydrogen need to be carefully controlled.

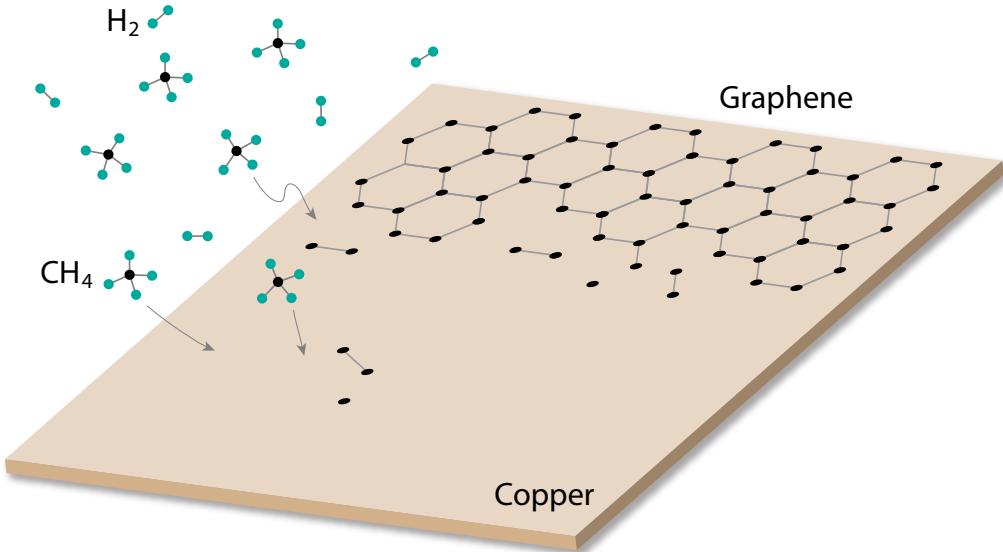


Figure 7: Graphene CVD growth. At a temperature $T \approx 1000$ °C, methane molecules will react with copper surface and leave carbon atom on the surface. Carbon atom will self-assemble into graphene due to lattice-match between graphene and copper super-cell. Hydrogen is used to etch away the smaller domain so that the final single layer graphene domains is as large as possible. If the hydrogen is too much, the larger domain will be etched as well. Ratio of methane and hydrogen has to be carefully controlled.

a. Copper substrate preparation Various materials are used for graphene growth, most commonly SiC, Ir, Ni and Cu. Cu is the most common choice due to the low carbon solubility scalability[?]. Compared to other substrate materials, copper is also easy to be etched with wet chemicals. In CVD growth, the carbon atoms form into graphene following the shape of the substrate. Therefore, the graphene quality is directly related to the substrate. Surface roughness and contaminants on copper provide nucleation centers for graphene, and can cause polycrystalline structure and multi-layer growth[?]. The is-

sue of surface roughness can be addressed with electrochemical polishing[?], or mechanical polishing. In my experiment I used a graphene substrates polishing procedure developed by Dr Brian D'Urso's graduate students, by using diamond turning machine to reduce the roughness from several hundred of nanometer to a few nanometers[?]. Compared to electro-chemically polished substrates, this method can produce surface 50 time smoother and the copper domain sizes are 5 time larger.

Diamond turning machine (DTM) is commonly used for high precision manufacturing, such as laser reflective mirrors. The DTM works like a lathe, where the workpiece is fixed on a turning spindle, and the cutting tool approaches the workpiece and shape or polish the surface by steps. Figure 8 shows the DTM at work. The cylindrical metal piece in 8(a) is the spindle of DTM. Copper piece is fixed on the spindle by vacuum and spins at 2000 rpm. The diamond tool approaches the copper surface from the opposite side. 8(b) is an image taken in the middle of a cutting step. Mineral spirit is sprayed from a nozzle from the left, cools down the surface and blow away the metal debris to the right. Reflection of the tool shows a flat finishing.

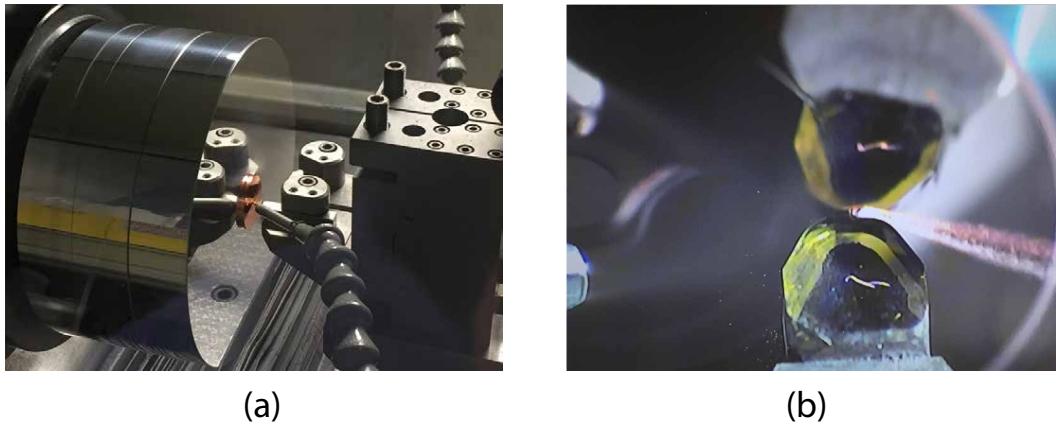


Figure 8: Diamond turning machine at work. (a) Copper is fixed onto a turning spindle with vacuum. (b) Mineral spirit is sprayed to the tool and cutting spot, to cool down the surface and blow away the debris.

Instead of using high-speed-steel tool, the DTM uses a curved-edge (radius of curvature $r = 1.5$ mm) diamond tool for cutting (Figure 9). By reducing the incremental step to 10 μm , as shown in Figure 10, the roughness of the finishing surface can be smaller than 2 nm. The diamond tool sometimes have dents on the surface, which can leave a trace after each revolution.



Figure 9: (a) Diamond cutting tool. (b) Magnified image of the tool tip. The radius of curvature $r = 1.5$ mm.

Other than surface roughness, the copper domain size is another limiting factor for large single domain graphene growth. Although it was not clear the effect of copper substrate domains on the graphene quality[?, ?, ?], larger copper domain sizes gives us a chance to grow larger graphene single crystals. Annealing the copper at high temperature will reconstruct and merge the polycrystalline domains into larger domains, but mechanical processing will introduce defects and break the single domains, therefore the substrate needs to be re-annealed several times before graphene growth starts.

The procedure of making copper rods into copper foils for graphene growth is shown in Figure 11. 1' long oxygen-free high thermal conductivity copper (OFHC) rods with ultra high purity (99.99%) are purchased from McMaster Carr. The rod is first annealed at $T = 1050^\circ\text{C}$ for 24 h, and the polycrystalline domains will merge into larger domains within

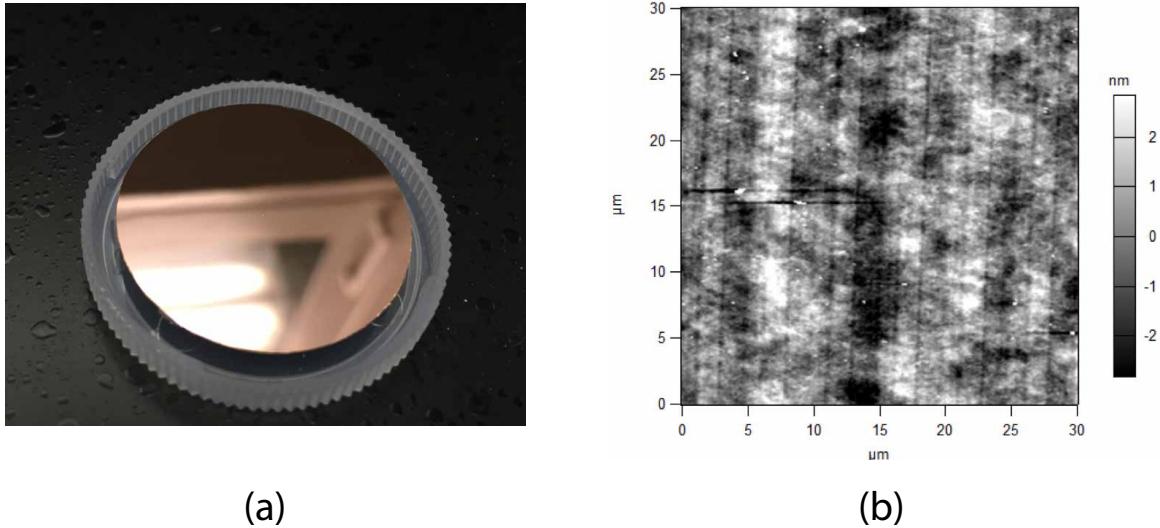


Figure 10: (a) The copper foil after DTM thinning and polishing. The final thickness is about $100 \mu\text{m}$. (b) The AFM image of the copper foil surface. The roughness is within 2 nm. Some nanoscale particles can be seen. They will serve as nucleation centers for graphene CVD growth. Reducing the density of surface contaminants will reduce the density of nucleation centers and enlarge the domain size. The vertical grooves are left by the dents on the diamond tool for each revolution. The spacing between grooves is the same as the approaching speed of the tool towards the center. The copper is annealed at $T = 1050^\circ\text{C}$ for another time before growth starts and the surface will reconstruct, possibly remove those grooves.

the rod. Then the rod is cut into copper discs of 2 mm thick in the machine shop. The mechanical cutting process will break the domains and introduce surface contaminant, so the surfaces copper discs are cleaned on the DTM, and then annealed again at 1050°C for 8 h. After annealing, the domain re-formation in the disc will cause corrugation on the surface. Therefore, the copper surface is cut with DTM for a second time. Then the copper discs are cut into $100 \mu\text{m}$ foils, by $10\mu\text{m}$ steps. During the thinning procedure, the copper will expand on the cutting face and cause deformation, and eventually break the vacuum sealing between the disc and spindle on the backside. Therefore the copper disc needs to be flipped

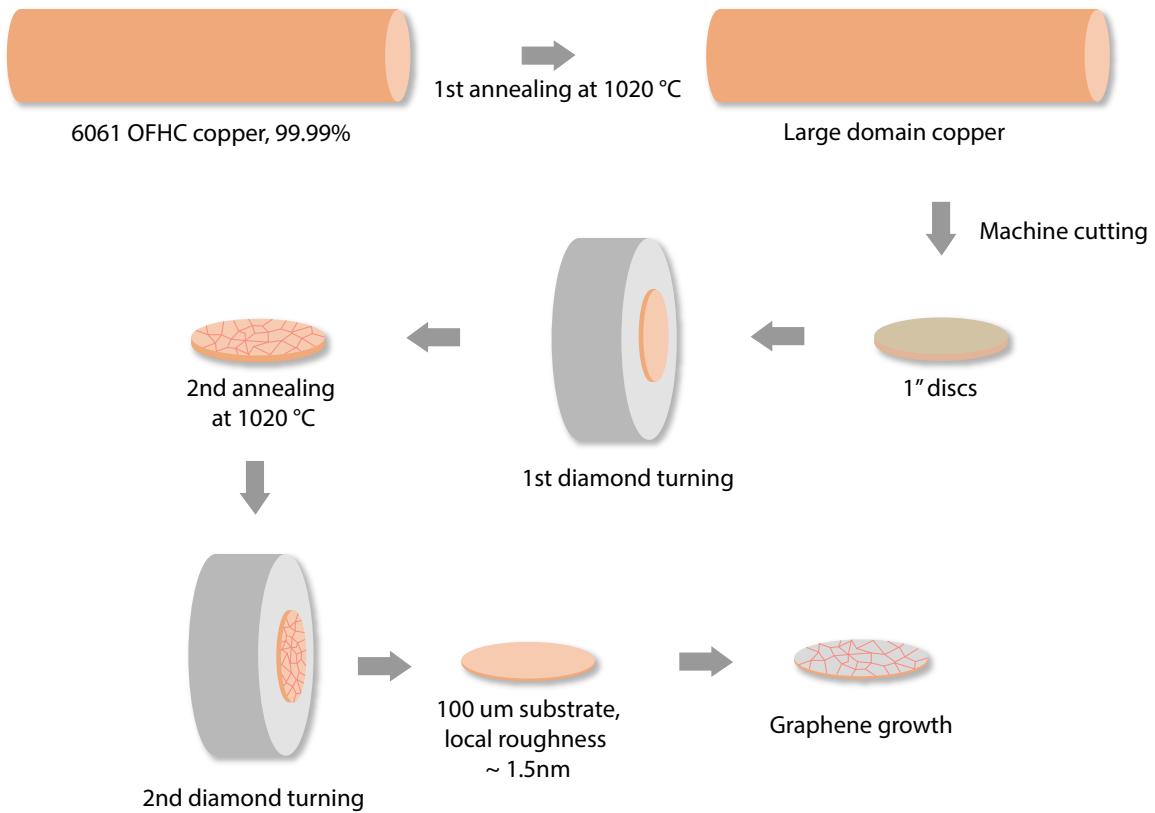


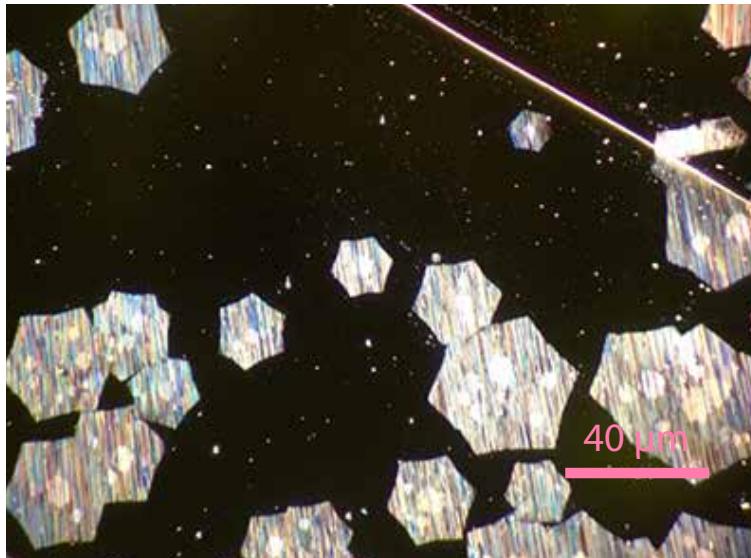
Figure 11: Procedure of cutting copper rods into $100\mu\text{m}$ foils for graphene growth. High purity OFHC copper rods are purchased from McMaster Carr. The rod is first annealed at 1050°C for 24 h so that the polycrystalline domains merge into larger domains. Then the rod is cut into 2 mm thick copper discs in the machine shop. The discs are cleaned with DTM and then annealed again to “heal” the domains broken by the mechanical cutting. Domain re-formation would cause surface corrugation, therefore the copper disc surface is cleaned with DTM and then cut into $100 \mu\text{m}$ foils and ready for graphene CVD growth.

several times to balance out the expansion and keep it flat when thinned from 2 mm to 100 μm . The process takes about 5 h.

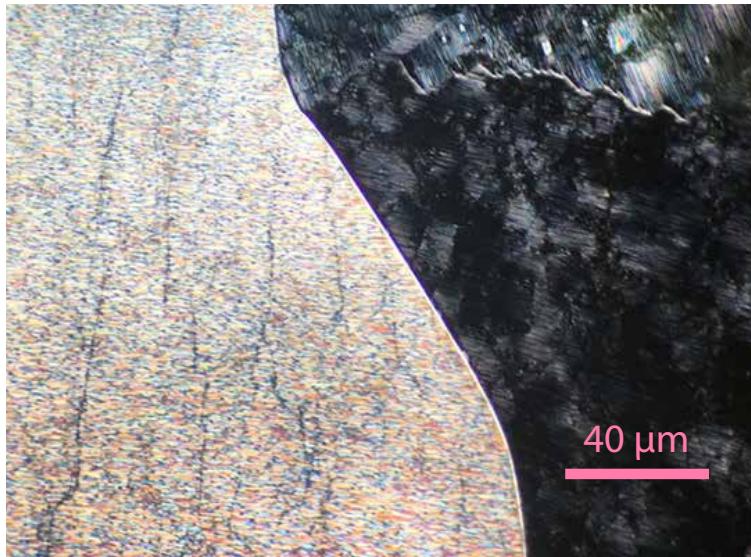
b. CVD growth The CVD graphene growth can be performed in low pressure (LPCVD) or atmospheric pressure (APCVD). LPCVD grows graphene at a pressure lower than 100 mTorr, in continuous flow of pure hydrogen and methane and 1000 °C substrate temperature. The problem with LPCVD is that the melting point of copper ($T_m = 1085^\circ\text{C}$) is close to the growth temperature. As a result, the copper will evaporate severely and contaminate the furnace[?]. Also the pure precursor gases bring fire hazard. APCVD can eliminate those difficulties associated with the low pressure and improve the graphene quality[?, ?]. Instead of using pure hydrogen and methane, APCVD uses gas mixture of 2.5 vol % H₂, balance Ar and 0.1 vol % CH₄, balance Ar. The furnace is pumped into low vacuum, and then backfilled with hydrogen and methane mixture with argon to atmospheric pressure. The pressure from argon will suppress the evaporation of copper at annealing and growth temperature. The growth procedure are as follows[?].

- The furnace is pumped down to ~ 10 mTorr.
- Backfill the furnace with H₂/Ar mixture at 186 sccm, up to ~ 100 Torr.
- Start the furnace temperature controller to ramp up to $T = 1050^\circ\text{C}$, and let the furnace stay at this temperature for 1 h.
- Start flowing mixture of CH₄/Ar at 14 sccm into the furnace, and let the copper foil stay in graphene CVD growth condition for 1.5 h.
- Cool down the system to room temperature. The flow of CH₄/Ar mixture is turned off at $\sim 650^\circ\text{C}$. H₂/Ar mixture keeps flowing overnight until the system is below 50°C.

In Figure 12 the dark-field mode of optical microscope clearly shows the CVD graphene on copper substrates. In 12(a) the copper substrate is partially covered with graphene. Growth process was terminated before graphene fully covered the substrate. Hexagonal shape of single domain graphene flakes can be identified. Some double layer can also be observed. On the top right corner of 12(a) a copper domain boundary can also be identified. 12(b) is the image of graphene grown in another run. The substrate is fully covered with graphene.



(a)



(b)

Figure 12: Dark field mode microscope images of CVD graphene on copper substrates. (a) The copper substrate is partially covered with graphene. Hexagonal shape single domain graphene can be observed. Growth was terminated before graphene fully covered the substrate. (b) Graphene covers the entire copper substrate. Copper domains of two different orientations can be observed.

The image shows two copper domains with different orientations, and therefore the graphene grown on the two domains look different. Wrinkles on graphene can be observed, resulting from different thermal expansion coefficient of copper and graphene.

B. LAO/STO SAMPLE PROCESSING

One of the challenges with experiments on LAO/STO interface is to make good electrical contact from the bonding pads to the interface. A major part of my research is process the samples before the c-AFM writing can be performed, or graphene can be transferred. The processing methods have to be carefully chosen, so that the 2DEG and interface switchability will not be affected. Sample cleanliness is another concern. Any nano-particles introduced in the processing would affect sample quality, considering that the dimensions of devices are also nanoscale. The procedure has those steps. Details of each steps can be found in the following subsections.

- **Initial cleaning:** the sample is cleaned with acetone and isopropanol alcohol (IPA) in ultrasonic cleaner so that the particles introduced in shipping are cleaned. Usually I do not use deionized-water (DI-water) for cleaning. Proton from the water can get adsorbed on sample surface and affect the interface [].
- **Photolithography:** the sample is patterned with standard UV photolithography procedures, either with optical mask and UV lamp or with direct laser writer, for further processing. The finest features in my design are $2 \mu\text{m}$ wide.
- **Ion milling:** after the desired patterns are developed on the photoresist, the sample is bombarded with Ar^+ ion flow so that the LAO/STO interface is exposed for electrical contact.
- **Deposition:** titanium and gold are used for making contact with the exposed LAO/STO interface either by electron-beam evaporation or sputtering.
- **Lift-off:** the excessive metal outside the patterns are removed by the lift-off process following the metal deposition. Like the initial cleaning process, I use IPA and acetone and do not use water as solvent.

- **Oxygen plasma cleaning:** the sample is covered with residue from liquid and photoresist, until it is cleaned with oxygen plasma cleaner. Then the atomic steps can be seen with AFM.

1. Photolithography

Photolithography is a commonly used technology in semiconductor industry. The substrate is covered with a thin layer of photosensitive material, and exposed under UV with designed pattern. UV would change the solubility of photoresist, and the pattern can be developed in buffered base solution after exposure. The modern state-of-the-art photolithography technology can reach resolution of 10 nm, and is the pillar of support for the semiconductor industry. There are two types of photoresists: positive and negative. Positive photoresist will be washed off after been exposed to UV, while negative photoresist is stabilized by UV and the unexposed parts are removed by developers. In my experiment, three types of positive photoresists are used: AZ4620, AZ4210 and AZ4110. For each sample, a 3" carrier wafer is used as a fixture, because dimensions of the sample ($5\text{ mm} \times 5\text{ mm} \times 1\text{ mm}$) is non-standard and most photolithography instruments are designed for 3" or 4" wafer in the university facility. AZ4620 is used to fix the sample to the carrier wafer during spin-coating, exposure and other steps, as a cleanroom-safe "epoxy".

a. **Spin-coating** There are two limiting factors of the feature resolutions in photolithography: diffraction limit of light and photoresist thickness. Spin-coating is a standard technique to keep the photoresist thin and uniformly covering the sample surface. When the sample is spinning at a speed between 1000 rpm and 6000 rpm, the photoresist solution on sample surface would spread out. The liquid will be finally in equilibrium of viscosity, roughness of sample surface and centrifugation from spinning. After the spin-coating the sample is baked at a temperature around 100°C to vaporize the remaining solvent in photoresist and stabilize the film before UV exposure. In my experiment, the spinning speed is 4000 rpm. Thickness of AZ4210 and AZ4110 are $2.1\text{ }\mu\text{m}$ and $1.1\text{ }\mu\text{m}$, respectively. These two types of photoresists have the same chemical composition. They have different concentrations so

Photoresist	AZ4210	AZ4110
Spinning speed	4000 rpm	4000 rpm
Spinning time	30 s	30 s
Baking temperature	95 °C	95 °C
Baking time	60 s	60 s
Thickness	2.1 μm	1.1 μm

Table 1: Spin-coating conditions for AZ4210 and AZ4110

they end up with different thickness at the same speed. Usually thinner films will have more stable performances for fine features (2 μm , to be specific). In the cases where photoresists are consumed in the processing, such as reactive ion etching, thicker photoresist will be a better option.

The spin-coating conditions are listed in Table 1.

b. UV exposure The UV exposure is to use short wavelength photon to change the solubility of photoresist, so that the following developing step can selectively remove the photoresist. Like all optical system, the resolution of exposure is limited by diffraction limit of light. Historically, mercury gas-discharge lamp is used, and 436 nm (“g-line”), 405 nm (“h-line”) and 365 nm (“i-line”) are selected as UV source. Besides Hg lamp, solid state laser and excimer laser with shorter wavelengths are also commonly used UV source. Samples are exposed with two different methods: mask exposure and direct laser writing.

Mask exposure is performed by a mask aligner (Suss MJB3 or MA6, see Figure 14). Incoherent and collimated UV is generated from a Hg lamp and an area of 4” \times 4” is exposed with UV at constant intensity of 10 mW/cm². The sample is covered with a photomask (Figure 13), a square piece of soda glass (to reduce UV absorption) coated with chromium on one side. The desired pattern is pre-printed on the chromium by chemical etching. The patterns on photoresist will be the copy of the patterns on the photomask.

Direct laser writing uses focused laser beam to write micro-patterns directly on photo-

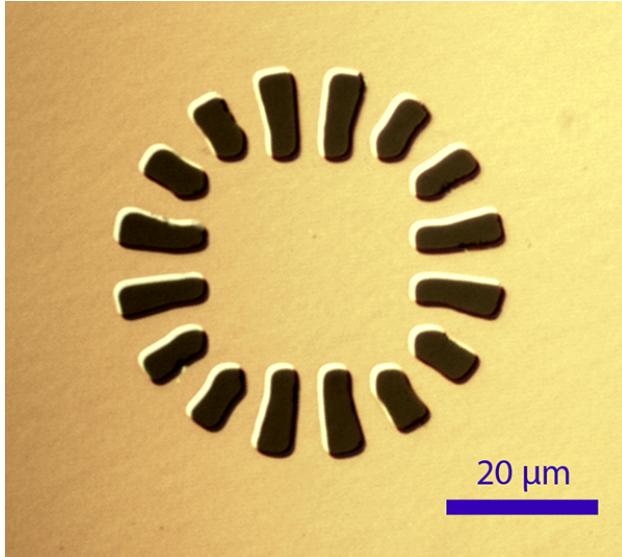


Figure 13: Mask for photolithography. The dark parts are transparent, while the bright parts are covered with chromium. Sample will be exposed where UV is not blocked by chromium. The figure shows the electrodes of a canvas for writing. The pedal shape area to be exposed will be etched away on the sample, and filled with titanium and gold to make contact with the LAO/STO interface.

sist, without using a mask. This is usually how the photomask is manufactured, but it can also be used to pattern samples. In my experiment I use Heidelberg MLA100 (Figure 15), with 405 nm wavelength UV generated from a solid state laser. The laser is expanded and filtered with a pinhole, and focused on the sample with a high-NA objective. The focusing objective is fixed, and the sample is placed on a piezostage controlled with computer, to follow the designed pattern. The UV dose is determined by both the laser power and laser spot dwell time. It is important to keep the sample surface horizontal, so that laser spot will not defocus while moving through the entire sample surface. Sometimes, especially when the critical dimension is close to $2 \mu\text{m}$, the photoresist patterns are not evenly developed, and it is caused by sample tilting and laser defocussing. Focusing quality can be checked with a real-time CCD image, while the sample is illuminated with red light from a second diode.

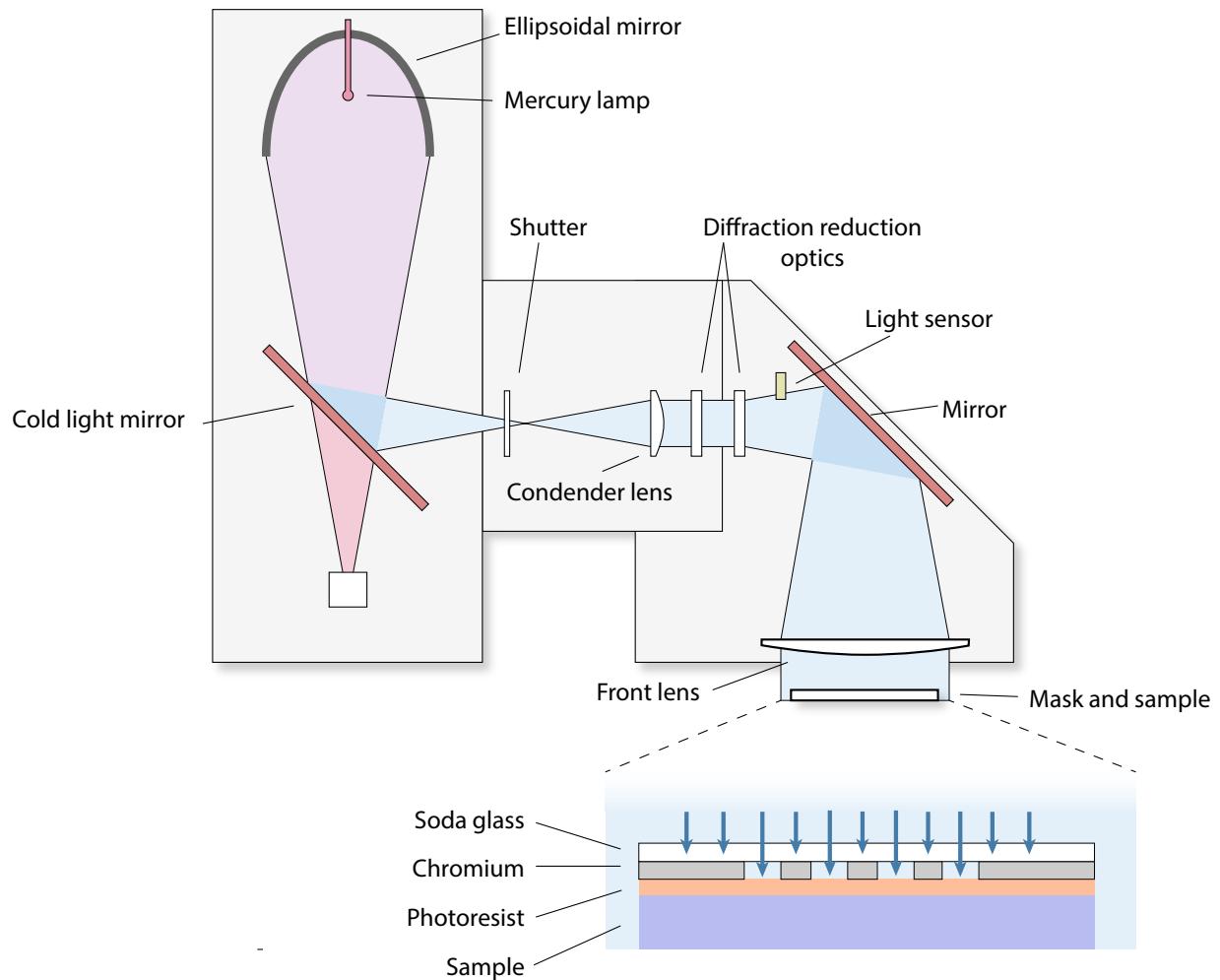


Figure 14: Suss UV mask aligner. The mercury lamp generates broad band light from arc. The light is reflected by an ellipsoidal mirror and focused on a cold light mirror, where the UV is selected. The exposure time and dose is controlled by a shutter. The UV is collected by a condenser lens and diffraction reduction optics for collimation and resolution enhancement. Finally, the UV is shed on the sample with a folding mirror and front lens. The sample is in close contact with a mask, where chromium is partially removed. UV exposes the photoresist on sample and print the mask pattern on it.

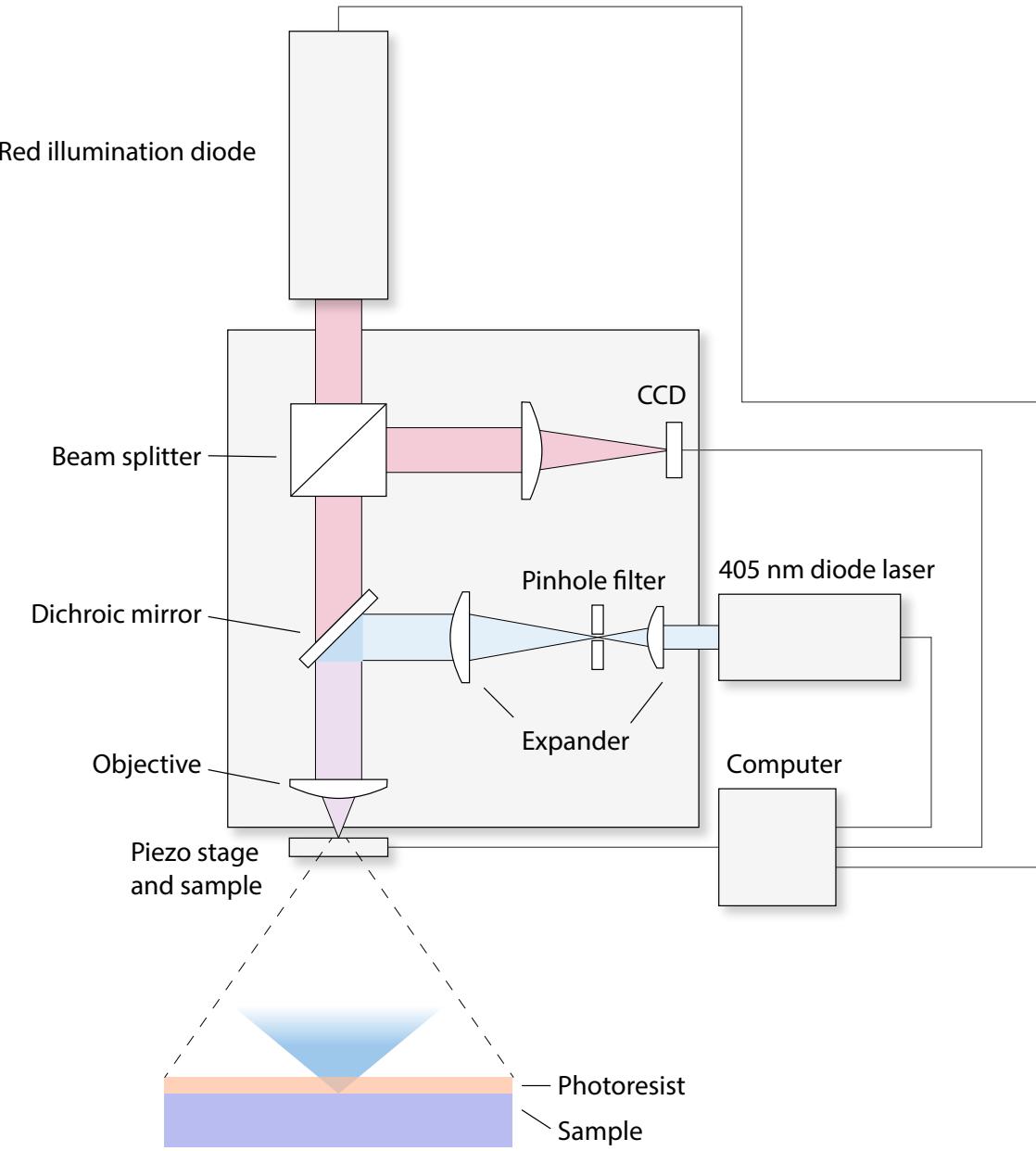


Figure 15: Heidelberg direct laser writer. The sample is illuminated by red light from a diode, and realtime image of sample surface can be monitored with a CCD. UV with $\lambda = 405$ nm is generated from a laser diode. The laser is expanded and filtered, and focused on the sample surface with a high-NA objective. The sample is fixed on a piezo-driven stage. The CCD image, laser power, sample movement and layer alignment are all controlled with computer, so that the laser path and dose control is fully automatic.

c. Developing Developing is the process using buffered base solution to washed away the unwanted parts of photoresist after exposure and reveal the pattern on sample, for future processing steps. Depending on the type of photoresist, either the exposed parts (positive photoresist, such as AZ4210) or unexposed parts (negative photoresist, such as AZ5214) are removed. In my experiment I use AZ400K (potassium borate) for developing. The developer is diluted with DI-water, by 1:3 or 1:4 ratio. Different concentration will maintain different pH level and developing speed. The sample is washed with DI-water after developing, to remove the excessive developer.

A picture of sample after UV exposure and developing is shown in Figure 16. Photoresist in the bright area are washed away while the rest of the sample is still covered with photoresist. Note that the edges of the pattern are less sharp than the mask, because of the resolution limit from diffraction.

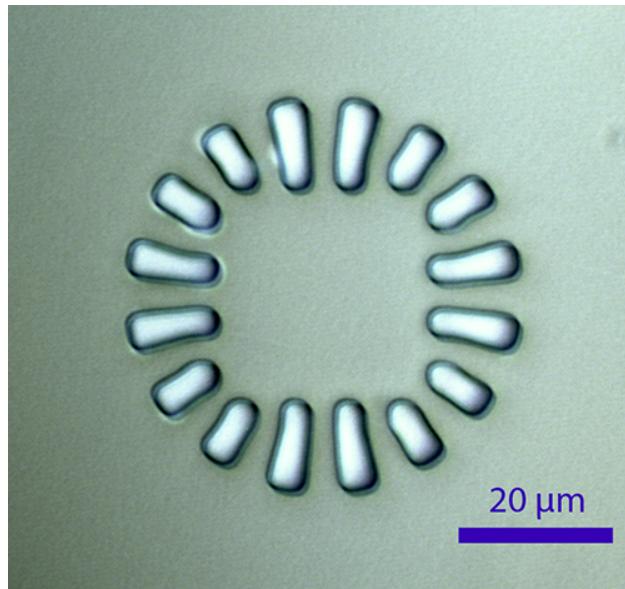


Figure 16: Exposed and developed sample. The bright regions are free of photoresist while the rest of the sample is still covered.

The conditions for exposure and developing are listed in Table 2.

Photolithography is the part that has the most uncertainty in the entire processing, and can affect some samples (like graphene/LAO/STO) more significantly. This will be discussed

Photoresist	AZ4210	AZ4110
UV Dose	170 mJ	100 - 120 mJ
Developer : Water	1 : 4	1 : 4
Developing time	120 - 180 s	120 - 180 s
Resolution	3 - 5 μm	2 μm

Table 2: UV exposure and developing conditions for AZ4210 and AZ4110

in future sections.

2. Ion milling

LAO/STO interface is buried underneath LAO, so if contact need to make with the interface the LAO has to be etched. Usually there are three methods for etching: wet chemical etching, dry plasma etching and ion milling. Wet chemical etching using reactive chemical solutions (such as HF, HCl) to etch away the materials. This is cheaper but less controlled, and etching is isotropic. Plasma etching, uses the plasma or free radicals of active gas molecules (such as O₂, SF₆, etc) to react and remove materials, at pressure between 0.1 and 5 Torr. It is better controlled in etching rate and directionality, but the instrument is more expensive, and finding the right gas to selectively etch the target material while keeping the protecting layer intact is not always feasible. Although there are mature industrial solutions for silicon, alumina and silica etching with plasma, there is not a good solution to etch complex oxides like LAO/STO with our facility plasma etcher. Ion mill uses accelerated ionized argon plasma to physically bombard sample. The etching rate is slower than the previous two methods, but performs well on most inorganic materials including LAO/STO. Also, the etching is more directional. In Table 3 is a comparison of the three etching methods.

Ion mill accelerates Ar ions to a certain kinetic energy and physically etch samples with bombardment. As shown in Figure 17, Ar⁺ is generated from the discharge chamber. The entire chamber is pumped to high vacuum ($< 10^{-6}$ Torr), and back filled with Ar gas to

Method	Chemical etching	Plasma etching	Ion milling
Cost	low	high	high
Directionality	anisotropic	anisotropic	isotropic
Selectivity	high	high	low
Speed control	poor	good	good
Target material	inorganic	organic or inorganic	inorganic
Environment	aqueous, ambient	0.1 - 5 Torr, vacuum	$< 10^{-4}$ Torr, vacuum

Table 3: Comparison of etching methods.

10^{-4} Torr. On the right-hand-side of the figure, a filament is heated up and a biased voltage is applied between the filament and chamber wall. Electrons are extracted from the filament and ionize the argon molecules. Usually a magnetic field is applied through a solenoid to increase the path and ion yield before the electrons reach the chamber wall. At the opening side of the discharge chamber, there are two or three layers of electrically isolated screen grids (made of sputter-resisting materials like Mo or W). A voltage of 500eV is applied between the grids so that Ar^+ can be accelerated to the same energy. The pores on the grids are aligned so that the Ar^+ flow is collimated. Right after the Ar^+ exits the chamber, it is mixed with free electrons from a neutralizer filament, so that flow will not be diverged by coulomb attraction between Ar^+ before they reach the sample. The sample is 20° to 30° off the normal direction, to create undercut and facilitate electrical contact between the interface and the electrodes. The kinetic energy of the plasma is converted to heat on the sample surface. Either the sample is cooled down with flowing water inside the chuck, or the plasma flow is shutoff with a duty cycle so that the sample can cool down by radiation. Temperature over 150°C will cause phase transition of the photoresist on the sample, and make hard to remove by lift-off.

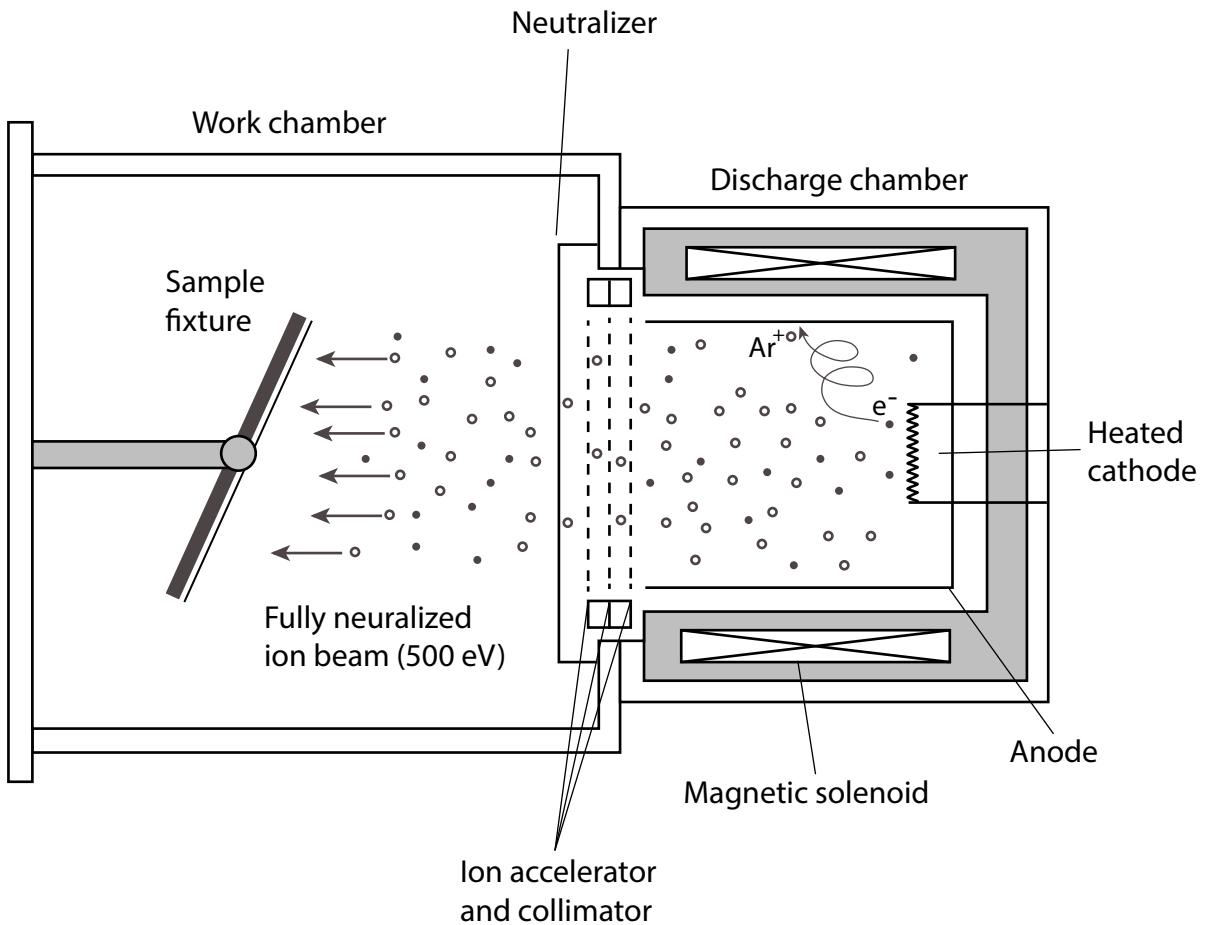


Figure 17: Simplified view of ion mill. The argon ions are generated from the discharge chamber on the right hand side. Electrons are extracted from the heated cathode and ionize Ar molecules before they reach the chamber, which works as an anode cup. A magnetic field is applied through a solenoid to increase electrons' path. Ar^+ are accelerated and aligned by the grids to 500 eV. Electrons from the neutralizer are mixed with the Ar^+ plasma flow so that the collimation can be maintained. Sample is tilted by an angle of 22.5°.

3. Deposition

Titanium and gold are used for making contact with the interface of LAO/STO. I use two methods to deposit the material: sputtering and electron-beam (e-beam) evaporation. Sputtering is similar to ion milling, but instead of using Ar^+ plasma flow to bombard and etch the sample, it is directed towards the target materials (Ti, Au, etc). The binding energy of the target material atoms is much smaller than the kinetic energy of Ar^+ , and are ejected from the target and deposit onto the sample surface. E-beam evaporation (Figure 18) controls electron-beam to heat up the target material and vaporize it. The vapor is thrown through a long distance ($> 10 \text{ cm}$) before it reaches the sample. Compared to sputtering, the e-beam evaporation is more directional, because of the long throw-distance of evaporated material, compared to sputtering.

As can be seen in Figure 18, a beam of electron is accelerated by a high voltage of 10 kV. The direction and shape of the beam is controlled by magnetic field. The material is loaded in a crucible and heated up by the e-beam, and evaporated. The material is thrown upward, towards the loading chamber where the sample is located. A shutter is used to control the deposition time of material. Deposition rate is controlled by the e-beam current. The sample can be tilted if needed, to improve the electrical contact to the interface.

The pressure of the chamber is maintained at $< 10^{-6} \text{ Torr}$ to avoid any oxidation of material during deposition. Also, when titanium is used to make contact between gold bonding pads and LAO/STO interface, a pre-deposition evaporation of 10 minutes is required. The evaporated titanium can trap the remaining oxygen molecules in the chamber, and further reduce the oxidation on the target metals.

4. Lift-off

When the sample is patterned with photolithography and coated with metal, the photoresist and the excessive metal need to be removed. Photoresist can dissolve in organic solvents such as acetone. Acetone has high vapor pressure, so it is not suitable to be heated up to increase solubility of photoresist. Also, when acetone dries up on sample it forms streaks. Therefore the sample needs washed with IPA to remove the acetone residue. When the photoresist

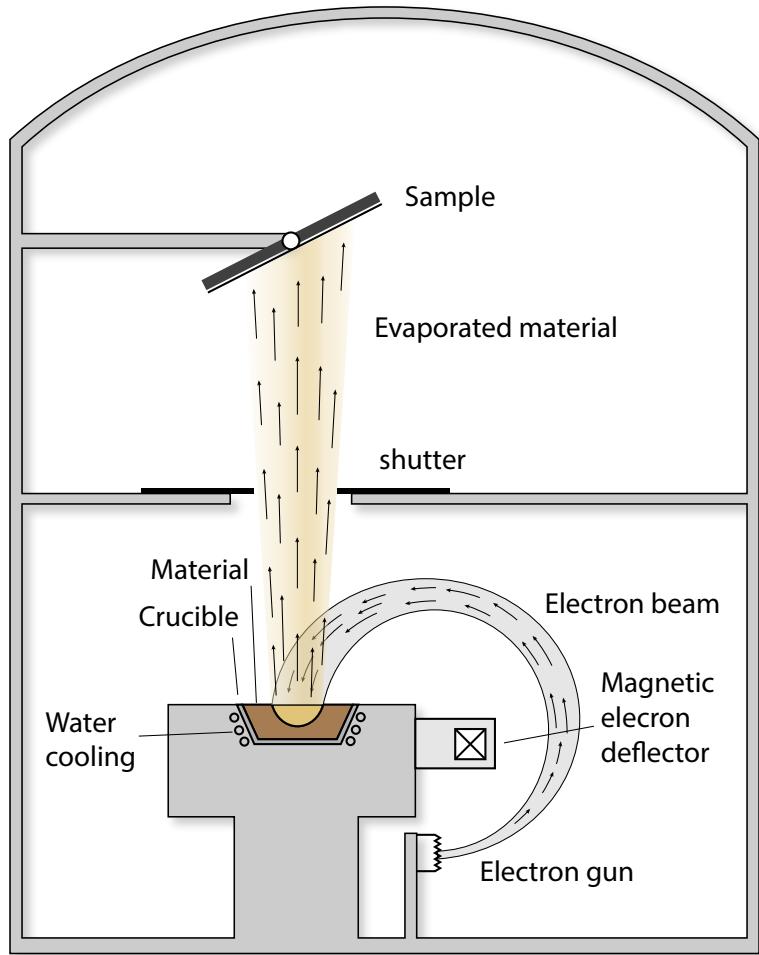


Figure 18: E-beam evaporator. The electron beam is generated from an electron gun on the bottom. The beam is deviated and shaped by magnetic field, towards the material. Them material is loaded in a crucible and heated up to melting point. The vapor of the material is thrown towards the sample on top. Deposition time is controlled by a shutter.

is heated up to 140 °C, it will cross-link and its solubility in will decrease. 1165 remover (1-methyl-2-pyrrolidone, or NMP) need to be used for lift-off in such cases. It has low vapor pressure and can be heated up to 80 °C. If 1165 still cannot fully remove the photoresist after sample is immersed in it for 24 hours, oxygen plasma cleaner is needed.

5. Oxygen plasma cleaning

The oxygen plasma cleaner activates oxygen molecules with a high frequency voltage (kHz to MHz) in low pressure, and forms plasma or oxygen radicals when electrons recombine with oxygen ions. The mixture is highly reactive and interactive with organic materials like photoresist residue on sample surface. Oxygen plasma cleaning can be categorized as a form of RIE (as discussed in Section II.B.2), but it usually operates at high end of 100 mTorr and the etching is less directional. Compared to chemical solvent, oxygen plasma removes the photoresist residue at a much lower speed (about 10 nm/min), but the finishing is much cleaner, while less invasive compared to RIE. Therefore, oxygen plasma is used as a final step for sample processing.

C. ATOMIC FORCE MICROSCOPE

The atomic force microscope (AFM) is a type of instrument that uses a nanoscale probe to interact with the surface, and characterize the properties (topography, coulomb interaction, conductivity, etc) of a sample. It was invented in IBM lab by Bin, Quate and Gerber [1] in 1986 and later commercialized in 1989. The development of AFM technology benefited from the advancement of STM and precise closed-loop spatial control using piezoelectric effect. However, AFM was invented to overcome the drawback of STM — only conductive or semi-conductive samples can be measured. AFM can measure various types of interaction between the probe and surface, such as surface potential, magnetic force, Van de Waals force, etc. Also, unlike the scanning electron microscopy (SEM) or tunneling electron microscopy (TEM), the AFM can be operated in various environment (aqueous, ambient, vacuum or

cryogenic), and does not require the sample to be pre-treated (such as metal coating) or being conductive. The robustness and non-invasive nature of AFM make it a powerful tool for studying surface phenomena including charge density, magnetic dipole moment, capacitance, chemical bonding, nano-device lithography, and even biomacromolecules. AFM can also be integrated with other techniques, such as infrared spectroscopy[]]. The limitation of AFM is that the imaging cannot be as fast as SEM or TEM, as it relies on the physical movement of the tip.

1. Working principle

The AFM is mainly consist of three parts, as shown in Figure 19, AFM tip, laser optics, and piezoelectric scanning system. The AFM tip has a sharp end, with a radius of curvature in nanometer scale. A laser is reflected from the top surface the AFM tip and collected by a quad detector. The quad detector determines the position by

$$\begin{aligned} I_{\text{sum}} &= I_A + I_B + I_C + I_D \\ I_{\text{vdiff}} &= (I_A + I_B) - (I_C + I_D) \\ I_{\text{hdif}} &= (I_A + I_C) - (I_B + I_D), \end{aligned} \tag{II.1}$$

where I_A , I_B , I_C and I_D are the intensities on the four quadrants of the detector. I_{sum} is the total intensity. I_{hdif} and I_{vdiff} are the horizontal difference and vertical intensity difference. Assume the laser spot has Gaussian distribution. When the center of laser spot changes, the distribution of intensity on the four quadrant would change. The detector can monitor the movement by the horizontal and vertical differences in intensities.

When the interaction between the tip and sample surface changes and the tip is slightly deformed, the laser spot would move on the quad detector. The deformation of tip follows Hooke's law:

$$F = -k \cdot x$$

Where x is the amount deformation, and k is the spring constant of the tip cantilever. k is mostly between 1 N/m and 100 N/m, depends on the type of the tip. In my experiment I use tips with spring constant $k = 3$ N/m. The interaction between the AFM tip and sample

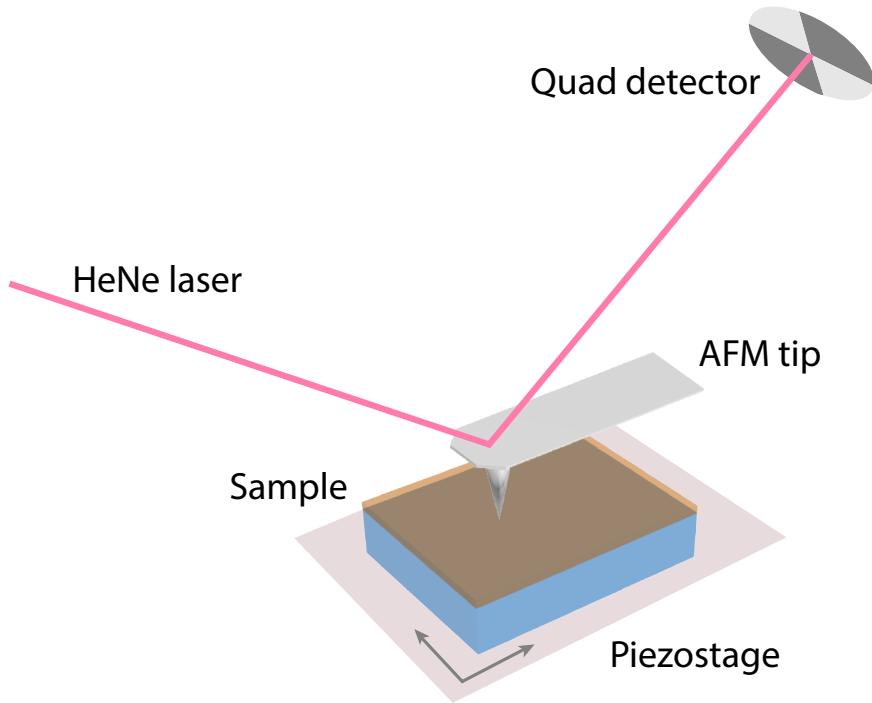


Figure 19: The schematics of AFM. A laser is reflected from the top surface of the AFM tip, and collected by a quad detector. A small amount of deformation would cause the center of laser spot to move on the quad detector, and measured as a change of differential voltage.

surface is a function of distance. The attractive interaction decays slower than the repulsive interaction, therefore the total interaction follows the curve in Figure 20.

The sample is located on the piezoelectric scanning stage. The movement of the sample and the tip, and signal from the laser optics system are controlled and monitored by a computer. The type of signal depends on the working mode of AFM and nature of the interaction it is targeting. More details are discussed in the following sections.

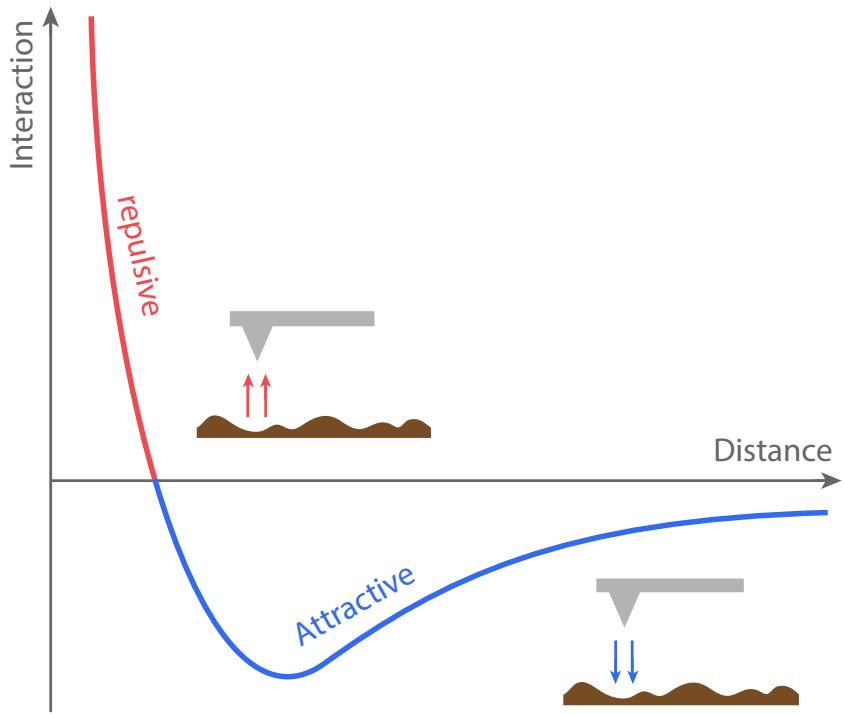


Figure 20: The interaction between tip and the sample surface. At long distance the interaction zero. When the tip moves towards the sample, the interaction is attractive. As the tip gets closer, the interaction would switch from attraction to repulsion, and gets larger and larger the tip moves closer.

2. Contact mode

In contact mode, the AFM tip is in close contact with the sample. The distance between the tip and sample is controlled by a piezoelectric actuator. As shown in Figure 21, When the tip approaches the sample surface, repulsion from the sample would deform the tip and changes the reflection angle of laser spot. The intensity difference on the quad detector would also change, in terms of a voltage signal. When the sample is driven by the piezostage and cause the tip moves relative to the sample, the repulsive force between the tip and sample surface is regulated by a feedback loop between voltage on the piezoelectric actuator, and the voltage

difference on the quad detector, so that the amount of tip deformation and repulsive force is a constant.

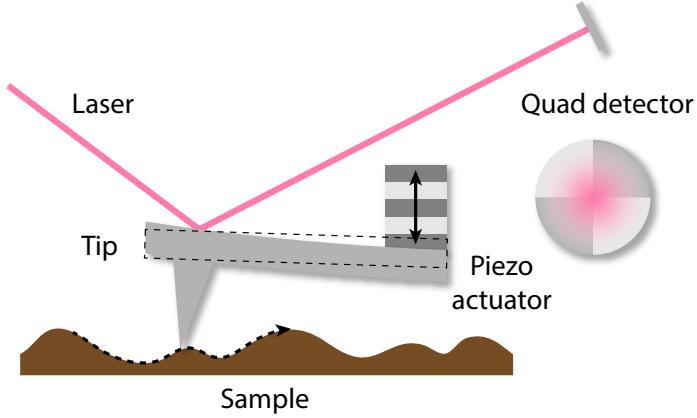


Figure 21: AFM contact mode. The tip is pressed to the sample surface by the piezoelectric actuator. The deformation of the tip is monitored by the quad detector. The voltage of the piezoelectric actuator is controlled with a feedback loop with the deflection voltage on the quad detector.

3. AC mode

In AC mode, the AFM tip is driven by a sinusoidal electrical signal on the tip piezoelectric actuator, as shown in Figure 22. The electric frequency is close to the resonance frequency of the tip (10 kHz - 500 kHz). The driving frequency is not exactly at the resonance frequency, so that the gradient of amplitude change is maximized and the system is most sensitive to the interaction change. The laser spot and difference signal also oscillates at the same frequency. When the tip approaches the sample surface, the interaction between the tip and surface will cause the resonance frequency to shift and change the amplitude A and phase θ of the tip oscillation and monitored by the differential voltage signal on the quad detector. The measurement can be integrated with lock-in amplifier technique (more discussion can be seen in section ??) for noise reduction. When the tip scan through the surface, A and θ are recorded for each position on the sample surface and mapped onto a 2D image. The

voltage on the piezoelectric actuator (z voltage) is controlled by a feedback loop so that the amplitude A is maintained at a set point. During the scan the tip is not continuously in contact with the sample surface, but “tapping” the sample with oscillations, therefore AC mode is sometimes called “tapping mode”. Compared to contact mode, in the AC mode the tip does not scratch the sample surface, so it has less effect on the sample.

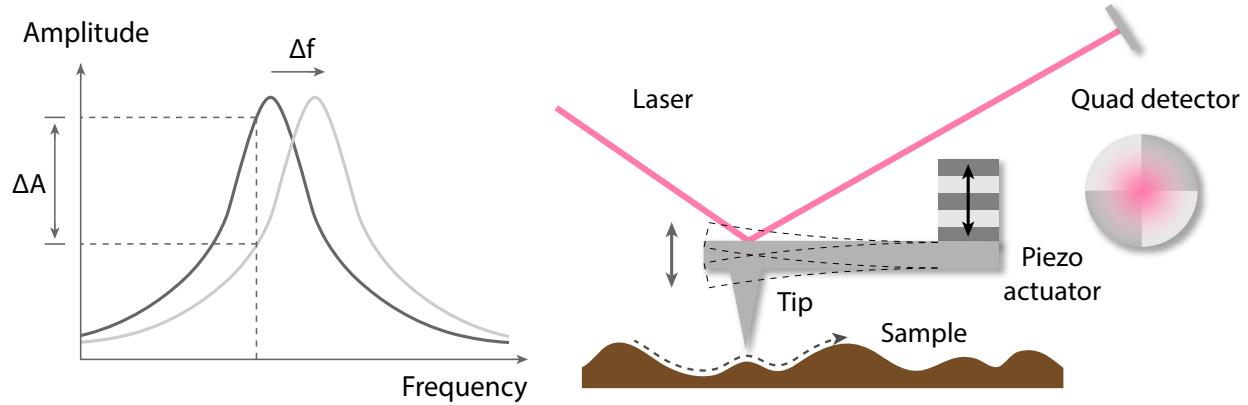


Figure 22: AFM AC mode. Left: the tip is driven at a frequency close to the resonance frequency. When the resonance frequency shifts as the interaction between the tip and the sample changes, the Change of oscillation amplitude is monitored by the quad detector and sent to the computer.

4. Non-contact mode

In the non-contact mode, the tip is maintained at the attractive regime so that the sample does not have direct contact with the tip. The tip and the sample is maintained at distance that the interaction is always attractive while the tip is scanning through the sample (Figure 23). This is especially important for biomacromolecules and organism.

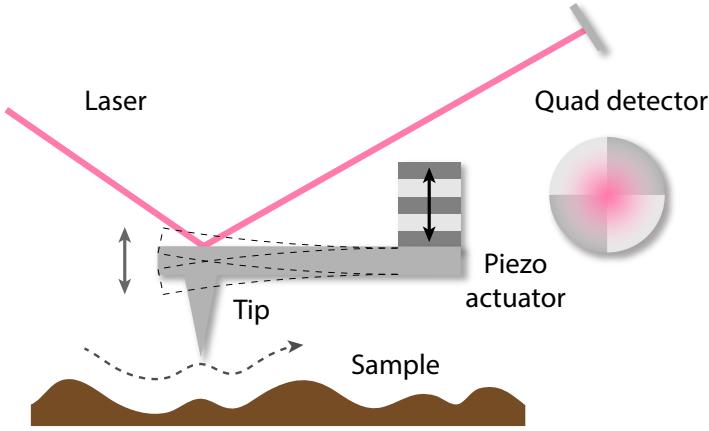


Figure 23: AFM Contact mode. The tip is kept at a distance from the sample so that the interaction is always attractive. The tip does not have direct contact with the sample during the scanning.

5. Magnetic force microscopy

When the tip is coated with ferromagnetic materials (e.g. Co, Fe), the AFM is able to measure the magnetic interaction with magnetic force microscopy (MFM). Similar to AC mode, the tip is driven by an oscillating AC voltage close to the resonance frequency (about 100 kHz in my experiments), and the change of interaction is monitored by the amplitude A and phase θ of the differential voltage on quad detector. The challenge of MFM is that the magnetic and Van der Waals interactions are coupled together. The two-pass technique is used to solve this problem, considering that Van der Waals interaction is short-ranged and decays much faster than the magnetic interaction. First, the tip scans the sample close to the surface, and measures the topography. Second, the tip is lifted and maintained at a constant distance (such as 50 nm) from the sample surface using the height information obtained from the previous scan, so that the Van der Waals interaction is negligible, and the spatial gradient of magnetic force can be measured[?]:

$$\Delta A \approx \frac{2A_0Q}{3\sqrt{3k}} \cdot \frac{\partial F_z}{\partial z}, \quad \Delta\phi \approx \frac{Q}{k} \cdot \frac{\partial F_z}{\partial z}, \quad \Delta f \approx -\frac{f_0}{2k} \cdot \frac{\partial F_z}{\partial z}, \quad (\text{II.2})$$

where ΔA , $\Delta\phi$ and Δf are the change of amplitude, phase and resonance frequency; A_0 and f_0 are the original amplitude and frequency; Q and k are the resonance quality factor and cantilever spring constant. $\partial F_z/\partial z$ is the spatial gradient of magnetic force.

Like contact and AC mode of AFM, the resolution of MFM is limited by the tip radius of curvature. Ferromagnetic coating of the MFM tip is about 40 nm in my experiments, so the resolution is in the same order of magnitude.

The advantage of MFM is that, the resolution and sensitivity is higher than other methods such as magneto-optical effect imaging. Also, like contact and AC mode AFM imaging, MFM does not require special treatment on the sample. The downside of MFM is that the imaging speed is limited by the raster scanning speed. The two-pass method takes twice as long as usual AFM AC scanning. Also, as equation (II.2) shows, MFM signal is not determined by the absolute value of magnetic force, but is proportional to the spatial gradient of the force $\partial F_z/\partial z$, therefore the scanning speed would also affecting the signal. The field from the AFM tip can also affect the magnetic dipoles on the sample, and MFM image can change after each scan. Other long range interaction such as coulomb interaction cannot be eliminated by the two-pass method, and need to be taken care of. More details are discussed in the next chapter. In spite of these disadvantages, the MFM is still a power tool to study surface magnetism for its robustness and simplicity of operations.

6. Piezoresponse force microscopy

The high sensitivity and extendability of AFM make it a versatile tool to measure different types of interactions between the tip and sample. One important variant is piezoresonse force microscopy (PFM). Piezoelectric effect is the phenomenon when an external stress or strain is applied to a piezoelectric material, the deformation will induce electric dipole moments and build up an internal electric potential across the sample. Inversely, the external electric field can also induce mechanical deformation. Depending on the properties of the material, the deformation can be either expansion or contraction. The effect can be described with

$$X_i = d_{ki}E_k,$$

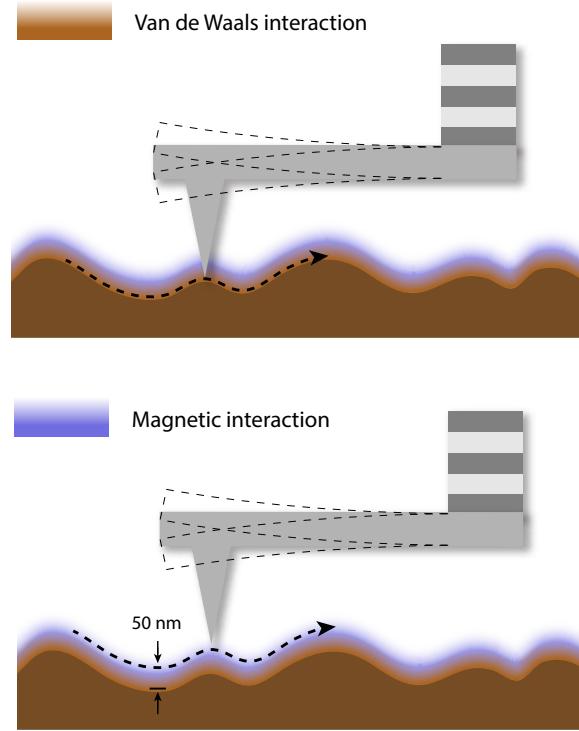


Figure 24: MFM mode. The first scan is close to the sample surface, so that the topographical information can be obtained. In the second scan the tip is lifted to a constant distance from the sample surface, so that the Van der Waals interaction is negligible and only the long-ranged magnetic interaction is measured.

where X_i is the strain tensor, d_{ki} is the piezoelectric tensor and E_k is the electric field. For a tetragonal system,

$$\begin{bmatrix} X_1 \\ X_2 \\ X_3 \\ X_4 \\ X_5 \\ X_6 \end{bmatrix} = \begin{bmatrix} 0 & 0 & d_{31} \\ 0 & 0 & d_{32} \\ 0 & 0 & d_{33} \\ 0 & d_{15} & 0 \\ d_{15} & 0 & 0 \\ 0 & 0 & 0 \end{bmatrix} \begin{bmatrix} E_1 \\ E_2 \\ E_3 \end{bmatrix}$$

When a field is applied in E_3 direction, the resulting non-zero strain terms are $X_1 = d_{31}E_3$, $X_2 = d_{32}E_3$ and $X_3 = d_{33}E_3$. Therefore, an electric field in the c-axis of the crystal will cause elongation in the c-axis and contraction in the other two orthogonal directions, or vice versa. The piezoresponse of sample can bring insights of the sample properties such as carrier concentration[], ferroelectric domain orientation[], domain boundary[], etc. The piezoresponse of the LAO/STO system can be used to characterize the carrier density change on the interface. More details will be discussed in the following section.

In PFM measurement, a conductive tip made of doped silicon or coated with metal is in contact with the sample and a sinusoidal voltage signal is applied to the tip. For a sample with piezoresponse, the electric field will cause a deformation of the sample topography sinusoidal in time. Since the sample is in contact with the tip, the topographical change of sample will cause slight change of tip deformation and the change is recorded by the differential voltage on the quad detector. The sample deformation is usually in the order of 10 pm/V (e.g. 85.6 pm/V for BaTiO₃[]). Therefore, PFM signal is measured with lock-in technique for the best signal-to-noise ratio. As shown in Figure 25, If the sample contract in the direction of the electric field, the piezoresponse signal will be in phase with the electric signal; if the sample elongate in the direction of the field, the signal will be out of phase.

7. LAO/STO nano-device c-AFM lithography

Other than performing surface characterization, the main application of AFM in our lab is to create nano-scale structures on the interface of LAO/STO. As discussed in Section ??, the “water-cycle” mechanism[] and surface protonation[] are central for the reversible AFM lithography on LAO/STO interface. In contact mode, the AFM tip is in direct contact with the LAO/STO sample surface. When a positive voltage is applied to the conductive AFM tip, and $V_{\text{tip}} > +6\text{V}$ [], the electric field from the tip will dissociate the water molecules between the tip and sample surface, and leaves a trace of proton, as the tip scans through the LAO surface.

As shown in Figure 26, the golden regions are a pair of electrodes connected to the LAO/STO interface. An AC voltage of ± 100 mV is applied to one electrode, while the

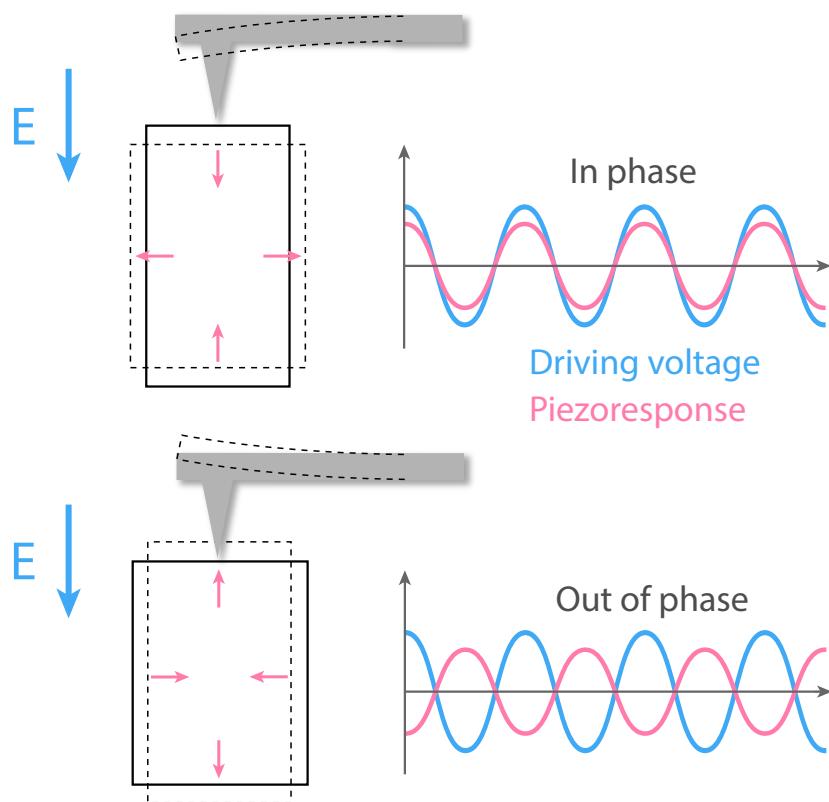


Figure 25: PFM measurement. The external electric field is applied on the sample through a conductive tip. The sample deforms and slightly bend the tip, and the deformation signal is monitored with a quad detector (not shown). If the sample contract in the direction of the field, the deformation signal is in phase with the driving voltage (above); if the sample elongate in the direction of the field, the signal would be out of phase with the driving voltage (below).

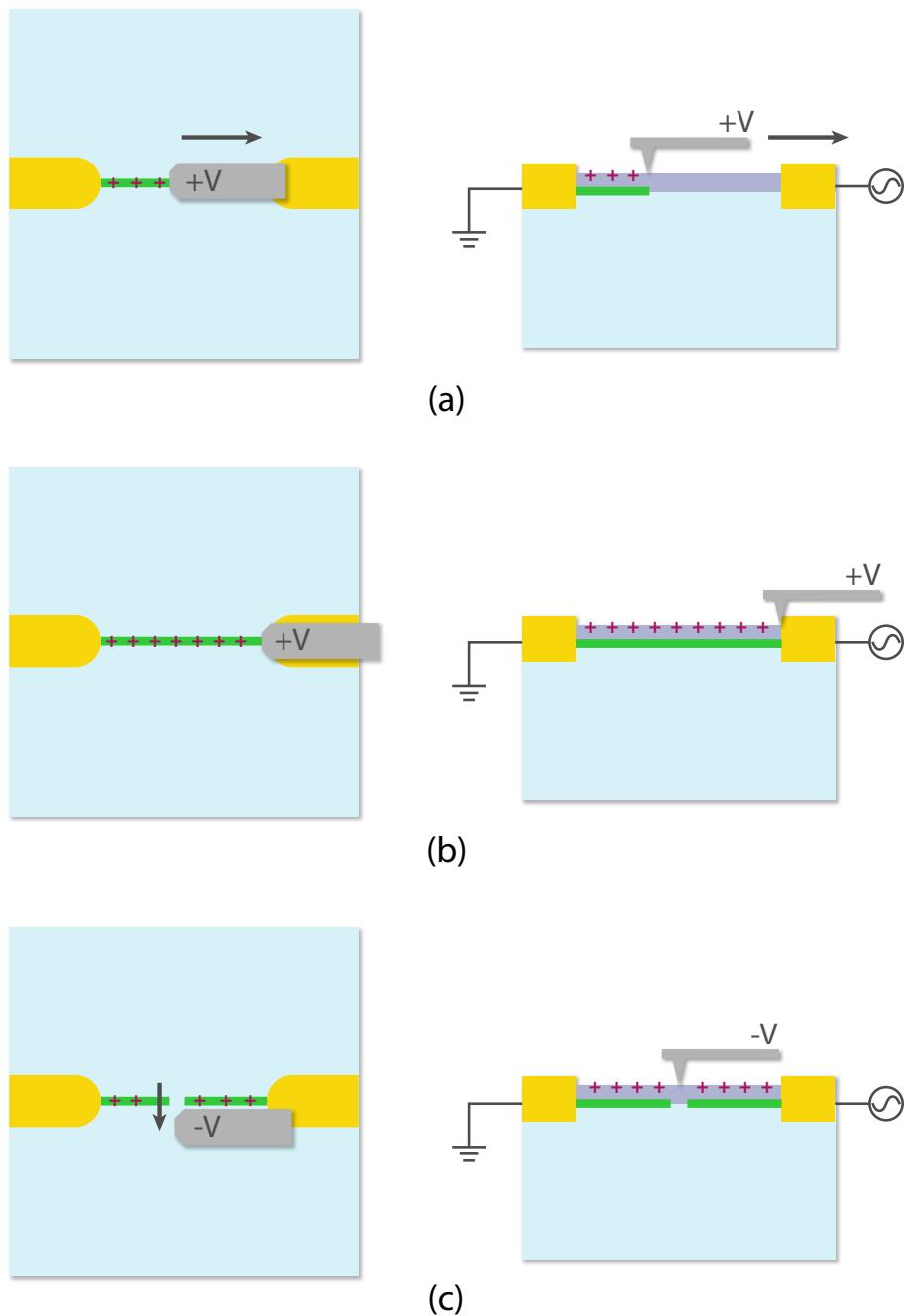


Figure 26: AFM lithography on LAO/STO. (a)(b) The AFM is set in contact mode. A positive bias voltage is applied to the tip. A trace of proton is left on the path of AFM tip, and 2DEG is formed underneath the path. Conductivity of the two interface electrodes are monitored, and a conductance jump will be observed once a closed loop is formed. (c) A negative voltage can remove the proton and cut the nanowire previously written.

other one is grounded. Before the c-AFM lithography starts, the interface between the two electrodes is insulating. The current between them is monitored in real-time as the c-AFM tip scans through the surface (zero before lithography starts). A positive voltage is applied, and protons are dissociated from the water meniscus and left behind the tip (plus signs in the Figure). 2DEG is formed under the protonated region and makes the interface conductive (green region). Once the two electrodes are connected by the conductive channel, a conductance jump can be observed. When a negative voltage on the tip is applied, the protons will be removed from the surface and make the interface insulating again. A typical resistance of the nanowire right after the writing is about $200 \text{ k}\Omega/\mu\text{m}$, and the width is in the same order of magnitude as the c-AFM tip??.

The sample is patterned in such a way that I can do AFM lithography and electrical measurement in real-time. Figure 27 show the pattern of a typical LAO/STO sample for c-AFM lithography. The dimension of the sample is $5 \text{ mm} \times 5 \text{ mm}$. There are two layers of metals. The blue layer is in direct contact with the LAO/STO interface. The sample is first etched with Ar ion mill and the etched areas are backed-filled with 4 nm of Ti and 25 nm of Au (as discussed in II.B.2). The orange regions are for electrical connections and wire-bonding. 4 nm of Ti and 50 nm of Au are directly coated on the LAO surface without etching. The orange crosses on the corners indicate the corners of the sample. the blue crosses and triangle are guidance for alignment. The zoomed-in image shows the region where c-AFM lithography is performed. The patterns are written inside the $20 \mu\text{m} \times 20 \mu\text{m}$ area, and connected with the interface electrodes.

D. ELECTRIC SIGNAL MEASUREMENT

The electrical signal (voltage, current, etc) measured in my experiments are

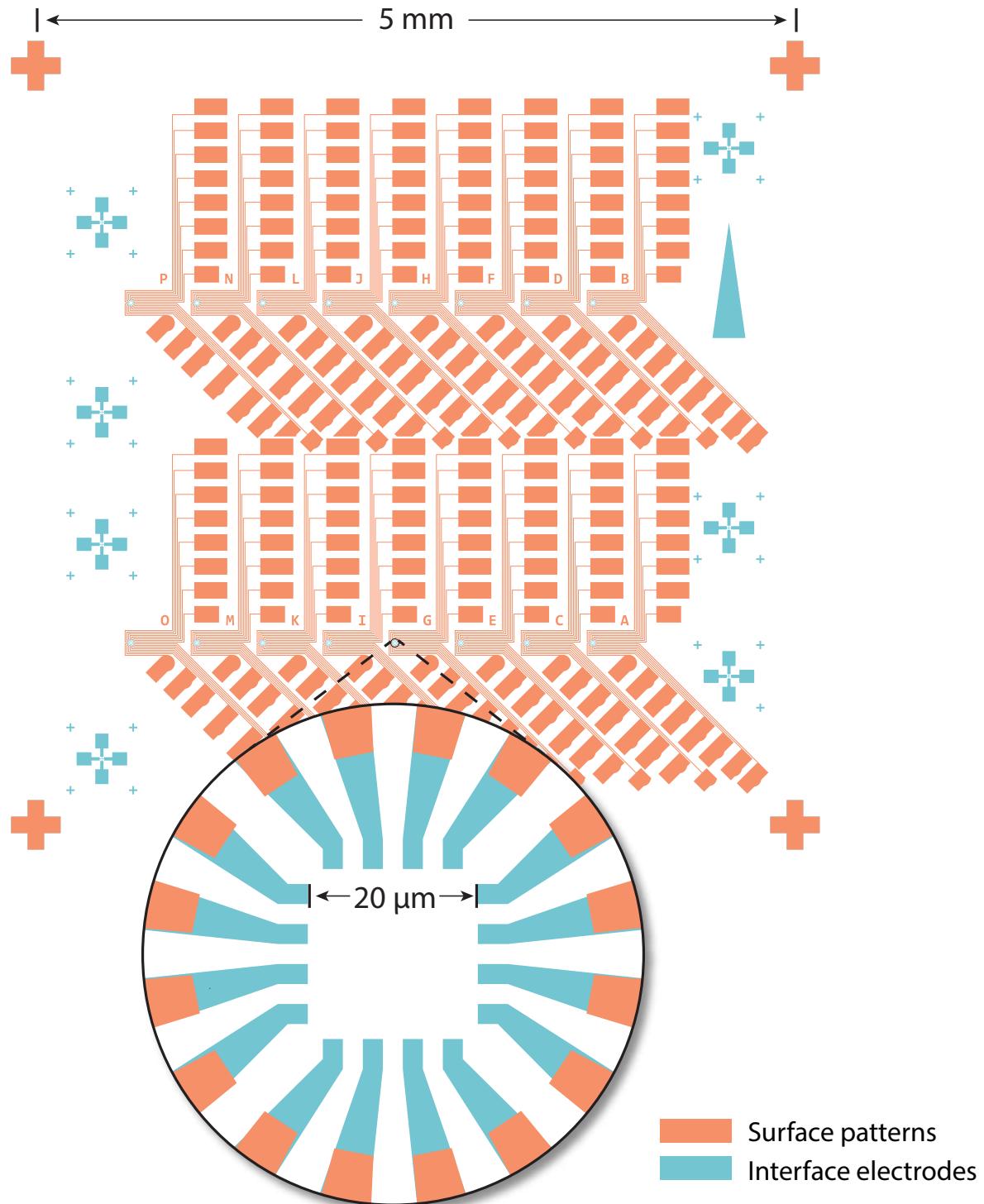


Figure 27: LAO/STO sample pattern for c-AFM lithography. The sample size is 5 mm × 5 mm. Canvas are 20 μm squares.

- 1. Lock-in amplifier**
- 2. Virtual lock-in**
- 3. IV-curve measurement**
- 4. Probe station**

E. GRAPHENE/LAO/STO DEVICE FABRICATION

Current state-of-the-art high quality graphene devices are fabricated from mechanically transferred exfoliated graphene encapsulated with hexagonal boron nitride (h-BN)[?], where the mean-free-path of the electron can exceed the dimension of the device[?] (tens of micrometers). For applications requiring an arbitrary substrate and graphene shape like my experiment, graphene grown from CVD method and transfer in liquid (“wet-transfer”) is preferred. The basic idea of wet-transfer is to use wet chemical etchant such as nitric acid, hydrochloric acid, FeCl_3 or ammonium persulfate (AP) to etch away the metal substrate while the graphene is floating on the liquid surface. The graphene is then scooped out and rinsed in DI-water for several times while floating on water. The substrate is then immersed in the DI-water and used to catch the graphene piece from the bottom. Conventionally, the wet-transfer needs polymers like poly(methyl methacrylate) (PMMA) a scaffold layer to support graphene on the liquid surface before it is transferred onto the substrate[?, ?, ?]. The PMMA is then patterned with deep UV exposure or e-beam lithography, so that graphene can be etched in to the designed shape. In the end the PMMA is cleaned with organic solvent. The procedure of using PMMA to transfer and pattern graphene shown in Figure 28.

The issue with PMMA as transfer medium is that, the residual PMMA is known to be a source of electron scattering, and would significantly reduce the electron mobilities[?, ?, ?]. Annealing the sample in H_2/Ar environment proves to partially remove the residue, but the process can also introduce structural defects to graphene[?], or increase coupling between graphene and the substrate and result in extrinsic doping and deterioration of mobility[?].

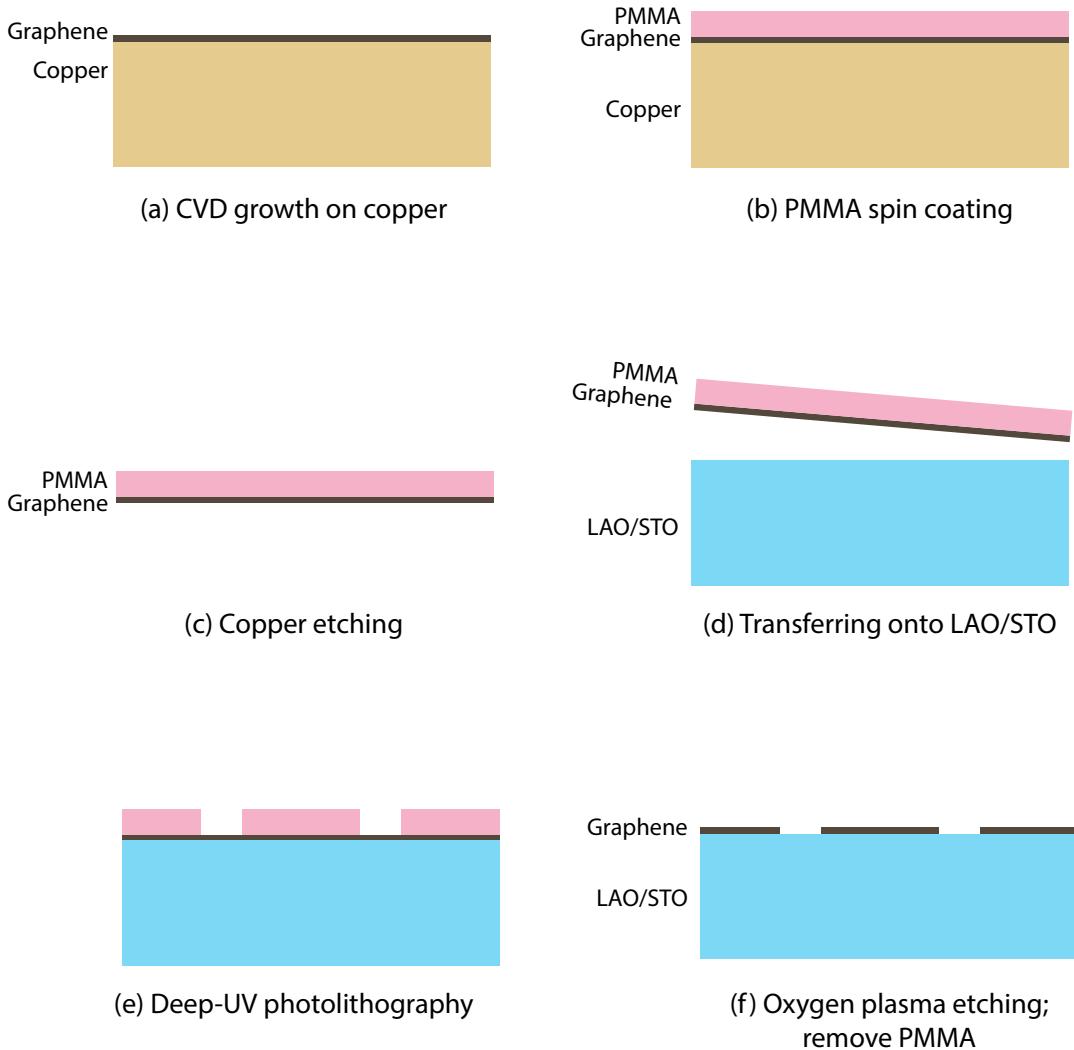


Figure 28: CVD graphene transfer and patterning with PMMA. (a) Graphene is grown on copper with CVD. (b) PMMA is spin-coated onto graphene surface. (c) Copper is etched chemically, while graphene floats on the liquid surface. (d) Graphene is transferred onto LAO/STO, using PMMA as a supporting layer. (d) PMMA is patterned with deep-UV lithography. (f) Graphene is etched with oxygen plasma and then PMMA is removed with organic solvent.

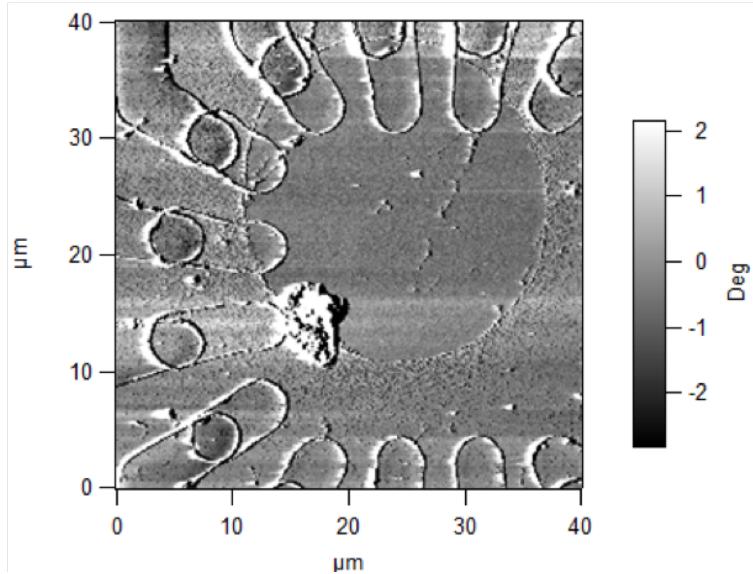


Figure 29: AFM phase image of graphene on LAO/STO, transferred with PMMA. The circular region is graphene. Particles can be seen outside graphene, on LAO/STO surface. Other features are the metal electrodes for graphene and interface contact.

Another drawback of using PMMA as a transfer medium is that LAO/STO substrate is susceptible to PMMA contaminants. Figure 29 shows the AFM AC phase image of LAO/STO with a graphene piece transferred and patterned with PMMA. The inside the circular region is graphene. On the LAO/STO surface, contamination particles can be seen. Many experiments in my project require tuning the 2D electron gas on the interface of LAO/STO with c-AFM (will be discussed in Section II.C). Most of the LAO/STO samples were found to lost the interface tunability after the graphene transfer with PMMA.

A replacement for PMMA is a type of perfluoropolymer Hyflon from Solvay. Our collaborator Brian D'Urso has used Hyflon for hydrodynamic experiment, and suggested it might work as a perfect protection layer for graphene. Hyflon is a type of perfluorinated polymer (similar to Teflon), and is highly hydrophobic and chemically stable. Hyflon has been reported to be widely used in membrane applications such as fuel cells, due to the inertness of C-F bonds[?, ?, ?]. It has also been reported that the Hyflon membrane between graphene

and substrates like SiO_2 can reduce the extrinsic p-type doping in graphene by preventing water molecule adsorption to the dangling bonds on the substrates[?]. It means that, unlike PMMA that leaves residue and deteriorate graphene quality, Hyflon can actually preserve the graphene while used as a transfer medium. Hyflon is also high selectivity to solvent. Most of the organic or inorganic solvent such as acetone, IPA or DI-water cannot dissolve Hyflon, which makes it a perfect protection layer for graphene.

The commercially available Hyflon is in powder form. It is only soluble in a few types of perfluorinated solvent. In my experiment I used FC-40 to make Hyflon solutions. There are different types of Hyflon available, mainly Hyflon AD 40 and Hyflon AD 60, with different molecular weights and phase transition temperature. I have tested both types, and found that graphene transferred with Hyflon AD 60 has higher quality. Although AD 40 has smaller molecular weight and easier to be dissolved, the sample soft-baking temperature is higher than the phase transition temperature of AD 40, and would cause cross-linking of the polymer chains. This makes Hyflon AD 40 hard to be removed and affects graphene quality as a result.

1. Overview of the Hyflon transfer method

The procedure for graphene transfer with Hyflon is similar to the PMMA transfer method. As shown in Figure 30, the graphene on copper is spin coated with Hyflon. The Hyflon film is only 50 nm, therefore a second supporting layer, photoresist, is spin-coated on top of the Hyflon. The copper is etched with ammonium persulphate while graphene with Hyflon and photoresist floats on the surface of chemical. The graphene is then rinsed 4 to 5 times and then transferred onto a pre-patterned LAO/STO substrate, and soft-baked to remove the DI water between graphene and LAO. The photoresist is then removed, and another layer of photoresist is spin-coated. The sample is patterned into Hall bars with standard photolithography. Excessive graphene is etched away with oxygen plasma, along with the Hyflon covering it. The patterned graphene/LAO/STO sample is rinsed with acetone to remove the photoresist, and then Hyflon is removed with FC-40. The particles left on the patterned graphene surface is cleaned by AFM in contact mode scans.

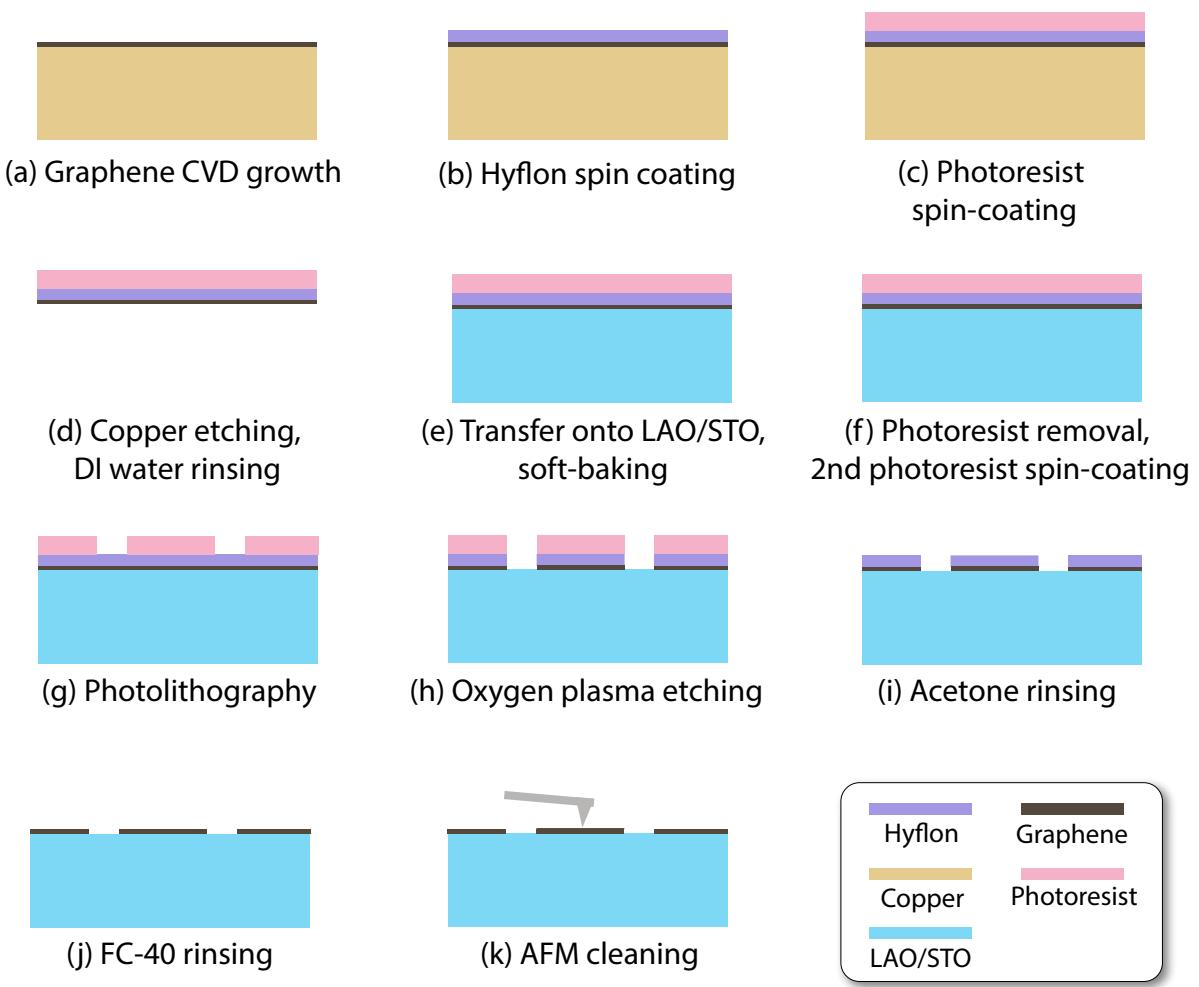


Figure 30: Hyflon transfer and patterning procedure. (a) Graphene is grown with CVD on a copper substrate. (b) Hyflon is spin-coated on graphene. (c) A layer of photoresist is coated on Hyflon as a supporting layer for wet-transfer. (d) Copper substrate is etched with ammonium persulphate; graphene is rinsed in DI-water for several times. (e) Graphene is transferred onto LAO/STO surface. (f) Photoresist for transfer assistance is removed; another layer of photoresist is coated. (g) Standard UV photolithography. (h) Oxygen plasma etching on the excessive graphene. (i) Photoresist removal with acetone. (j) Hyflon removal with FC-40. (k) Residue particle cleaning with AFM.

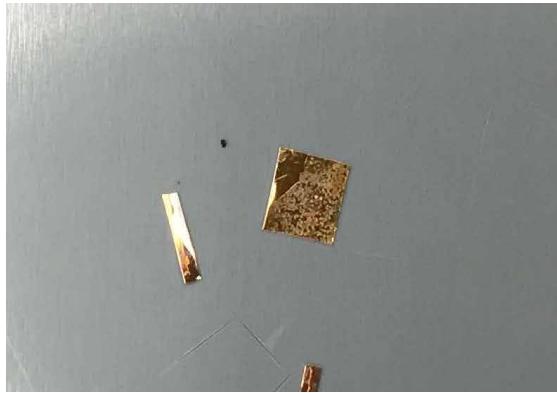
2. Hyflon solution preparation and spin-coating

Hyflon solutions in FC-40 are made by adding 2.5 grams of Hyflon powder into 100 ml FC-40 (nominally 2.5%). The solution is shaken at 100 rpm for 48 hours. Before spin-coated on sample, the Hyflon solution needs go through a series of filters of 450 nm, 200 nm and 100 nm in size to remove any undissolved particles. When spin-coated at 3500 rpm, the thickness of Hyflon film is about 50 nm. In the past I also used 0.5% Hyflon solution and spin-coated at 1000 rpm to make coatings of 50 nm, but it turned out that higher concentration of Hyflon solutions spin-coated at a higher speed would yield more uniform films on graphene samples.

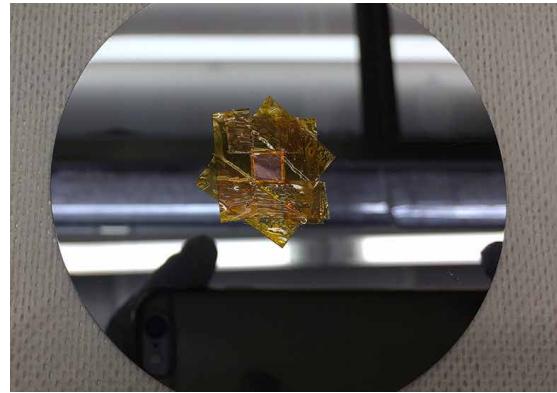
3. Graphene preparation, copper etching and wet transfer

The as-grown CVD graphene is on 100 μm thick circular copper foil. The LAO/STO substrates that graphene will be transferred on are 5 mm \times 5 mm squares. Therefore, the graphene on copper needs to be cut into 5 mm \times 5 mm shape with a razor blade as shown in Figure 31(a). Graphene single domains can be seen in the Figure. The square shape copper foil is then fixed onto a 3" silicon wafer for spin coating, with Kapton tapes, prepared for Hyflon spin coating. The edges of the graphene need to be carefully sealed, so that the Hyflon solution will not leak into the space between the copper and silicon wafer. In the past, I used to leave the edges open, and the Hyflon will be left on the backside of the copper. After copper etching and graphene transferred onto the LAO/STO, the Hyflon on the backside will be trapped between graphene and substrate and impossible to be removed.

Transferring graphene with only the 50 nm Hyflon proved to be very hard. To address this issue, an additional layer of AZ4210 photoresist is spin-coated on Hyflon. However, since the Hyflon is highly hydrophobic, the photoresist solution cannot stick to the surface and form a film of 2.1 μm thick, as it usually does on hydrophilic surfaces. A mild oxygen plasma etching is performed to modified the Hyflon surface and improve surface hydrophilicity. The parameters of plasma etching need to be carefully tested. Etching will not only change the Hyflon hydrophilicity, but also change the solubility of Hyflon in FC-40. In the last step where Hyflon is removed by FC-40 from graphene, if the previous Hyflon surface treatment is too strong, the modified Hyflon can cross-linked and hard to be fully removed from graphene.



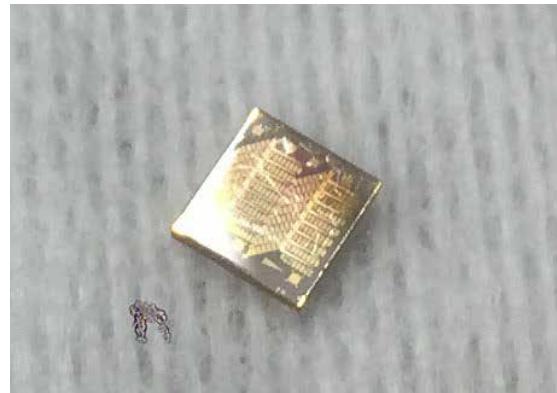
(a)



(b)



(c)



(d)

Figure 31: (a) The graphene with copper foil is cut into squares. The graphene single domains can be seen. (b) copper foil with CVD graphene is fixed onto a silicon wafer as a spin-coating carrier. The edges of the copper foils are carefully sealed with Kapton tape to prevent Hyflon solution leaking into the space between copper and silicon substrate. (c) After the copper is fully etched by ammonium persulfate, the graphene flake with Hyflon and photoresist floats on the surface of the liquid. (d) Graphene transferred onto pre-patterned LAO/STO. Liquid between the graphene LAO is evaporated in an oven or on a hot plate.

In practice, etching the Hyflon for only a few nanometers would drastically improve the hydrophilicity and make photoresist spin-coating possible.

After spin coating, the copper foil is taken off the silicon substrate. 1 mol/L concentration ammonium persulfate ($(\text{NH}_4)_2\text{S}_2\text{O}_8$) DI-water solution is prepared beforehand. Ammonium persulfate is a type of oxidation agent widely used as copper etchant in printed circuit board industry. Compared to other chemicals like ferric chloride, ammonium persulfate does not leave metal ion to graphene, which is critical for experiments on magnetism or transport. The ammonium persulfate solution also needs to go through a 450 nm polymer filter before the copper foil is place on the surface of it. Copper etching usually takes 3-4 hours.



Figure 32: (a) Without copper substrate backside cleaning, the graphene transferred onto LAO/STO has contaminant trapped. (b) The graphene on LAO/STO is much cleaner if the backside is cleaned.

After the graphene transferred onto the LAO/STO, the sample is sometimes found dirty, and the contaminants seemed to be introduced by the transfer procedure. AFM scanning showed that the contaminants were trapped between graphene and LAO. One possibility was that the contaminants were the graphene grown on the backside of the copper. When the copper was etched away by ammonium persulfate, the backside graphene did not sinked

into the etchant, but transferred with the topside graphene onto the sample and got trapped between graphene and LAO.

The *two-step etching* procedure can be used instead. After the copper top surface is coated with Hyflon, it is placed on the surface of ammonium persulfate solution and left inside an ultrasonic cleaner for 15 minutes. While the backside of the copper is been etched, it is also scratched by the ultrasound wave so that the backside graphene can be removed. After 15 minutes, the backside is washed by spraying DI-water on it. The graphene on the topside is protected by Hyflon, and will not be affected by the backside cleaning. Then the copper foil is placed back on the surface of ammonium persulfate. After 3–4 hours the copper foil is fully etched, and the graphene with Hyflon and photoresist would float on the surface, as shown in Figure 31(c). The gray color of the floating flake is from the AZ4210 photoresist. The dark spot on the lower-left corner of the flake is a piece of contaminant.

The graphene flake is then scooped out with e mesh and place on the surface of DI-water, repeated for 4–5 times to make sure the ammonium persulfate is washed off the backside of the flake. Then the LAO/STO substrate is immersed in the DI-water where the flake is floating. The substrate is slowly lifted up towards the flake on the liquid surface, and catch the flake when it comes out of the water. The substrate with the graphene flake on surface is then baked in an oven pre-heated to 50°C, or on a hot plate at 70°C for 5–10 minutes so that the DI-water between graphene and substrate is evaporated and graphene firmly adheres to LAO (Figure 31(d)).

4. Graphene/LAO/STO sample pattern

The metal electrodes and bonding pads on LAO/STO are patterned before graphene is transferred. Therefore, electrodes are making contacts with graphene from below. The patterns are slightly modified from the one in Section II.C.7, so that there are electrodes dedicated for making contacts to graphene. As shown in the zoomed-out image in Figure 33, the graphene Hall bars are located on the canvas for c-AFM lithography. The graphene is in direct contact with the metal electrodes on LAO/STO surface (orange in color), while LAO/STO are in contact with the interface electrodes (blue in color). Therefore, the two

layers can be measured separately.

5. Photolithography and etching

The graphene transferred onto LAO/STO is patterned with standard UV photolithography, as discussed in Section II.B.1. Although there is already photoresist on graphene flake to assist wet transfer, it needs to be washed off and re-coated, because it is exposed to UV and chemical etchant. Unlike the photolithography for LAO/STO electrodes, patterning graphene into Hall bars need to wash off the photoresist outside of the Hall bars and leave the Hall bar region covered with photoresist, so that the graphene is protected from oxygen plasma etching. The oxygen plasma barrel etcher and RIE proved to be effective for patterning graphene. However, other than the Hyflon and graphene, there are usually contaminant introduced by wet-transfer procedure that need to be cleaned with RIE as well (Figure 34).

a. One-step etching Before 2018 I was using the one-step etching method (as shown in Figure 30), that is to etch the graphene and clean the contaminants with RIE in one step. Figure 34 shows the image of a sample before and after the RIE etching. In Figure 34(a), it can be seen that bubbles or foldings introduced from the wet transfer covers the entire sample. Fortunately in my experiments, most of the graphene transferred on LAO/STO needs to be etched away, and only the graphene device inside red circles in 34(a) need to be covered with graphene. A zoomed-in image is shown in Figure 35, with graphene covered with Hyflon. The metal patterns are contact electrodes for graphene and LAO/STO interface. Wrinkles on graphene can be seen, which are following the corrugations on the copper substrate during CVD growth. The patterns for the entire sample are designed in the way that there can be as many graphene devices fit in the sample as possible, so that there are higher chances of finding a clean graphene device. The contaminants are etched away with an aggressive RIE procedure (listed in Table 4).

As shown in Figure 34(b), the single-step RIE etching can completely clean the contaminants on LAO/STO. However, the etching can be problematic for the LAO/STO interface. As shown previously in Figure 33, the graphene Hall bar locates inside the 20 μm canvas

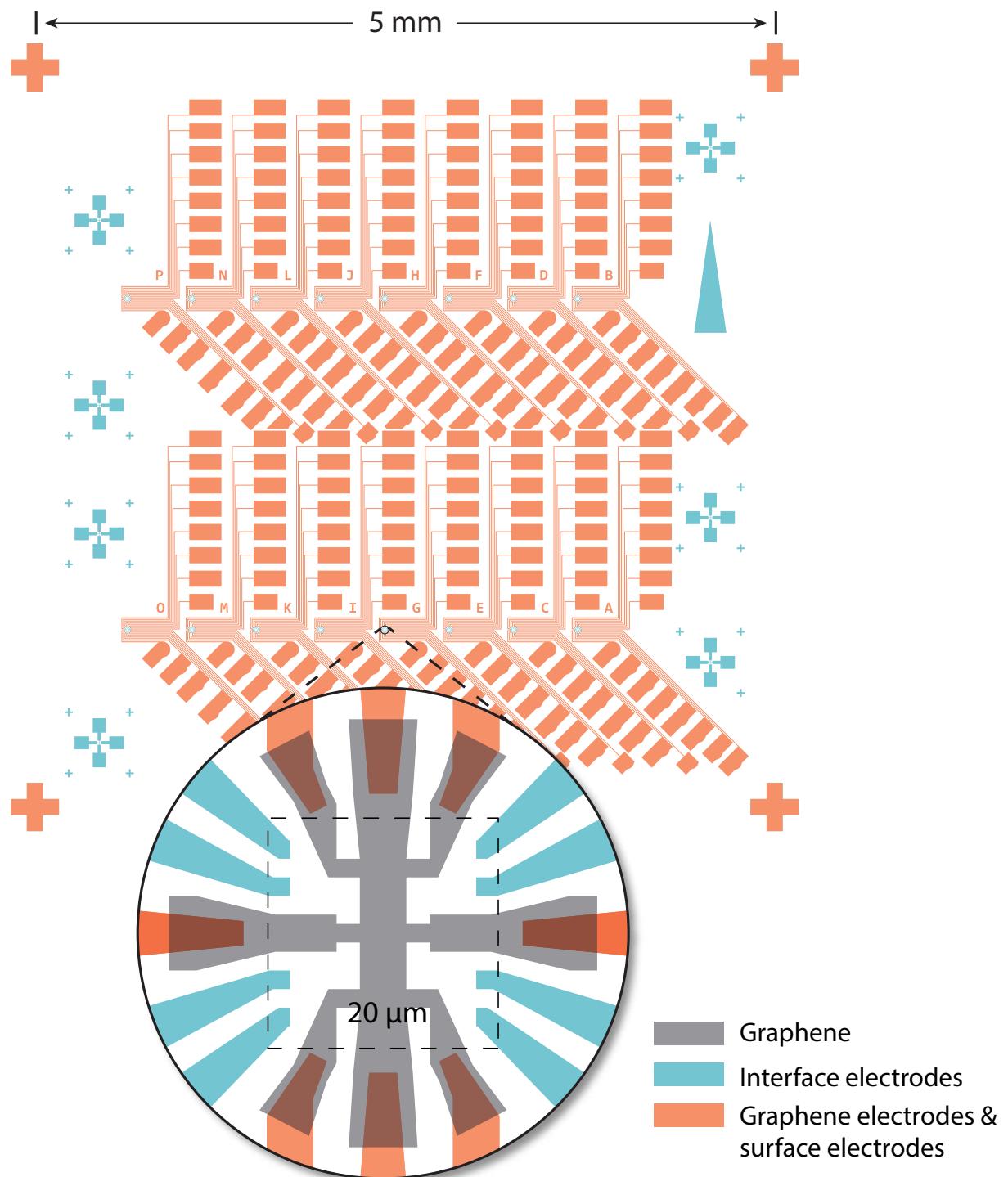
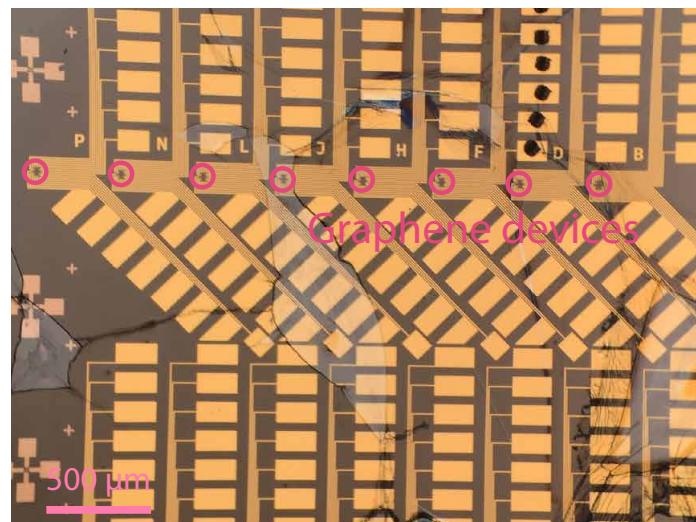
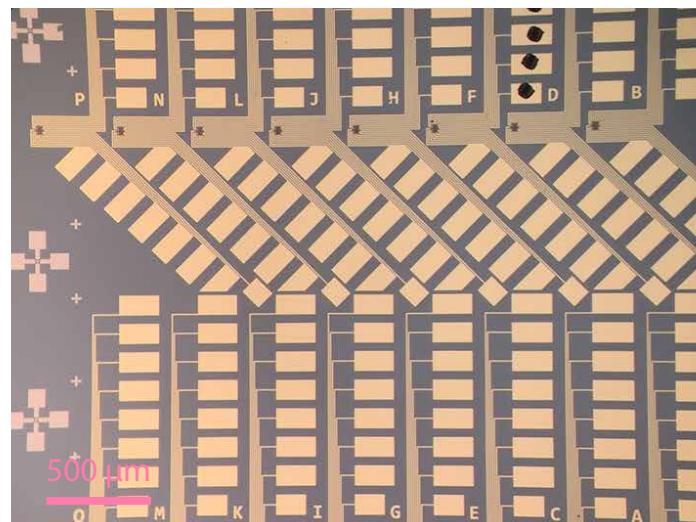


Figure 33: Patterns for graphene/LAO/STO. Two sets of electrodes are patterned separately, for interface and graphene contact.



(a)



(b)

Figure 34: Contaminants can be introduced by wet transfer. The source can be photoresist, copper etchant, or liquid trapped in the bubble formed from transferring. (a) Contaminants can be seen after graphene transfer. Only the graphene inside the red circles (zoomed-in image in Figure 35) need to be protected with photoresist, and rest of the graphene can be removed along with the contaminants by RIE procedure. (b) Sample surface is clean after RIE procedure.

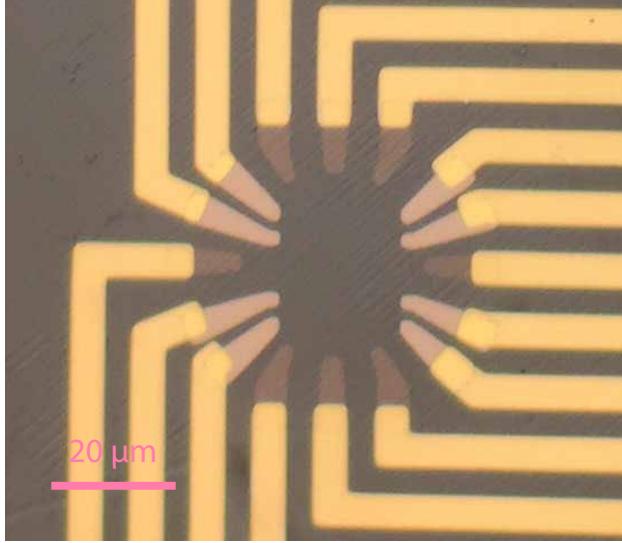


Figure 35: The graphene area of interest on pre-patterned LAO/STO sample. The patterns are for graphene and LAO/STO interface contact.

area. When the graphene outside the Hall bar and surface contaminants are etched with the same aggressive RIE recipe, the bombardment would damage the LAO/STO interface and it would be impossible to write nanowires with c-AFM in some cases. However if I etch the sample with a milder recipe, the contaminants cannot be fully cleaned in some cases.

b. Two-step etching To address the interface-damaging issue, I started to use the two-step etching method. The basic idea is to separate sample cleaning and graphene etching steps. As shown in Figure 36. In the first step of etching, the Hyflon and graphene are protected by photoresist in a square shape, that covers the Hall bar devices to be pattern in the next step and the LAO/STO canvas region. The rest of the sample is etch with an aggressive RIE step so that the contaminants on bonding pads and connection patterns are fully removed. After the etching the photoresist is washed away with acetone and IPA. In the second step, another layer of photoresist is spin-coated, and patterned into graphene Hall-bar device shape. Then the graphene and Hyflon outside the Hall bar region but covering the LAO/STO canvas are etched away with a much weaker oxygen plasma, so that the

Instrument	RIE
Gas	Oxygen
Flow rate	19 sccm
Power	50 W
Pressure	300 mTorr
Time	60 - 180 s

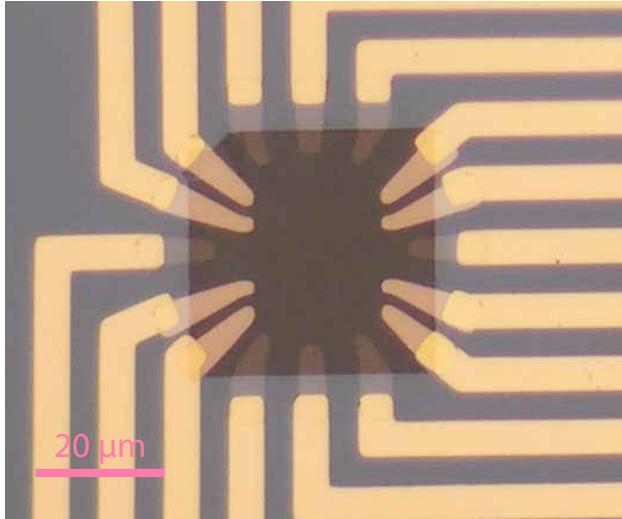
Table 4: One-step RIE etching recipe.

LAO/STO interface will not be damaged.

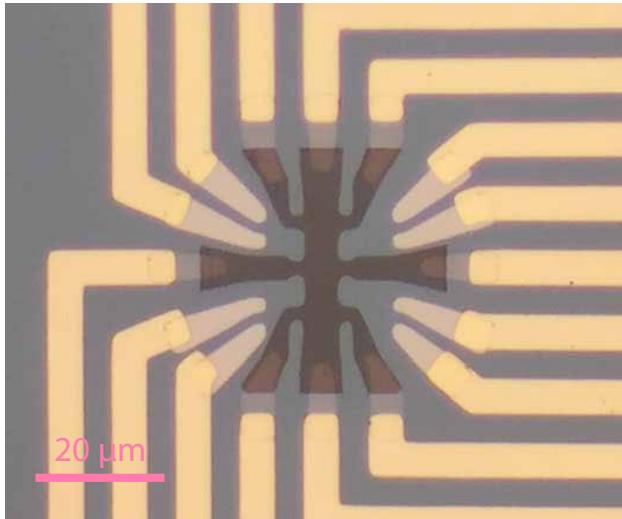
The detailed procedures are shown in Figure 37. Right after graphene is transferred onto LAO/STO and the supporting photoresist is washed off, it is spin-coated with AZ4210. The square-shape is patterned with photolithography to protect the canvas. Then the sample is etched with RIE, until all the contaminants are cleaned. In some cases, the contaminants needs long etching time, and photoresist may be etched through. Multiple photolithography and RIE etching steps will be needed. For the same reason, AZ4210 (2.1 μ m thick) is preferred than AZ4110 (1.1 μ m thick). After that, AZ4210 is washed off, and AZ4110 is spin-coated. Photolithography on AZ4110 has higher precision. One thing I noticed was that the developing time required for AZ4110 is much longer than usual, possibly because the previous RIE etching changed the surface hydrophilicity and make the exposed photoresist harder to be washed off. After the Hall bar is patterned with inverse pattern, the sample is etched with a weak oxygen plasma barrel etching recipe, and shape the graphene and Hyflon into Hall bars (Figure 36). The recipe for photolithography and etching are listed in Table 5 and Table 6.

6. Hyflon removal

Over the entire processing, the graphene is covered with Hyflon until the last step. The Hyflon is only solvable in perfluorinated solvent such as FC-40. On one hand, this makes it



(a)



(b)

Figure 36: Two-step graphene etching. (a) A square shape region is protected by AZ4210 photoresist. Outside of the square region the sample is etched with an aggressive RIE process to remove the contaminants introduced by wet-transfer. The figure shows the graphene with Hyflon. Photoresist has been washed off. (b) A second step of photolithography is performed and a Hall-bar shape region is protected by AZ4110 photoresist. Outside of the Hall-bar the Hyflon and graphene is etched away with a mild oxygen plasma barrel etching recipe, and the LAO/STO interface is still writable. The figure shows the graphene Hall-bar covered with Hyflon.



Figure 37: Two-step graphene etching. (a) AZ4210 is spin-coated on Hyflon. (b) The photoresist is patterned into square-shape protection layer for the canvas. (c) An aggressive RIE process is used to fully remove the contaminants outside the canvas. (a) to (c) might be repeated if necessary. (d) AZ4110 is spin-coated. (e) Hall bar shape is patterned, to protect the Hyflon and graphene on the regions of interest. (f) A weak oxygen plasma etching step is used to remove the unwanted Hyflon and graphene. (g) AZ110 is washed off by solvents. (h) Hyflon is removed by FC-40.

Photoresist	AZ4210 (step 1)	AZ4110 (step 2)
Spin-coating	4000 rpm	4000 rpm
Baking	95 °C	95 °C
UV dose	170 mJ	100-120 mJ
Developer : DI-water	1 : 4	1 : 4
Developing time	120 s	240 - 600 s

Table 5: Two-step etching photolithography recipe. First layer of AZ4210 is used to protect the canvas against the aggressive RIE for contamination removal. Second layer of AZ4110 is for graphene Hall bar protection, and only need to resist mild oxygen plasma etching.

Instrument	RIE (step 1)	Barrel etcher (step 2)
Gas	Oxygen	Oxygen
Pressure	300 mTorr	~ 600 mTorr
Power	50 W	100 W
Time	200 - 400 s	300 s

Table 6: Two-step etching recipe. Although it seems that the barrel etcher is running at higher power, the structure of the instrument make the etching much less invasive compared to RIE.

possible for Hyflon to serve as a protection layer for all the other chemicals like acetone, IPA or photoresists. On the other hand, it also make Hyflon hard to be fully removed. I used two recipe for Hyflon removal.

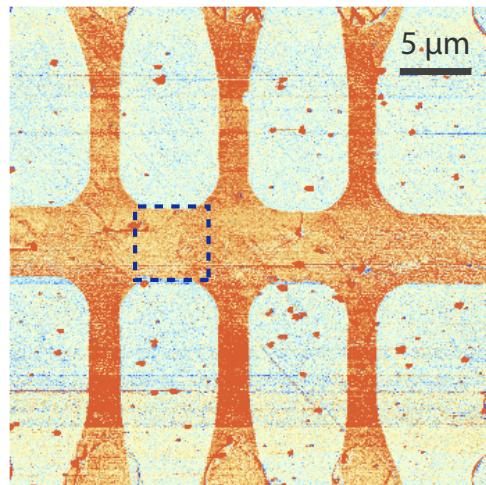
a. High temperature method FC-40 heated up to 165 °C in a beaker. The boiling point of FC-40 is about 175 °C and it decomposes into HF, therefore I keep the temperature lower than that. Once the sample is placed in FC-40, the beaker is taken off the hot place and left on a shaking stage at 100 rpm, and the sample is shaken in FC-40 for 12 hours while it cools down to room temperature. Then the sample is taken out and dried with nitrogen.

b. Low temperature method FC-40 is heated up to 70 °C in a beaker. After the sample is immersed in FC-40, the beaker is shaken at 100 rpm while the temperature is kept at 70 °C for 12 - 24 hours. Then the sample is taken out and dried with nitrogen. I started to use this method since 2017.

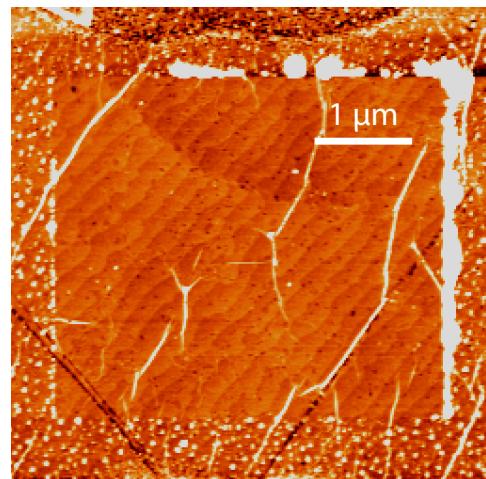
It is hard to determine which method is more preferable. The room temperature method does seem to be less harsh, and might affect the graphene and LAO/STO. However, the residue on graphene is more than the high temperature method. There was a period of time (about a year) when I could not produce high quality graphene/LAO/STO sample. Whether or not it is related to switching from the high temperature method to low temperature method for Hyflon removal is still uncertain.

7. AFM cleaning of graphene

From the past experiences, all the solvents including DI-water, IPA and FC-40 would leave a layer of nano-particles on sample surface, until cleaned by oxygen plasma. For graphene/LAO/STO sample, even a weak recipe of oxygen plasma etching can potentially cause structural damage to the graphene. Therefore, the nano-particles from solvents, especially FC-40, and the residues of Hyflon are cleaned with AFM contact mode scanning, as a final step of processing.



(a)



(b)

Figure 38: AFM cleaning of graphene. (a) The organ Hall bar region is the graphene, after Hyflon removal in FC-40. Particles on graphene can be observed. (b) Zoomed-in image of the square region in (a). The center has been scanned in AFM contact mode. Particles are pushed to the top and right edges. Terraces of LAO/STO and wrinkles can be observed.

Figure 38 shows the AFM images of graphene before and after cleaning. Figure 38(a) is the AFM AC mode phase image. The Hall bar shape in orange is the graphene, and the LAO surface is in cyan. As can be seen, particles of different sizes can be observed. Larger particles (~ 100 nm) are probably from the FC-40, while the smaller particles (~ 10 nm) are the residues of Hyflon. Figure 38(b) is the zoomed-in image of the squared region in 38(a), after the center is been scanned with AFM in contact mode. Particles are pushed to the edges of the square, and it can be seen that the scanning is bottom to up, left to right. Inside the clean region, terraces ($h = 4\text{\AA}$) of the LAO/STO substrate can also be observed. Wrinkles of the graphene are formed during the transfer or AFM contact scan process. Mechanical strength of graphene is high enough[] to withstand the contact scanning, however it is possible that graphene is broken by the AFM tip, especially when the raster scan speed is too fast and large amount of resilient contaminants are present on graphene (and a large contact force has to be applied), or when the AFM tip is scanning from the LAO to graphene from the edge with a large contact force. Typical cleaning recipes with on models of AFM are listed in Table 7, with which the graphene does not break.

AFM model	Asylum MFP3D	Asylum Cypher
Tip spring constant	3 N/m	3 N/m
Deflection set-point	0.05 – 0.5 V	0.1 – 1.0V
Contact force	40 – 400 nN	20 – 200 nN
Scanning speed	10 $\mu\text{m}/\text{s}$	10 $\mu\text{m}/\text{s}$
Line separation	10 – 20 nm	10 – 20 nm

Table 7: Graphene cleaning recipes on Asylum MFP3D and Cypher.

To prove that the sample is clean after AFM cleaning, an room temperature STM measurement is performed. Tunneling currents are measured from graphene surface to a tungsten tip. From the AFM image the graphene atoms can be identified, which proves that the graphene on LAO/STO is atomically clean.

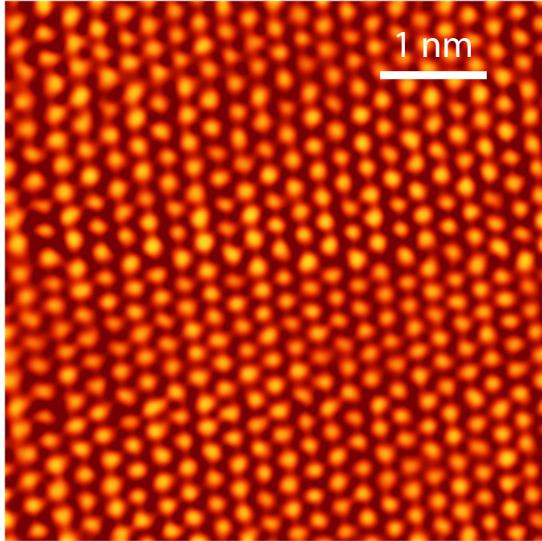


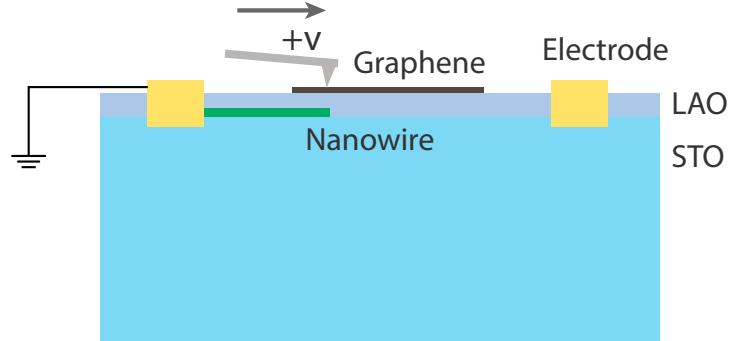
Figure 39: STM image of graphene on LAO/STO after AFM cleaning.

8. Graphene/LAO/STO nano-scale device lithography

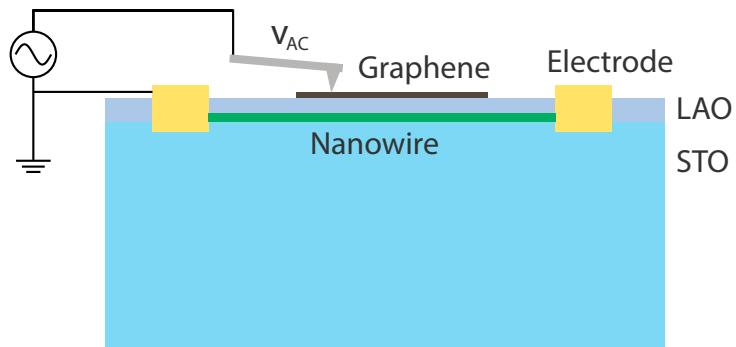
Pattern nano-scale devices on graphene/LAO/STO with reversibly with c-AFM is one of the major techniques I was trying to develop in my research. As been discussed in Section II.C.7, conductive channels can be created and erased on the interface of LAO/STO with c-AFM. Similar experiments have been conducted on graphene/LAO/STO samples.

9. c-AFM lithography and PFM imaging

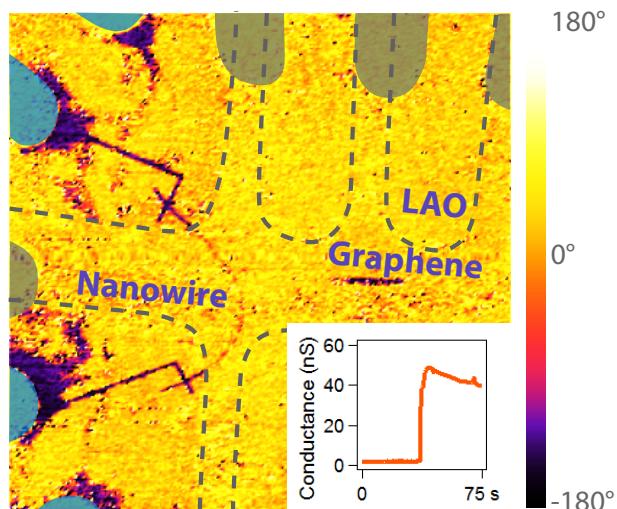
Figure 40 demonstrates the setup of c-AFM lithography on graphene/LAO/STO, and the subsequent PFM imaging of the nanowire underneath the graphene. The setup of c-AFM lithography is similar to bare LAO/STO samples. The interface electrodes is grounded or measure. A positive voltage $V_{\text{tip}} > +6V$ is applied to the c-AFM tip, while the tip sketches between the two interface electrodes. A protective resistor of $1 \text{ G}\Omega$ is connected in series with the tip and voltage source (not shown in Figure 40). The graphene is disconnected and floating in this experiment. The tip goes through the graphene covered area before two interface electrodes are connected. The typical voltage applied to the AFM tip is $+15 \text{ V}$, and



(a)



(b)



(c)

Figure 40: (a) c-AFM lithography of graphene/LAO/STO nanowire. (b) PFM measurement setup. (c) PFM image of the nanowire on LAO/STO interface, underneath the graphene. The inset shows the sudden jump of conductance after the two interface electrodes are connected.

the tip moving speed is $0.5 - 1 \mu\text{m}/\text{s}$, but can be as high as $10 \mu\text{m}/\text{s}$ in some cases. Contact force is about 20-80 nN. A sudden increase of conductance between two electrodes can be observed (inset of Figure 40(c)), indicates that a conductive channel has been created.

A subsequent PFM measurement is performed. An AC voltage (with modulation frequency f close to the PFM resonance frequency of the sample) is applied to the c-AFM tip, while the tip scans in contact with the sample. The alternating voltage from the tip modulates the carrier density on the LAO/STO interface, and the carrier density change will cause deformation of STO through the Jahn-Teller effect[1]. The area that previously written with nanowire would respond differently to the AC tip voltage, causes contrast on the PFM image. Figure 40(c) shows the PFM phase image of the nanowire written. The region colored in blue are the interface electrodes, and the region in gray are electrical contact with graphene. The graphene covered regions are enclosed with dashed lines. The narrow path in dark purple color is the nanowire previously written with c-AFM. The path under the graphene regions is dimmer than the outside sections, indicating the shielding effect of graphene against both the c-AFM lithography voltage and the PFM AC voltage on tip.

10. More about the c-AFM writing conditions on graphene and damage thresholds

The graphene on LAO/STO makes the c-AFM lithography more complicated than on bare LAO/STO. As discussed in Section II.C.7, the lithography and erosion of nanowire on the interface relies on the water-cycle and protonation induced by c-AFM tip voltage. It was also found that the surface conditions of LAO can affect the interface 2DEG[2]. Graphene on LAO also changes the critical thickness of LAO/STO[?].

The LAO/STO is fairly robust under the writing and erasing with c-AFM. The graphene however, can be damaged by a high negative voltage on the tip, from the anode oxidation effect[3]. It was reported[4] that the graphene damaging threshold voltage on the c-AFM is about $V_{\text{damage}} \approx 6V$. This voltage is dependent on whether the graphene is floating or grounded and how much current is flowing between the tip and graphene. Figure 41 is an

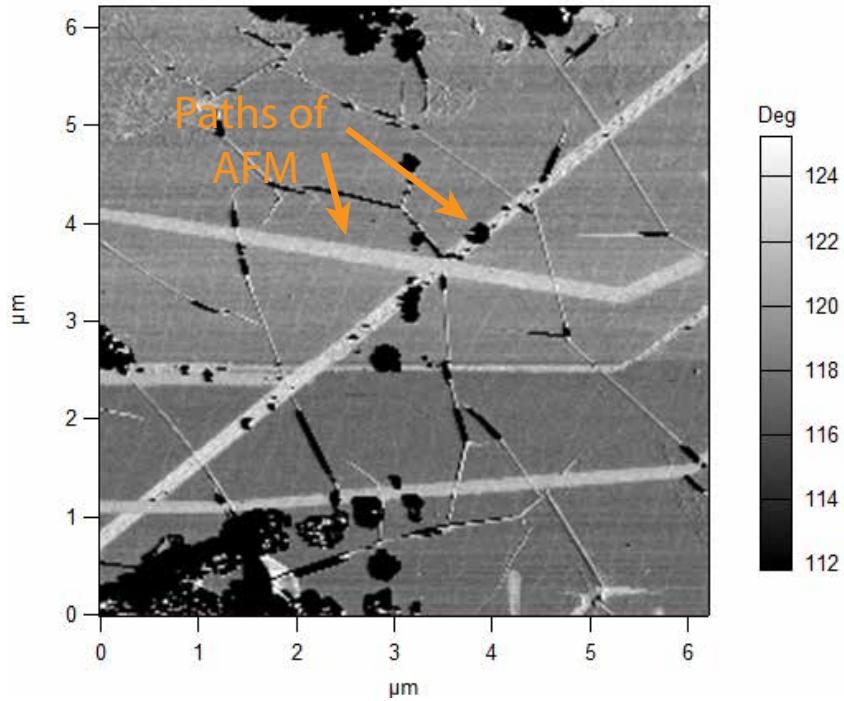


Figure 41: Damages on graphene from c-AFM lithography. On the AFM paths the graphene is oxidized and show contrast on the phase image.

AC phase image of damaged graphene. The light color paths are from AFM sketching with -15 V on the tip, at 400 nm/s speed and 75 nN contact force. The contrast clearly shows the oxidation on graphene. The upper and lower regions of the graphene are electrically disconnected. To avoid oxidation damage to graphene, the tip voltage is limited to -5 V when the graphene is grounded, and -8 V when the graphene is floating while performing c-AFM on graphene with a negative voltage. From my experience the damaging thresholds are also related to the AFM tip condition or graphene surface roughness, but in general under these voltages the graphene damage can be avoided. The AFM lithography parameters are listed in Table 8.

	Writing	Erasing
Tip spring constant	3 N/m	3 N/m
Deflection set-point	0.05 - 0.5 V	0.1 - 1.0 V
Tip voltage	+10V to +20V	-5V (graphene grounded) to -8V (graphene floating)
Contact force	80 nN	80 nN
Tip speed	0.4 - 10 μ m/s	0.4 - 10 μ m/s

Table 8: c-AFM writing and erasing parameters on graphene/LAO/STO samples.

III. GRAPHENE P-N JUNCTION EDGE-STATE ENGINEERING

A. TRANSPORT MEASUREMENT IN GRAPHENE

1. Charge-neutrality point
2. Temperature dependence
3. Carrier densities and mobilities
4. Quantum Hall effect in graphene

B. P-N JUNCTION ON GRAPHENE/LAO/STO

1. Graphene edge channels and p-n junctions
2. Fabricating p-n junction with c-AFM lithography
3. Measurement and results

IV. GRAPHENE/LAO/STO SUPERLATTICE DEVICE

A. GRAPHENE BAND STRUCTURE ENGINEERING WITH SUPERLATTICE

B. C-AFM LITHOGRAPHY OF GRAPHENE/LAO/STO SUPERLATTICE

1. Measurement and results

V. MAGNETO-OPTICAL KERR EFFECT ON LAO/STO INTERFACE

A. MAGNETISM ON LAO/STO INTERFACE

B. MFM MEASUREMENT

C. OPTICAL SETUP

D. RESULTS

VI. OUTLOOK

VII. CONCLUSIONS

BIBLIOGRAPHY