











	Α			В		С	D			E	
				REVISION HISTORY							
	VER	DATE	DESIGNER	DESIGNER CAUSE		DESCRIPTION					
	RFQ	2023-06-30	Zhongxian Su	First version		First version					
1	VER_P1_A	2023-07-12	Zhongxian Su	The Layout does not meet the req	quirements	Some resistors are changed to 0402 package CS_C10_C15_C45_C65_C123_C131 28022C-040007 10 BT 507 +240 0402 28022C-040007 10 BT 507 +440 0402 28022C-04000					
	VER_P1_B	2023-07-13	Zhongxian Su	Adjust LPD according to customer	r needs	1:Delete the original LPD circuit 2:Update LPD					
	VER_P1_C	2023-07-14	Zhongxian Su	Meta and CPS team review		1. Update the network label 2. Update the UART net 3. Update the UART net 4. Update the NurCs power to control 4. Update the NurCs power to control 5. Update the NurCs power to control 6. Update the NurCs power to control					
	VER_P1_D	2023-07-15	Zhongxian Su	Meta team review		1:Opdate V 375 from 5V to 3.3V, CF8801 Vin to FVIN 2:Added CF8801 Program point 3:VRSUS Changed from 83D to 5V8					
	VER_P1_D	2023-07-16	Zhongxian Su	Meta team review		1: Add 1M Revision Detect 2.0F0 Output P-FET? BC cap remove 2.1					
	VER_P1_E	2023-07-18	Zhongxian Su	Meta team review		BTO RIOSChange 5.1k to 10k NT/ RIOSChange 2.7k to 5.1k 11 MC shares one 12C, and the left and right addresses of the MC are added 2.2nd 8.1k to 5.1k					
	VER_P1_E	2023-07-22	Zhongxian Su			ESD1/ESD2/ESD3/ESD4/ESD13/ESD14 change 0603-R to DFN1006-2					
2	VER_EVT_A	2023-10-14	Zhongxian Su	PCB LAYOUT optimization	1 2 3	2. Remove the excess poll-up resistor position of IAC, N74, N75 2. Remove the NF3 / NF4 connector; 1. Remove the single point connection GMD resistor of R150, and directly use the PCB wire single point GMD connection 4. Remove R142, N140 1. Remove R142, N140 1. Remove R142, N140					
					6 9 8	. MINISES CAMPAIN OF FOWER UNA AND SIGNAL NOW OPERATION . 6. The GHD OF the LED was changed from the Signal GHD to the GHD 7. Remove R151 / R152, and use wireless charging single point grounding through R153 / R154. 7. Change GAPCIG to On Add GIT/L108 with 6 ligs.					
				1.LED UX control logic optimization 2.LED brightness adjustment	8	1.078 2.7% to Nc, Ri44 Nc to 2.48; 1.Rilli ik to Nc, Ri45 Nc to 680 chm; 1.Rilli ik to Nc, Ri45 Nc to 680 chm; 1.Rilli 2.7% to NC, Ri46 Nc to 2.4%; 1.Rilli 2.7% to NC, Ri46 Nc to N					
•	Since the output voltage of the wireless tharging Rv varies from 2.8V to 4.7V, it is accessary to adjust the wireless charging poltage gain Grov. **R2 charge from 1.5% to 1.8K, voltage regulating range from 3.8V-11.1V change to 2.9V-8.78V; accessary to adjust the wireless charging poltage gain Grov. **R2 charge from 1.5% to 1.8K, voltage regulating range from 3.8V-11.1V change to 2.9V-8.78V; accessary to adjust the wireless charging poltage gain Grov. **R2 charge from 1.5% to 1.8K, voltage regulating range from 3.8V-11.1V change to 2.9V-8.78V; accessary to adjust the wireless charging poltage gain Grov. **R2 charge from 1.5% to 1.8K, voltage regulating range from 3.8V-11.1V change to 2.9V-8.78V; accessary to adjust the wireless charging poltage gain Grov. **R2 charge from 1.5% to 1.8K, voltage regulating range from 3.8V-11.1V change to 2.9V-8.78V; accessary to adjust the wireless charging poltage gain from 1.5% to 1.8K, voltage regulating range from 3.8V-11.1V change to 2.9V-8.78V; accessary to adjust the wireless charging poltage gain from 1.5% to 1.8K, voltage regulating range from 3.8V-11.1V change to 2.9V-8.78V; accessary to adjust the wireless charging poltage gain from 1.5% to 1.8K, voltage regulating range from 3.8V-11.1V change to 2.9V-8.78V; accessary to adjust the wireless charging from 1.5% to 1.8K, voltage regulating range from 3.8V-11.1V change to 2.9V-8.78V; accessary to 3.8V-11.1V change from 3.8V-11.1V c										
				1.Power optimization 2.ESD performance improvement 3.PCB version change	1 2 3	1.U2 (CCC 5) pin U2 connected to U5 (CF88601) pin12, intermediate series R158 (0 cbm) and added R156(4.78) pulled up to VSVS. 2.U2 (CCC 5) pin SP connected to U7 (CF88601) pin12, intermediate series R159 (0 cbm) and added R157(4.78) pulled up to VSVS. 3.ESD13/ESD14/ED0 charge from Dem to S1540H1-24V to AU2421P1. 4.R148 charge from Dem to S1540H1-24V to AU2421P1.					
	`			EMC performance improvement	122	1.84, M85, M18, M10 charge from Nt to Ochar 2.055,C77,C10,(11) charge from Nt to 33mF; 3.Add C127,C128,(2.2mF) at the UFF_V80S wire to GND;					
3				When Moku power down, HMD will try Moku DFP port. Which cause HMD alwa consuming power.	OTG to a	hdd Q6/Q7(N-MOSFET N_ZSK1018) in series on the CC line of the DFF port.					
				Cost down	12	1.08 charge from BECT2001 to NC, and R149 charge from NC to 0 ohm; 2.202 charge from BET52018S to NC					
	VER_EVT2_A	2023-12-19				EVT2 version					
4							F		N	1	4
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								2. 符合通力《公司环境管 ·動 乘 标准 服务、们不明子	AV R&D	Approveg*Alan Xiso>	Date g2023-10-14
								展 Ro H型。 0	Model No: <model no:<="" td=""><td>Sch VergMOKU_SCH_FROM_TONLY_</td><td>VERSEW BA220231218</td></model>	Sch VergMOKU_SCH_FROM_TONLY_	VERSEW BA220231218
L	A			В		c	D	商足无倉要求 不含有等苯二甲 開散	Titlee «Page Name»	PCB Verg-46-3000000-3000000-	Sheet: 8 of 7
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