











	A	4		В		С		D			E		
					REVI	SION HISTORY							
	VER DATE		DESIGNER CAUSE			DESCRIPTION							
	RFQ	2023-06-30	Zhongxian Su	First version		First version							
1 _	VER_P1_A	2023-07-12	Zhongxian Su	The Layout does not meet the re	equirements	Some resistors are changed to 0402 pack: C9,C10,C19,C45,C88,C123,C131 280202-000087 10 NF 50V +/-10% 0402 ->280101-000024-1 10NF 25V +/-20% 0201	age					1	
	VER_P1_B	2023-07-13	Zhongxian Su	Adjust LPD according to customer needs		1:Delete the original LPD circuit 2:Update LPD							
	VER_P1_C	2023-07-14	Zhongxian Su	Meta and CPS team review		1.Update the network label 2.Update the UART net 3.Update the Zener voltage form 15V to 1 4.Update the NTC's power to control 5.Update V.SYE from 3.3V to 5V, CPE8600	8V Vin, CRS8600 pin define						
$H_{\underline{}}$	VER_P1_D	2023-07-15	Zhongxian Su	Meta team review		1:Update V SYS from 5V to 3.3V, CPS8601 2:Added CPS8601 program point 3:VBUS Changed from ESD to TVS 4.BUCK FOR HMD U3 Change SY8386A to MP.							
	VER_P1_D	2023-07-16	Zhongxian Su	Meta team review		1:Add HW Revision Detect 2.DFP Output P-FET's DS cap remove 3.Adjust NC Downer supply range 2.Adjust NC Downer supply range 3.Adjust NC Downer supply range 3.							
	VER_P1_E	2023-07-18	Zhongxian Su	Meta team review		R70 / R105change 5.1K to 10K R71 / R106change 2.7K to 5.1K 1: WC shares one I2C, and the left and : 2:add R149	right addresses of the WC a	are added					
	VER_P1_E	2023-07-22	Zhongxian Su			ESD1/ESD2/ESD3/ESD4/ESD13/ESD14 change 0603-R to DFN1006-2							
2 -	VER_EVT_A	2023-10-14	Zhongxian Su	PCB LAYOUT optimization		1. Remove the excess pull-up resistor position of I2C, R74, R75 2. Remove the XP3 / XP4 connector; 3. Remove the single point connection GND resistor of R150, and directly use the PCB wire single point GND connection 4. Remove R142, R140							
						b. Wireless charging of Power GND and Sig 6. The GND of the LED was changed from the 7. Remove RI51 / RI52, and use wireless c 8.Change C68/C104 to NC.Add C172/C108 with	e Signal GND to the GND harging single point ground: n 0.1uF.	ing throu	gh R153 / R154.				
				1.LED UX control logic optimization of the control logic optimization of t	on	i.R77 iK to NC, R143 NC to 680 ohm; 2.R78 2.7K to NC, R144 NC to 2.4K; 3.R111 1K to NC, R145 NC to 680 ohm; 4.R112 2.7K to NC, R146 NC to 2.4K; 5. Adjust the resistor package from 0201	to 0402						
_				Since the output voltage of the w charging Rx varies from 3.8V to 4 necessary to adjust the wireless voltage gain Gvv.	vireless .7V, it is charging	6.Delete R77,R78,R111 R112 1.R72 charge from 1.5K to 1.8K, voltage regulating range from 3.8V-11.1V change to 2.9V-8.78V; 2.R107 charge from 1.5K to 1.8K, voltage regulating range from 3.8V-11.1V change to 2.9V-8.78V;						•	
				1.Power optimization 2.ESD performance improvement 3.PCB version change		1.U2 (CCG 5) pin G2 connected to U5 (CPS8 2.U2 (CCG 5) pin B7 connected to U7 (CPS8 3.ESD13/ESD14/ZD4 charge from ESD5401N-24V 4.R148 charge from 0ohm to 5.1K.	7 to AU2421P1.	ries R158 ries R159	(0 ohm) and added R15 (0 ohm) and added R15	6(4.7K) pulled up t 7(4.7K) pulled up t	o VSYS. o VSYS.		
				EMC performance improvement		1.R84,R85,R118,R119 charge from NC to Ooh 2.C65,C77,C101,C113 charge from NC to 33n 3.Add C127,C128,(2.2nF) at the UFP_VBUS wi	n; F; re to GND;						
3				When Moku power down, HMD will try Moku DFP port. Which cause HMD all consuming power.	y OTG to ways	Add Q6/Q7(N-MOSFET N_2SK3018) in series on	the CC line of the DFP po	rt.				3	
				Cost down		1.Q8 charge from BRCS2301 to NC, and R149 2.ZD2 charge from BZT52C18S to NC	charge from NC to 0 ohm;						
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									□1.混 SON Y ●原: (●□型 适 用 のは):使用 \$2\$-000年中提的 11級 环 (個)数	# -	Designe <zhongxian su⊳<="" td=""><td>Dates Thursday, November 02, 2003</td></zhongxian>	Dates Thursday, November 02, 2003	
									0 再生 树脂-被獾线树脂 须 从 紫 尼 的 的 伙件认定例应商级 购	***	Check: <bingkun su=""></bingkun>	Date@2023-10-14	
									2. 符合通为《公司两维 管 珊瑚·原标·准 要求。但不限于: 選 RoHS2. 0	Model No:	Арр. Отов	Date g2023-10-14	
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									■ 満足无喜要求 ■ 不含有邻苯二甲 配数	Title # <page name=""></page>	PCB Verg:40-X00000X-X0000X-	Sheet: 8 of 7	
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