

MX8ULP-EVK9SOM (52006)

MX8ULP-EVK9SOM Board Errata

Rev. 0.1 -- 18/Aug/2023

Errata

This document lists and describes all known errata for the MX8ULP-EVK9SOM. It also provides available workaround for each erratum with detailed explanation, wherever required.

Revision	Date	Description
Rev. 0.1	Aug/18/2023	Initial Release

The table above lists the revision history of this document.

Table 1. Revision history

This table below summarizes all known errata and their workarounds for the MX8ULP-EVK9SOM

Table 2. MX8ULP-EVK9SOM board errata summary

Erratum	Applicable PCB revision	Fixed Plan on schematic/ PCB revision
E-001: SPI Flash U14 doesn't work	Rev. B	Rev. B1/Rev. B

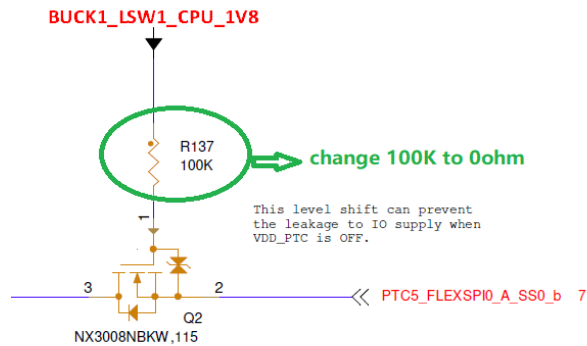


E-001: SPI FLASH U14 doesn't work.

Description

SPI FLASH access failed

It is caused by the high value(100K) of R137



Impact

SPI FLASH U14 doesn't work in rev B boards

Workaround

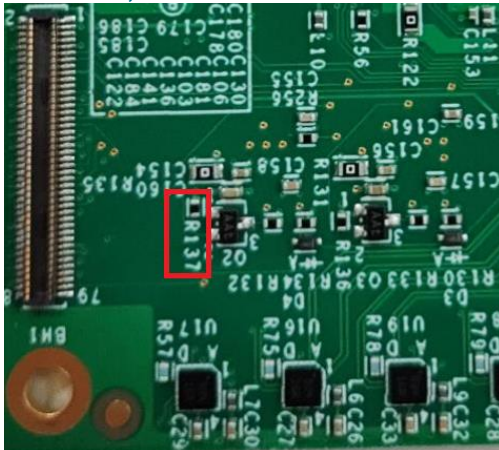
Replace R137 with 0ohm/0402 resistor

Rework Detail:

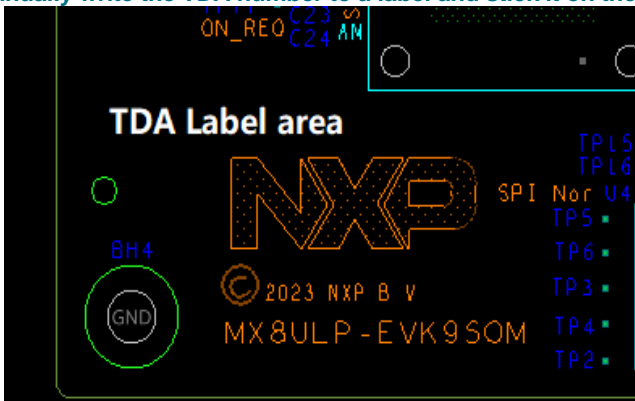
1. Prepare a 0ohm/0402 resistor
2. Before to do rework, please follow step 1~5 in section "Test the rework" to verify board can enter uboot normally. If yes, continue below steps:
3. Make sure remove the power adapter and USB cable from board.
4. Pull out the DUT board from BB board softly as the red square show



5. On bottom side of DUT, mount R137 with a 0ohm/0402 resistor



6. Manually write the TDA number to a label and stick it on the NXP logo area

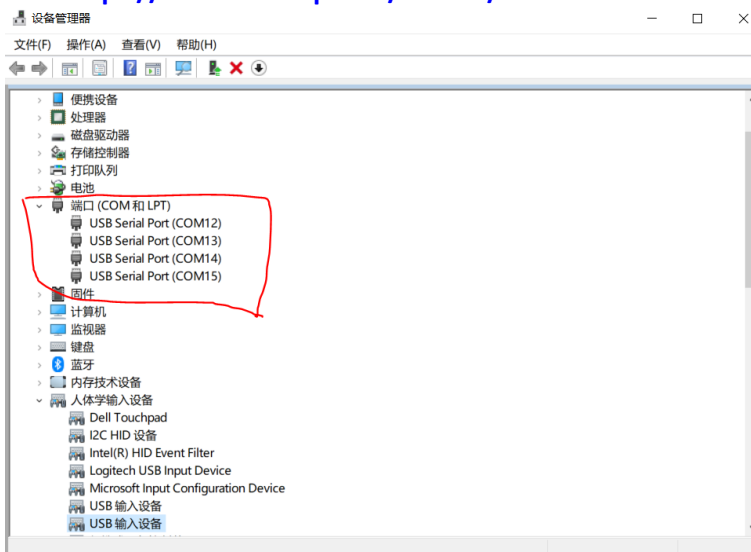


Test the rework:

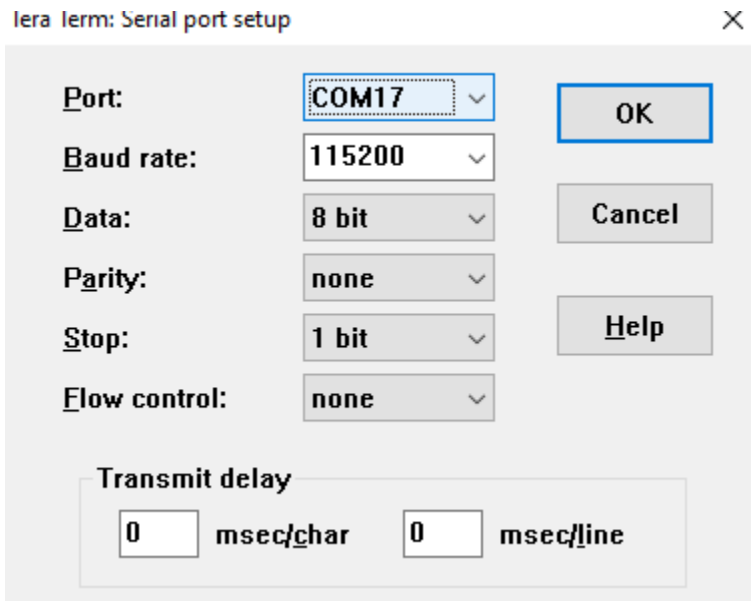
1. Plug the DUT board to BB board softly(there are 3 connector in X&Y directly, align them first before pressing it)
2. Plug micro B USB cable to J17 and the other side to laptop



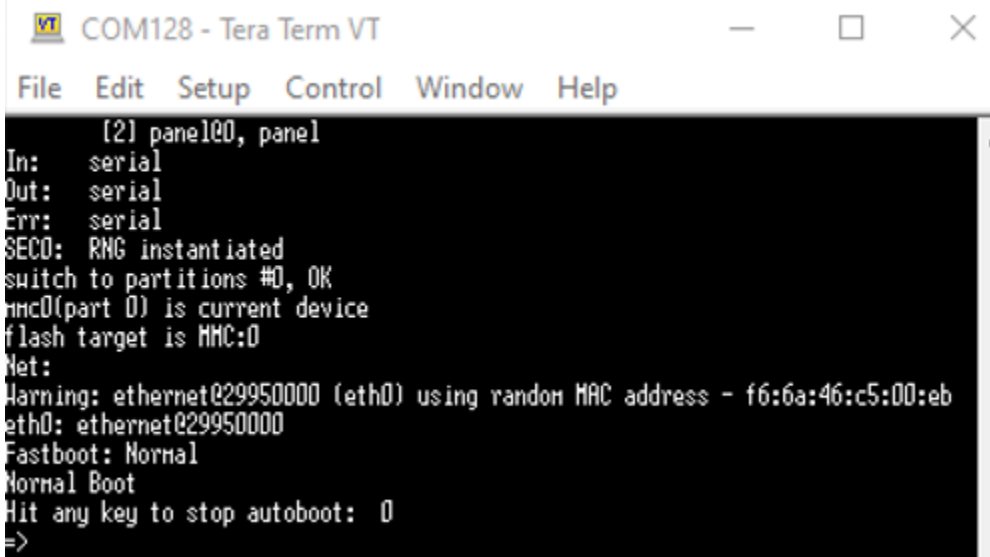
3. Open the device manager, four com ports will display, as below showing. Notes, the port number may vary; It will install the driver automatically for the first time; If the driver is not installed successfully, try to download from <https://www.ftdichip.com/Drivers/VCP.htm> and install it manually.



4. Open the Tera Term or other terminals, open a new window and select the third com port in above showing, set baud rate 115200.



5. Plug the 5V power adapter to the P1 connector, turn on SW10, the Tera Term window will print out the log, Press any key to stop and enter uboot mode as below showing.(If not showing, recycle SW10)



6. Enter the command "sf probe 0:0"
The rework passes if it return "SF: Detected gd25lx256e with page size 256 Bytes, erase size 4 KiB, total 32 MiB"

Please contact jingbo.yu@nxp.com for any question.