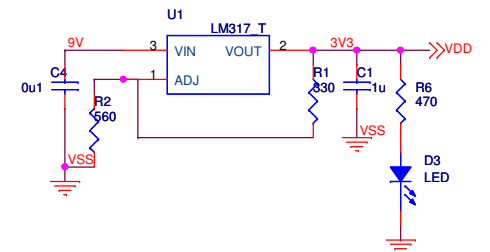
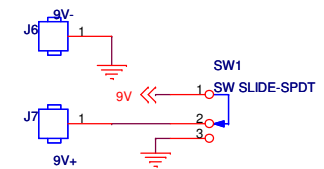
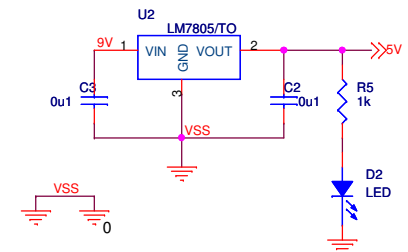


J6, J7 may be DC socket, 9V battery clip, or accessible pins to connect DC supply  
May also have multiple options available for flexibility



R1, R3 may be replaced with 470, 820 respectively if 330 or 560 is unavailable

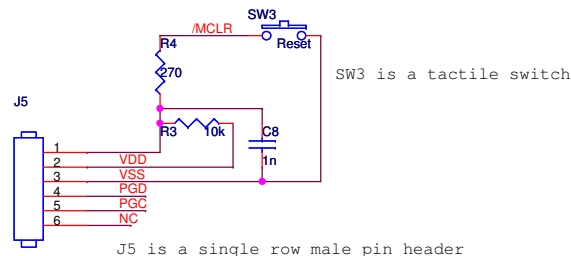
You may compute for own values of R1, R3 as well



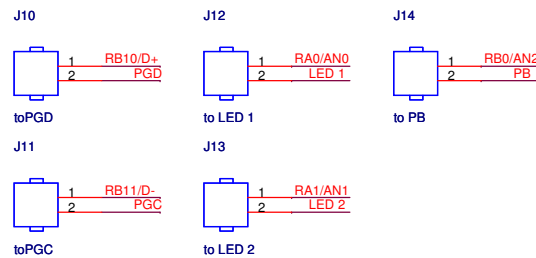
D2, D3 V Rail indicator LEDs

3V3 rail is Vdd supply  
5V rail will be used for other modules such as LCD

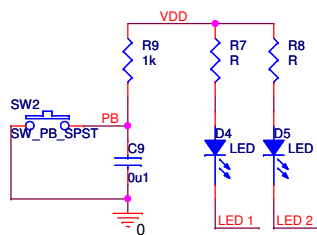
Title		
CoE 115 S21617 Bare Board		
Size	Document Number	Rev
B	<Doc>	<RevCode>
Date:	Friday, February 03, 2017	Sheet 1 of 1



J5 is a single row male pin header

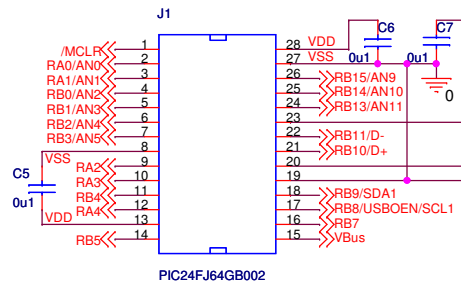


J10 to J14 2-pin male headers  
-- J10, J11 shorted by jumper when programming  
-- J12, J13, J14 shorted during board testing

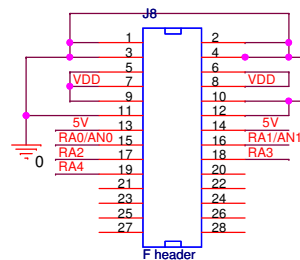


Test LEDs D4, D5, and PB SW2 are only for debugging purposes (i.e. to check if board is working correctly)

D4 - LED of your favorite color  
D5 - LED of your second favorite color :)



Place bypass caps, C5, C6, as close to uC as possible



Important:  
J8, J9 are single row female headers

J8, J9 must be spaced (Alexan) breadboard width from each other

Strict pin placement restrictions:  
-- J8 on left side of board contains supply pins and PORT A pins  
-- J9 on right side of board contains PORT B pins, VCAP, VBus

Pin assignments shown are suggestions, you may adjust the number of pins dedicated to supply pins, but pin placement restrictions still apply

