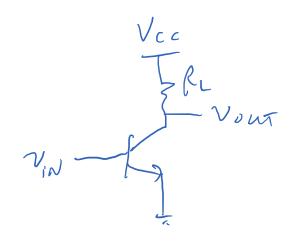


### EEE 51: Second Semester 2017 - 2018 Lecture 7

# **Current Mirrors**

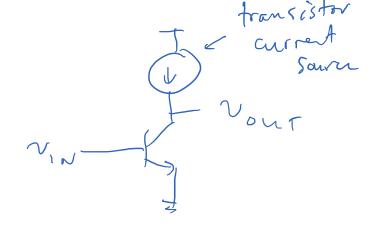
# Today

#### Current Mirrors

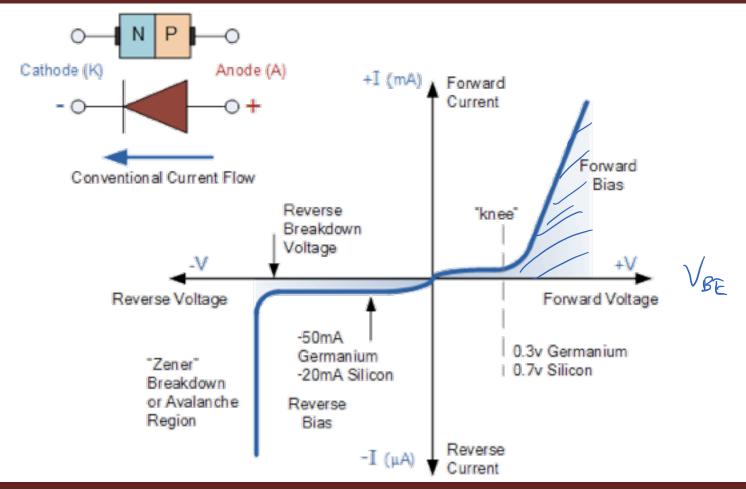


$$Av = -g_m(r_o/R_L)$$

$$r_a - g_m R_L$$

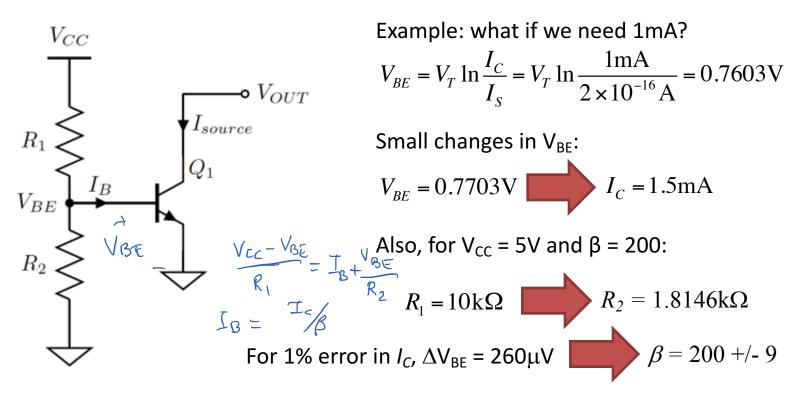


#### **Practical Diode Characteristics**



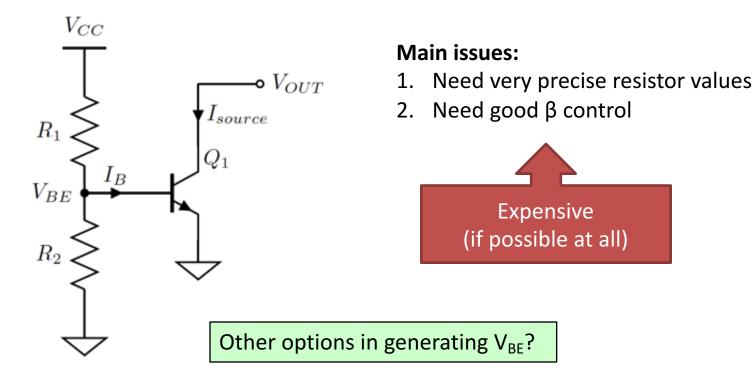
#### A Simple BJT Current Source

Provide V<sub>BE</sub> using a voltage divider

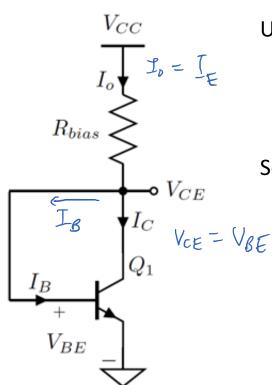


#### A Simple BJT Current Source

Provide V<sub>BE</sub> using a voltage divider



#### The Diode-Connected Transistor (1)



Use  $R_{bias}$  to apply a current  $I_o$  into the BJT:

$$I_o = I_C + I_B = I_C \cdot \left(1 + \frac{1}{\beta}\right) \quad \text{for } V_{CE} = V_{BE} \text{ (assuming } V_A \rightarrow \infty): \quad I_C = \frac{1}{\beta}$$

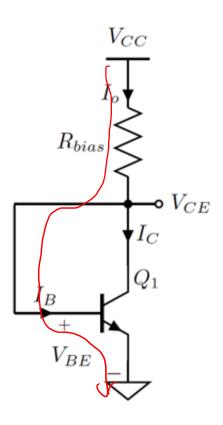
Solving for  $V_{CE} = V_{BE}$  (assuming  $V_A \rightarrow \infty$ ):

$$V_{CE} = V_{CE} - V_{BE}$$

$$V_{BE} = V_T \ln \left( \frac{I_C}{I_S} \right) = V_T \ln \left( \frac{I_o}{I_S} \cdot \frac{\beta}{\beta + 1} \right)$$

How do we generate  $I_0$ ?

#### The Diode-Connected Transistor (2)



KVL at base loop:

$$V_{CC} - I_o R_{bias} - V_{BE} = 0$$

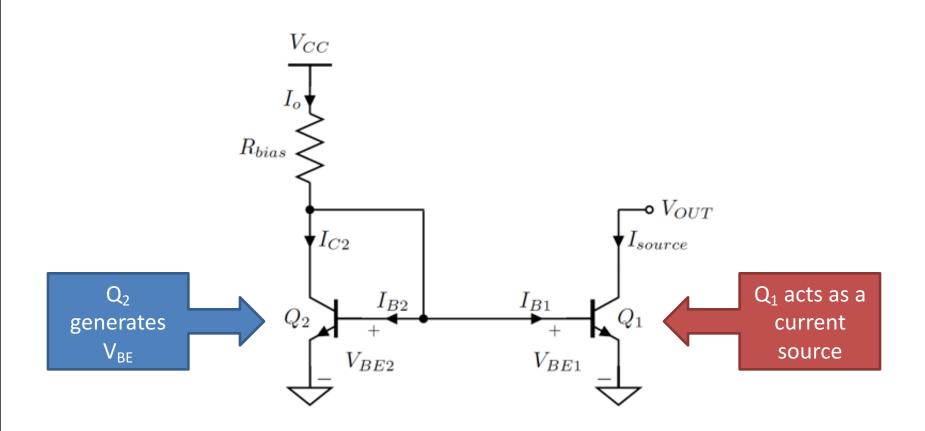
hus,  $R_{bia}$ 

$$R_{bias} = \frac{V_{CC} - V_{BE}}{I_o} = \frac{V_{CC} - V_{BE}}{I_S \cdot e^{\frac{V_{BE}}{V_T}} \cdot \left(1 + \frac{1}{\beta}\right)}$$

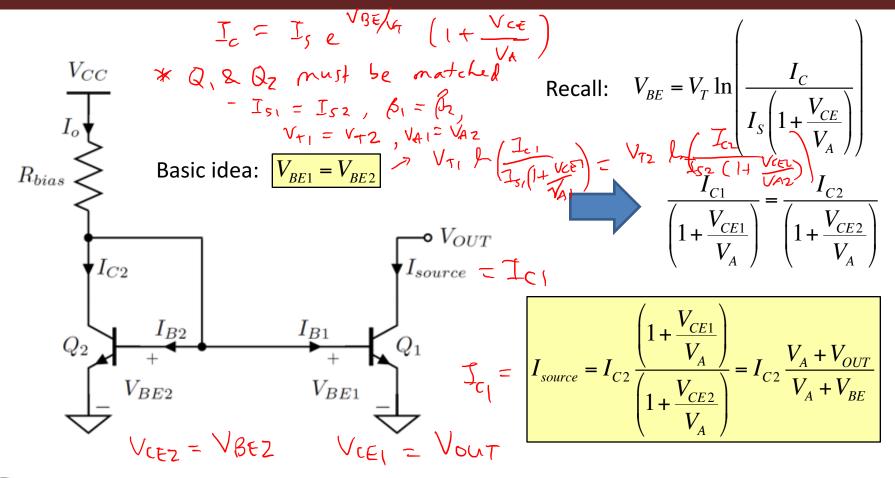


We can pick a resistor to generate V<sub>BE</sub>

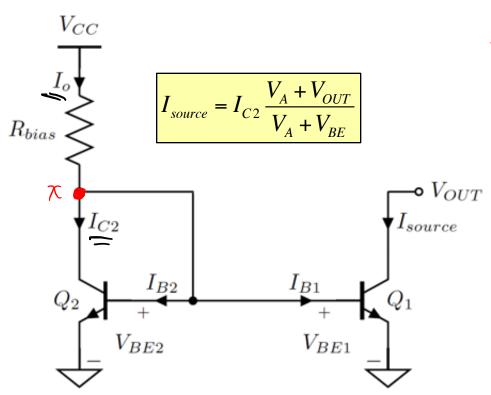
#### A Simple BJT Current Mirror (1)



#### A Simple BJT Current Mirror (2)



#### A Simple BJT Current Mirror (3)

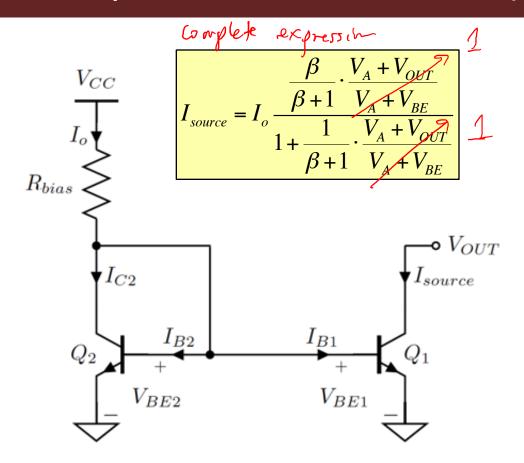


$$I_{C2} = I_o - I_{B1} - I_{B2} = I_o - \frac{I_{source}}{\beta} - \frac{I_{C2}}{\beta}$$

$$= \frac{\beta}{\beta + 1} I_o - \frac{I_{source}}{\beta + 1}$$

$$I_{source} = I_o \frac{\frac{\beta}{\beta + 1} \cdot \frac{V_A + V_{OUT}}{V_A + V_{BE}}}{1 + \frac{1}{\beta + 1} \cdot \frac{V_A + V_{OUT}}{V_A + V_{BE}}}$$

#### A Simple BJT Current Mirror (4)



Assume  $V_A \rightarrow \infty$ 



$$I_{source} \approx I_o \frac{\frac{\beta}{\beta + 1}}{1 + \frac{1}{\beta + 1}} = \frac{I_o}{1 + \frac{2}{\beta}}$$

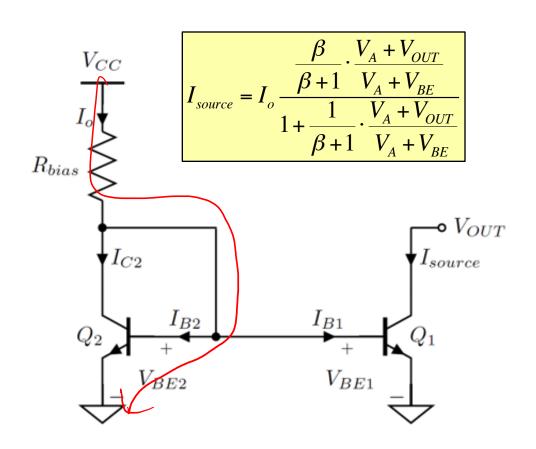
Assume  $V_A \rightarrow \infty$  and  $\beta \rightarrow \infty$ 

$$I_{source} \approx I_o$$
 Mirror!

#### **Mirroring Error:**

- 1. Due to the base currents
- 2. Due to V<sub>CE</sub> "mismatch"

#### A Simple BJT Current Mirror (4)

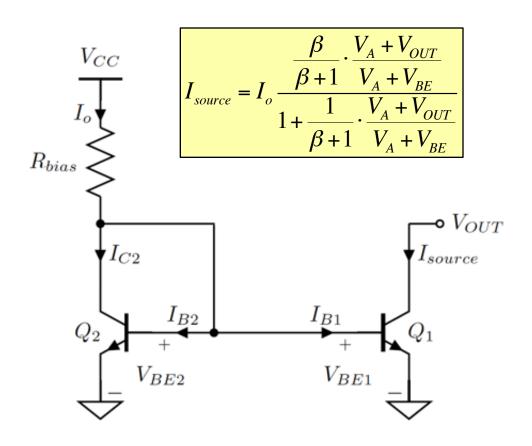


What about R<sub>bias</sub>?

$$R_{bias} = \frac{V_{CC} - V_{BE}}{I_o}$$

R is **linearly** related to I<sub>o</sub> and I<sub>source</sub> (not exponentially!)

#### A Simple BJT Current Mirror (5)



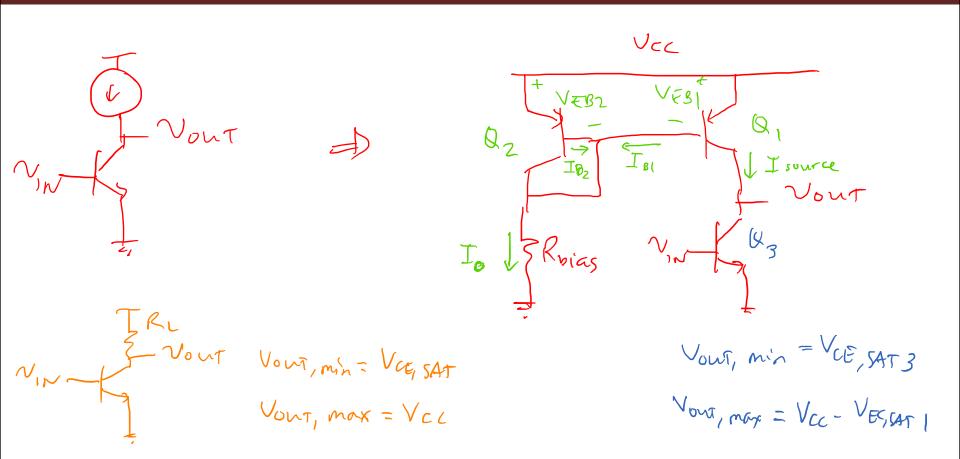
Assume  $V_A \rightarrow \infty$ 

$$I_{source} \approx \frac{I_o}{1 + \frac{2}{\beta}}$$

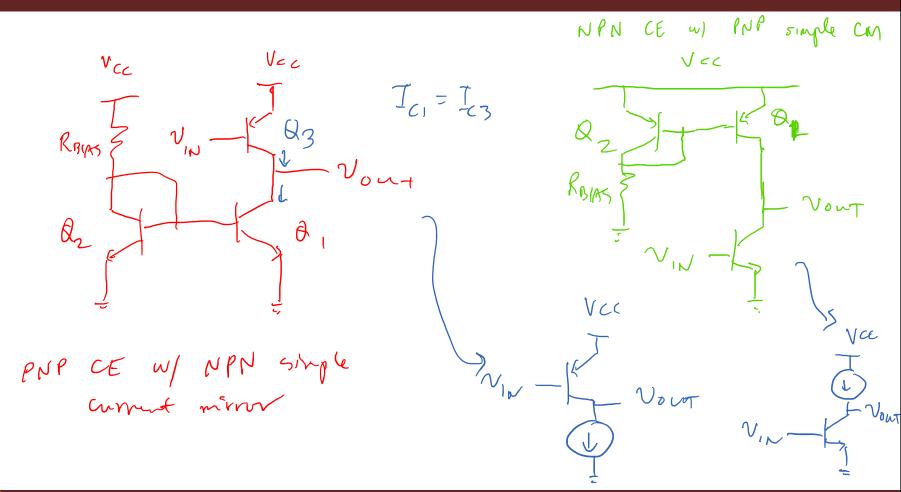
Again, what if we need 1mA?

 We can tolerate β as low as 99 and still get only 1% error!

#### OUTPUT SWING







#### **Next Meeting**

- Biasing Amplifiers Using Current Sources
- Differential Circuits