

Link with ***Asm_{CPU}***

Bottom layer (***L_{cpubtm}***) with ***Asm_{CPU}***

Multicore Linking Template

Optimize Intermediate Languages to
simplify linking

Per-CPU machine

Partial machines with environmental
context

Deterministic multicore machine
model with HW Oracle

Non-deterministic multicore
machine model

Refinement proofs

Abstract HW Setting

Refinement