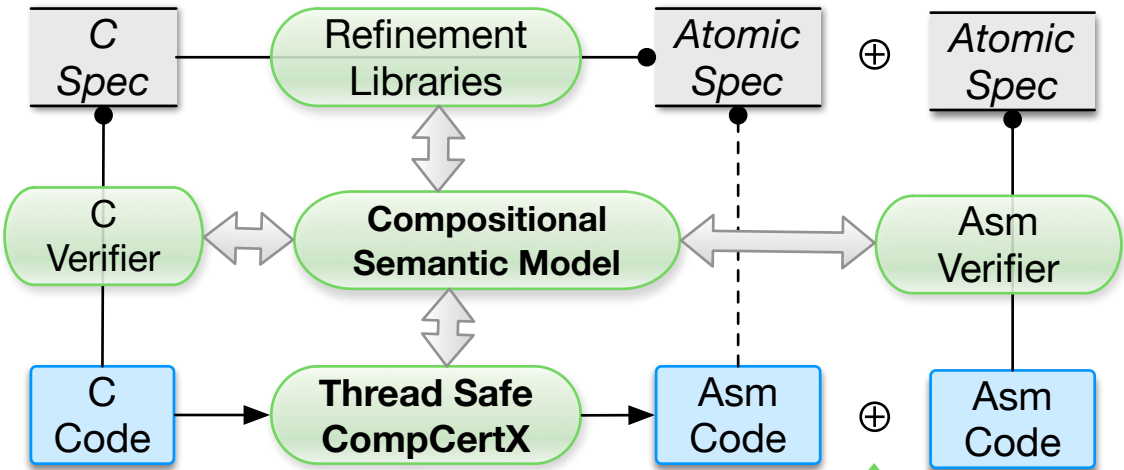


PerCoreLayer $L[c]$

c : CPU id; $D = \{c1, c2, \dots\}$
 t : thread id; $A = \{t1, t2, \dots\}$

PerThreadLayer $L[t]$



PerThreadLayer $L'[t]$

Certified Asm Linking

Certified Multi-threaded Linking

Certified Multi-core Linking

Certified Multi-layer Linking

Legend

Layer

Spec

Code

Certified Tool / Model

\oplus Linking

\bullet Refinement

\rightarrow Compile