

Link with the bottom layer on ***Asm_{CPU}***

Bottom layer (***L_{btm}***) with ***Asm_{CPU}***

Multicore linking template

Optimize intermediate languages to
simplify linking

Per-CPU machine

Partial machines with
environmental context

Deterministic multicore machine
model with HW oracle

Non-deterministic multicore
machine model

Refinement proofs

HW
configurations
for ***Asm_{CPU}***

Abstract definitions

