

Link with ***Asm_{CPU}*** (Section 3.7)

Bottom layer (***L_{cpubtm}***) with ***Asm_{CPU}***

Multicore Linking Template

Optimize Intermediate Languages to
simplify linking (Section 3.5)

Per-CPU machine (Section 3.4)

Partial machines with environmental
context (Section 3.3)

Deterministic multicore machine
model with HW Oracle (Section 3.2)

Non-deterministic multicore
machine model (Section 3.1)

Refinement proofs
(Section 3.6)

Abstract HW Setting
(Section 3.1)

Refinement