

Stripping multiple laterals, one step at a time Introducing the “step-taking modality”

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The presentation addresses the following:

- ▶ A brief coverage of what the later modality (the “later”) is
- ▶ The ongoing story about adding and stripping more and more later
- ▶ The challenges regarding later with respect to **expressivity** and **presentation**
- ▶ A proposal to solve both of these problems: The step-taking modality $\rightsquigarrow P$

Please do: ask questions, add missing clarifications, provide running feedback

What are later?

Structural recursion through program steps (step-indexing):

LATER-INTRO

$$\frac{P}{\triangleright P}$$

LATER-MONO

$$\frac{P \vdash Q}{\triangleright P \vdash \triangleright Q}$$

HT-FRAME-LATER

$$\frac{\{P\} e \{w. Q\}}{\{P * \triangleright R\} e \{w. Q * R\}}$$

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Higher-order ghost state (such as Invariants and propositional agreement):

HT-INV-OPEN

$$\frac{e \text{ is atomic} \quad \{\triangleright P * Q\} e \{w. \triangleright P * R\}}{\{\boxed{P} * Q\} e \{w. R\}}$$

HO-AGREE

$$\frac{[\text{ag}(P)]^\gamma \quad [\text{ag}(Q)]^\gamma}{\triangleright(P = Q)}$$

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“Co-inductive” definitions:

$$\text{is_stream} \triangleq \mu X \ell. (\ell = \text{inl}()) \vee (\exists v, \ell'. \ell = \text{inr}(v, \ell') * \triangleright X \ell')$$

What are the challenges with laterals?

People started putting them in various (sound) places, and they started to crop up in presented abstract specifications (ghost theories):

PROTO-RECV-R

$$\frac{\text{prot_ctx } \chi \ ([w] \cdot \vec{v}_1) \ \vec{v}_2 * \text{prot_own}_r \ \chi \ (\text{?}\vec{x}:\vec{\tau} \langle v \rangle \{P\}. \text{prot})}{\models \triangleright \exists \vec{y}. (w = v[\vec{y}/\vec{x}]) * P[\vec{y}/\vec{x}] * \text{prot_ctx } \chi \ \vec{v}_1 \ \vec{v}_2 * \text{prot_own}_r \ \chi \ \text{prot}[\vec{y}/\vec{x}]}$$

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Expressivity: Fine! We do take a step whenever we use the ghost theory

Presentation: How do we explain this specification to newcomers (and reviewers)?

“Dont think about it”

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“We can just get rid of it, when we take a step”

But then the problem multiplied.

Multiple laterers

People started putting even more laterers in (sound) places.

PROTO-ALLOC

$$\models \exists \chi. \text{prot_ctx } \chi \in \epsilon * \text{prot_own}_l \chi \text{ prot} * \text{prot_own}_r \chi \overline{\text{prot}}$$

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$$\frac{\text{prot_ctx } \chi \vec{v}_1 \vec{v}_2 * \text{prot_own}_l \chi (!\vec{x}:\vec{\tau} \langle v \rangle \{P\}. \text{prot}) * P[\vec{t}/\vec{x}]}{\models \triangleright^{|\vec{v}_2|} \text{prot_ctx } \chi (\vec{v}_1 \cdot [v[\vec{t}/\vec{x}]]) \vec{v}_2 * \text{prot_own}_l \chi (\text{prot}[\vec{t}/\vec{x}])}$$

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Expressivity: We can just synchronise using a physical lock and take many more steps!

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Presentation: How do we present this to newcomers (and reviewers)???

You roll up your sleeves..

The need for skip instructions. The rules `PROTO-SEND-L` and `PROTO-SEND-R` from Figure 20 contain a number of later modalities (\triangleright) proportional to the other endpoint's buffer. As explained in § 9.3 these later modalities are the consequence of having to perform a number of case analyses on the subprotocol relation, which is defined using guarded recursion, and thus contains a later modality for each recursive unfolding.

To eliminate these later modalities, we instrument the code of the `send` function with the `skipN (llength r)` instruction, which performs a number of skips equal to the size of the other endpoint's buffer r . The `skipN` instruction has the following specification:

$$\{\triangleright^n P\} \text{ skipN } n \{P\}$$

Instrumentation with skip instructions is used often in work on step-indexing, see *e.g.*, [SSB16; GST⁺20]. Instrumentation is needed because current step-indexed logics like Iris unify physical/program steps and logical steps, *i.e.*, for each physical/program step at most one later can be eliminated from the hypotheses. In recent work by Svendsen *et al.* [SSB16], Matsushita and Jourdan [MJ20], and Spies *et al.* [SGG⁺21] more liberal versions of step-indexing have been proposed, but none of these versions of step-indexing have been integrated into the main Coq development of Iris and HeapLang.

But what if you can only take one step?

e.g. if you have to put your ghost state in an invariant

Stripping multiple laterals at every step

People came up with clever solutions to strip multiple laterals during one step!

$$\frac{\text{HT-STEP-GET} \quad \{P * \bowtie 0\} e \{\Phi\}}{\{P\} e \{\Phi\}}$$

$$\frac{\text{HT-STEP-INCR} \quad \{P\} e \{w. Q\}}{\{P * \bowtie n\} e \{w. Q * \bowtie n + 1\}}$$

$$\frac{\text{HT-STEP-FRAME} \quad \{P\} e \{w. Q\}}{\{P * \bowtie n * \triangleright^n R\} e \{w. Q * R\}}$$

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Expressivity: We can strip all the laterals we need, by tracking a step lower bound inside our invariant that grows in tandem with our ghost state, so we can guarantee that we can always strip the appropriate laterals using Ht-step-frame, while updating the lower bound at every step using Ht-step-incr!

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Presentation: ...?

HT-STEP-GET	HT-STEP-INCR	HT-STEP-FRAME	STEP-DUP
$\frac{\{P * \mathbb{X} 0\} \langle ip; e \rangle \{\Phi\}}{\{P\} \langle ip; e \rangle \{\Phi\}}$	$\frac{\{P\} \langle ip; e \rangle \{w. Q\}}{\{P * \mathbb{X} n\} \langle ip; e \rangle \{w. Q * \mathbb{X} n + 1\}}$	$\frac{\{P\} \langle ip; e \rangle \{w. Q\}}{\{P * \mathbb{X} n * \triangleright^n R\} \langle ip; e \rangle \{w. Q * R\}}$	$\frac{\mathbb{X} n}{\mathbb{X} n * \mathbb{X} n}$

Fig. 12. The mechanism for stripping multiple lateres. We require e to be an atomic expression.

The shared logical context can then be captured as the following Iris invariant:

$$\exists Tl, Tr, Rl, Rr. \text{auth_list } \chi_{Tl} \ Tl * \text{auth_list } \chi_{Tr} \ Tr * \text{auth_list } \chi_{Rl} \ Rl * \text{auth_list } \chi_{Rr} \ Rr * \\ \text{prot_ctx } \chi_{\text{chan}} \ (Tl - Rr) \ (Tr - Rl) * Rr \leq_p Tl * Rl \leq_p Tr * \mathbb{X} |Tl| * \mathbb{X} |Tr|$$

Stripping multiple lateres. In Iris, and thus Aneris, one can strip a later whenever a step of computation is taken. Conventionally the intuition is that one step equates stripping one later. However, recent discoveries [Matsushita et al. 2022; Mével et al. 2019; Spies et al. 2022] uncovered various methods for stripping *multiple* lateres per step. Based on these discoveries we extended Aneris with a similar, albeit more simplistic, mechanism as presented in Figure 12. The mechanism lets us strip multiple lateres during one physical step, based on the amount of steps that has been taken thus far. The rule HT-STEP-GET lets us track a new lower bound of steps taken thus far $\mathbb{X} 0$, and HT-STEP-INCR allows us to increase it by one, every time a step is taken. Crucially, the rule HT-STEP-FRAME lets us frame resources under an amount of lateres corresponding to the lower bound

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WISHFUL-PROTO-ALLOC

$$\exists \chi. \text{prot_ctx } \chi \in \epsilon * \text{prot_own}_l \chi \text{ prot} * \text{prot_own}_r \chi \overline{\text{prot}}$$

WISHFUL-PROTO-SEND-L

$$\frac{\text{prot_ctx } \chi \vec{v}_1 \vec{v}_2 * \text{prot_own}_l \chi (!\vec{x}:\vec{\tau} \langle v \rangle \{P\}. \text{prot}) * P[\vec{t}/\vec{x}]}{\text{prot_ctx } \chi (\vec{v}_1 \cdot [v[\vec{t}/\vec{x}]]) \vec{v}_2 * \text{prot_own}_l \chi (\text{prot}[\vec{t}/\vec{x}])}$$

WISHFUL-PROTO-RECV-R

$$\frac{\text{prot_ctx } \chi ([w] \cdot \vec{v}_1) \vec{v}_2 * \text{prot_own}_r \chi (? \vec{x}:\vec{\tau} \langle v \rangle \{P\}. \text{prot})}{\exists \vec{y}. (w = v[\vec{y}/\vec{x}]) * P[\vec{y}/\vec{x}] * \text{prot_ctx } \chi \vec{v}_1 \vec{v}_2 * \text{prot_own}_r \chi \text{ prot}[\vec{y}/\vec{x}]}$$

Solution: Introducing the step-taking modality!

$$\vdash\!\!\!\rightarrow P$$

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$$| \rightsquigarrow P$$

Recovers the intuition that we can get P *after taking a step*:

$$\frac{\text{HT-STEP-MODALITY} \quad \{P\} e \{w. Q\}}{\{P * | \rightsquigarrow R\} e \{w. Q * R\}}$$

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Intentionally looks like the original later-stripping rule:

$$\frac{\text{HT-LATER-FRAME} \quad \{P\} e \{w. Q\}}{\{P * \triangleright R\} e \{w. Q * R\}}$$

Abstract specification of later-stripping mechanism

The step-taking modality can be used to express the later-stripping mechanism as an abstract specification pattern (rather than via Hoare triples):

$$\begin{array}{c} \text{STEP-STEP-GET} \\ \hline |\rightsquigarrow \Delta 0 \end{array} \qquad \begin{array}{c} \text{STEP-STEP-INCR} \\ \Delta n \\ \hline |\rightsquigarrow \Delta n + 1 \end{array} \qquad \begin{array}{c} \text{STEP-STEP-FRAME} \\ \Delta n * \triangleright^n P \\ \hline |\rightsquigarrow P \end{array}$$
$$\begin{array}{c} \text{HT-STEP-MODALITY} \\ \{P\} e \{w. Q\} \\ \hline \{P * |\rightsquigarrow R\} e \{w. Q * R\} \end{array}$$

These rules supercede the former Hoare triple rules for the later-stripping mechanism

Abstract specifications of ghost theory

We can derive abstractions with the step-taking modality on top of each other:

$$\text{prot_ctx_step } \chi \vec{v}_1 \vec{v}_2 \triangleq \text{prot_ctx } \chi \vec{v}_1 \vec{v}_2 * \mathbb{X} |\vec{v}_1| * \mathbb{X} |\vec{v}_2|$$

STEP-PROTO-ALLOC

$$\vdash \exists \chi. \text{prot_ctx_step } \chi \in \epsilon * \text{prot_own}_l \chi \text{ prot} * \text{prot_own}_r \chi \overline{\text{prot}}$$

STEP-PROTO-SEND-L

$$\frac{\text{prot_ctx_step } \chi \vec{v}_1 \vec{v}_2 * \text{prot_own}_l \chi (!\vec{x}:\vec{\tau} \langle v \rangle \{P\}. \text{prot}) * P[\vec{t}/\vec{x}]}{\vdash \text{prot_ctx_step } \chi (\vec{v}_1 \cdot [v[\vec{t}/\vec{x}]]) \vec{v}_2 * \text{prot_own}_l \chi (\text{prot}[\vec{t}/\vec{x}])}$$

STEP-PROTO-RECV-R

$$\frac{\text{prot_ctx_step } \chi ([w] \cdot \vec{v}_1) \vec{v}_2 * \text{prot_own}_r \chi (? \vec{x}:\vec{\tau} \langle v \rangle \{P\}. \text{prot})}{\vdash \exists \vec{y}. (w = v[\vec{y}/\vec{x}]) * P[\vec{y}/\vec{x}] * \text{prot_ctx_step } \chi \vec{v}_1 \vec{v}_2 * \text{prot_own}_r \chi \text{ prot}[\vec{y}/\vec{x}]}$$

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STEP-PROTO-RECV-R

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Hidden benefit of the step-taking modality abstracting over later

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Derived abstractions may hide the number of lateres that is needed to be stripped:

SESCROW-INIT

$$\models \exists \chi. \text{ses_own } \chi \text{ left } 0 \ 0 \ \text{prot} * \text{ses_own } \chi \text{ right } 0 \ 0 \ \overline{\text{prot}}$$

SESCROW-SEND

$$\frac{\text{ses_own } \chi \ s \ n \ m \ (!(\vec{x}:\vec{\tau}) \langle v \rangle \{P\}. \text{prot}) * P[\vec{t}/\vec{x}]}{\models \triangleright \text{ses_own } \chi \ s \ (n+1) \ m \ (\text{prot}[\vec{t}/\vec{x}]) * \text{ses_idx } \chi \ s \ n \ (v[\vec{t}/\vec{x}])}$$

SESCROW-RECV

$$\frac{\text{ses_own } \chi \ s \ n \ m \ (?(\vec{x}:\vec{\tau}) \langle v \rangle \{P\}. \text{prot}) * \text{ses_idx } \chi \ \bar{s} \ m \ w}{\models \triangleright^{???} \exists (\vec{y}:\vec{\tau}). \text{ses_own } \chi \ s \ n \ (m+1) \ (\text{prot}[\vec{y}/\vec{x}]) * w = v[\vec{y}/\vec{x}] * P[\vec{y}/\vec{x}]}$$

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SESCROW-RECV

$$\frac{\text{ses_own } \chi \ s \ n \ m \ (?(\vec{x}:\vec{\tau}) \langle v \rangle \{P\}. \text{prot}) * \text{ses_idx } \chi \ \bar{s} \ m \ w}{\vdash \exists (\vec{y}:\vec{\tau}). \text{ses_own } \chi \ s \ n \ (m+1) \ (\text{prot}[\vec{y}/\vec{x}]) * w = v[\vec{y}/\vec{x}] * P[\vec{y}/\vec{x}]}$$

This abstract specification pattern is *undefinable* without the step-taking modality.

The step-taking modality definition

$$\models\!\!\!\rightarrow P \triangleq \forall n. \mathbb{X}_{\bullet} n \Rightarrow (\mathbb{X}_{\bullet} n * (\triangleright^{n+1} \mathbb{X}_{\bullet} n + 1 \Rightarrow \mathbb{X}_{\bullet} n + 1 * P))$$

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- ▶ Show P

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OBS: details about masks are omitted for brevity sake

The step-taking modality properties

The step-taking modality enjoys a mix of the rules for the later modality and the ghost update modality:

$$\frac{\text{STEP-INTRO} \quad P}{\Downarrow P}$$

$$\frac{\text{STEP-MONO} \quad P \vdash Q}{\Downarrow P \vdash \Downarrow Q}$$

$$\frac{\text{STEP-UPD} \quad \Downarrow \Downarrow \Downarrow P}{\Downarrow P}$$

$$\frac{\text{STEP-SEP-COMM}}{\Downarrow P * \Downarrow Q \dashv\vdash \Downarrow P * Q}$$

These lets us derive abstract specification patterns on top of each other, without breaking abstraction

Further motivation for the step-taking modality

A valid concern is that the step-taking modality will become the new later modality

$$|\rightsquigarrow^n$$

The intention is that this wont happen.

Regardless of how later-stripping mechanisms evolved, the step-taking modality should always capture the notion of being able to strip however many later is available during *one* step!

If multiple step-taking modalities are iterated, that should semantically mean that multiple steps are *intended* to be taken.

Questions?