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2018203048

1. For the following C statement, what is the corresponding MIPS assembly code?

(#= g+(h-5);)

(MSS)

addi i, h, -5

add f, g, i

2. Write a single C statement that corresponds to two MIPS assembly instructions.

2. Write a single C statement that corresponds to two MIPS assembly instructions.

Add f, g, h
Add, f, I f

(78%) f=1+(9+h);

3. For the following C statement, write the corresponding MIPS assembly code.

(7647

Suh \$t0,\$53,\$54

511 \$to,\$to,2.

1w st1, 0(456)

add \$t1, \$t1, \$t0

IW \$t1.0(\$t1)

Sw \$t1, 32(\$57)

4. Thow how the value Oxahodef12 would be avanged in memory of a little-ordian and a big-endian machine

(2007

big-endion? EMEMZ. Oxab. Oxcd. Oxef. Ox12 202 MBZANI 2123 little-endune usus 0x12, 0xef. 0xcd. 0xob Ealet.

5. Translate Oxabodef12 into decimal.

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10×167 + 11×166 + 12×165+ 13×164+ 14×163+ 15×163+ 1×16+ 2×160 = 2,882,400,018