

## Personal Profile



1. **Name of the teacher (in full):** Asha Vijay Durafe
2. **Department:** Electronics
3. **Designation:** Assistant Professor
4. **Personal Details:**
  - a. Date of birth: 17/10/1980
  - b. Languages Known:- English, Hindi, Marathi, Sanskrit
  - c. Email-id:-asha.durafe@sakec.ac.in
  - d. Mobile No:-9004048302
  - e. Contact Address: G18/102, Shree Ganesh CHS, Sector 9, Ghansoli-400701
5. **Qualifications of the teacher:-**

Degree/ Post Graduate Degree Examination	Special / Principal Subjects Offered	Allied Additional Subordinate Subject Offered	Class Obtained	Year Of Passing	University
<b>B.E.</b>	Data Comm. Power electronics, MFOC,	Industrial Automation	I class distn.	2003	Mumbai
<b>M.Tech</b>	Data comm., Digital comm, Advanced DSP, Embedded systems	Nanoelectronics	I class distn.	2011	Mumbai
<b>PhD</b>	Cyber Security	Wireless communication, Advance Communication, Research Methodology	Pursuing		Sir Padampat Singhania University, Rajasthan

**6. Teaching Experience in this College:**

Years-14      Months-3

**7. Help Rendered in college administration by membership of various committees such as Discipline Committee, Admission Committee, Student Welfare Committee, etc**

1. Worked as a member in time table committee
2. Worked as a member in Admission committee
3. Facebook coordinator
4. Member of Research Cell
5. Internship Coordinator

**8. Previous Experience:**

Name of The Institution Where Employed Previously	Period of Service With Dates	Designation	Class Taught	Subjects Taught
Vidyalankar classes	Dec 2004 to Dec 2005	Technical writer	GATE contact programmes	Communication, digital design

**9. Subjects taught:** Power electronics and Drives, Power Electronics, Communication Networks, Communication Systems & Applications, Analog Integrated Circuits & Applications, Digital comm., Electrical Networks, Advanced Networking Technologies

**10. Any other contribution in:**

- a. **Teaching Methods:** Organized one day Orientation Program on Advanced Networking Technology.
- b. **Evaluation Methods:** Paper setter for Power Electronics M.E. semester I
- c. **Course Development etc:** Syllabus setting committee member for Power Electronics

**11. Academic and Professional Growth:**

<b>1. Research work Undertaken</b>		
<b>2. Papers published in Journals</b> (copies enclosed)	<b>National</b>	-
	<b>International</b>	1. International Journal of Engineering Development and Research. Paper

		<p>published: Zigbee based home patient monitoring Paper ID: IJEDR150116 ISSN: 2321-9939, Volume 3</p> <p>2. International Journal of Advanced Research in Computer and Communication Engineering. (IJARCCE) Paper published: Cognitive radio sensor network future of wireless sensor network. Volume 4, Issue 2, ISSN: 2278-1021</p> <p>3. International Journal of Research studies in Electrical and Electronics and Engineering (IJRSEEE) volume 2, ISSUE 1, 2016 pp. 1-5, Title: Controlling data access in cloud based on multiple level attribute set based encryption.</p> <p>4. International Journal of Innovative Research in Science, engineering and Technology, Vol.5, Issue 6, June 2016 for ML-ASBE security for controlling the access of data through multiple levels attribute set based encryption.</p> <p>5. International Journal of Creative Research and Thoughts (ISSN:2320-2882) for paper: A review of digital Steganography methods published in November 2017.</p> <p>6. International Journal of Creative Research and Thoughts (ISSN: 2320-2882) volume 6, Issue 2 for paper: ZigBee based restaurant management system in April 2018.</p> <p>7. International Journal of Creative Research and Thoughts (ISSN 2456-6470) volume 2, Issue 6 for paper: Matlab/Simulink model for Three phase voltage source Inverter in Oct 2018.</p>
<b>3. Papers presented in Conferences</b> (copies enclosed)	<b>National</b>	-
	International	1. Asha Durafe, Amutha Jeyakumar, FPGA based Spread Spectrum RADAR, 2011

		<p>IEEE , International Conference on Signal Processing and Communication, Tamilnadu.</p> <p>2.Asha Durafe, Amutha Jeyakumar, FPGA based ultra wideband pseudo noise Radar, International Conference on Recent Technologies 2011, Mumbai.</p> <p>3.International Conference and workshop on Electronics and Telecommunication Engineering 2014 TCET, Mumbai Feb 2014, Selected Title of the Paper: UART designing for four different baud rate for cyclone III family</p> <p>4.International Conference and workshop on Electronics and Telecommunication Engineering 2014 TCET, Mumbai Feb 2014, VHDL based UART.</p>
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12. **Major Projects Guided (UG/PG):** Guided more than 30 UG projects for Electronics Engineering and 5 PG projects.
13. **Books Published/ IPRs/ Patents:-**
14. **PhD Guide? Give field and University(synopsis attached):** Prof. Vinod Patidar, Chaotic Encryption, Sir Padampat Singhanian University, Rajasthan
15. **Consultancy Activities(Reports enclosed):-**
16. **Awards: ISA Maharashtra section Award for giving expert talk on Digital Steganography and its applications in cyber security in One Day Seminar on Cyber Security in Industrial Automation & Control on 24 February 2018**
17. **Grants Fetched:-**University Minor Research Grant of Rs. 35000/- for year 2018-19
18. **Conferences/Seminars/Training Programs/ QIPs:**
  - a. **Organized-**1. Organized Orientation program on Advanced Networking Technologies on 15<sup>th</sup> Feb.2011 at Shah and Anchor College of Engg., Chembur
  2. Coordinator for one week Short Term Training Program On Emerging Technologies in Power Electronics and Drives in June 2015
  3. Organized one day Industrial Visit at L & T Rabale switchgear section on 7<sup>th</sup> Oct. 2017 for students of Electronics engineering.

4. Organized one day Industrial Visit at CSMT Railway Yard on 15<sup>th</sup> March 2019 for students of Electronics engineering.
5. Attended eSIM workshop on Linux by IIT on 23<sup>rd</sup> Aug. 2019.

**b. Attended:**

Sr. No.	Title	Date	Venue
1	STTP on Wavelet Transform	24/11/2008 to 28/11/2008	K.J. Somaiya Institute, Mumbai
2	Software tools in Electronics and Telecommunication Engineering	15/12/2008 to 19/12/2008	Rajiv Gandhi Institute of Technology, Mumbai
3	STTP on 3G Technology and Recent Trends	10/5/2011 to 14/5/2011	NMIMS, Mumbai
4	STTP on Virtual Instrumentation	11/7/2011 to 15/7/2011	Shah and Anchor College of Engineering, Mumbai
5	STTP on Presentation and Effective Teaching Techniques	18/7/2011 to 22/7/2011	Shah and Anchor College of Engineering, Mumbai
6	Two week STTP on Computer Communication Networks with Lab	21/11/2011 to 2/12/2011	SPIT, Mumbai
7	Workshop on Best practices in Network Simulator-2	27/12/2010 to 30/12/2012	Xavier Institute of Technology, Mumbai
8	Workshop on Power electronic Applications	16/4/2012 to 20/4/2012	Fr. Rodrigues Institute of Technology, Vashi, Mumbai
9	STTP on Advanced communication techniques	08/07/2013 to 12/07/2013	Shah and Anchor College of Engineering, Mumbai
10	STTP on An Insight into VLSI and Nanotechnology	14/7/2014 to 18/07/2014	Shah and Anchor College of Engineering, Mumbai
11	STTP on Emerging Technological Trends in Power Electronics and Drives	06/07/2015 to 11/07/2015	Shah and Anchor College of Engineering, Mumbai
14	FDP on Teaching Power Electronics with MATLAB/Simulink	31/8/2015 to 01/08/2015	RAIT, Nerul
12	STTP on Design Aspects in CMOS Analog circuits, ASIC and MEMS	27/06/2016 to 01/07/2016	Shah and Anchor College of Engineering, Mumbai
15	National level workshop on Accreditation and Outcome based education	15/12/2016 to 16/12/2016	Terna College of Engineering, Nerul
13	STTP on Advanced Embedded Application and Real Time Design	03/07/2017 to 08/07/2017	Shah and Anchor College of Engineering, Mumbai
14	STTP on Pedagogy for Effective Integration of Information & Communication Technologies (ICT) in Engineering	03/05/2018 to 08/05/2018	Shah and Anchor College of Engineering, Mumbai

	Education		
15	STTP on Advanced Networking and security Prospects	01/07/2019 to 06/07/2019	Shah and Anchor College of Engineering, Mumbai

**Delivered-** Talk on Hall Effect sensor and its applications in one week Short Term Training Program on Emerging Technologies in Power Electronics and Drives in June 2015

**19. Position of Responsibility:**

1. Worked as Examiner for Sem VII Power Electronics and Drives at university level
2. Worked as moderator for for Sem VII Power Electronics and Drives at university level
3. Paper setter for Power Electronics Sem I and Sem II at M.E. level

**20. Areas of Interest:** 1. Communication Networks and cyber security

2. Power Electronics
3. Wireless communication
4. IoT and security
5. Digital Communication
6. Digital Image Processing