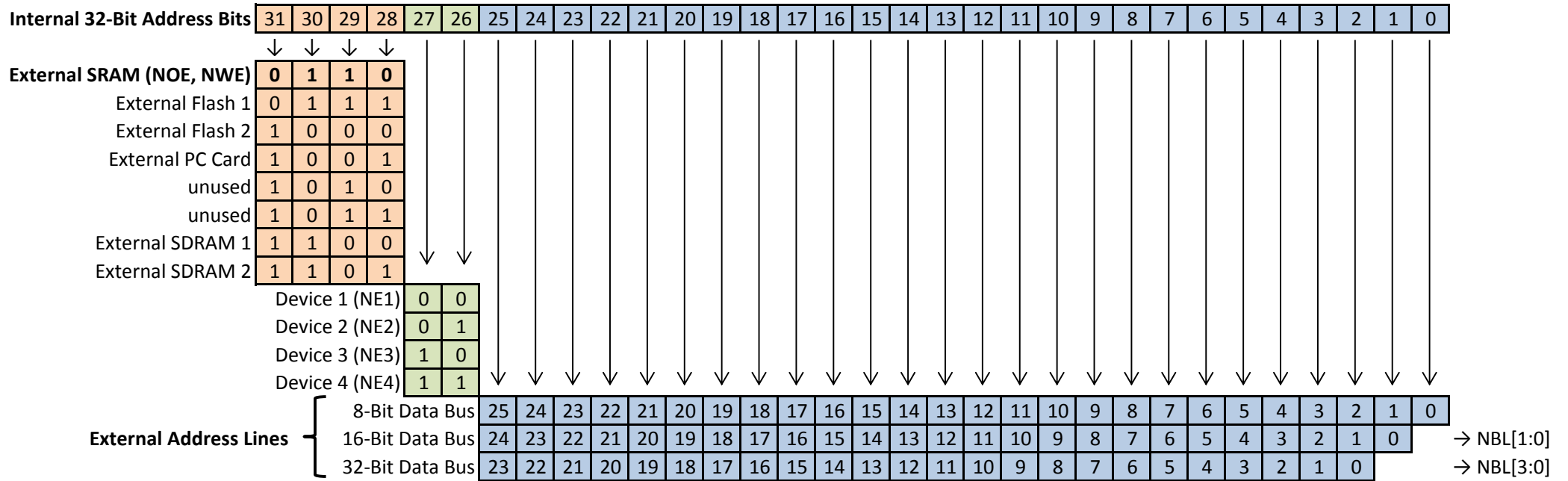


## STM32F429 Flexible Memory Controller (FMC) Decoding



A[31:28]	0x0 - 0x5	Non-FMC access to chip internal peripherals like flash, SRAM, control registers
	0x6 - 0xD	FMC access: Select one type of external memory
	0xE - 0xF	Non-FMC access to chip internal ARM peripherals like NVIC

For external SRAM	A[27:26]	0x0 - 0x3	Select one of 4 devices → controls NE1, NE2, NE3, NE4
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For external SRAM	A[25:0]	8-Bit external data bus	Internal lines A[25:0] mapped to external A[25:0] No NBL signals required
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16-Bit external data bus	Internal lines A[25:1] mapped to external A[24:0] Internal A[0] yields NBL[1:0]
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32-Bit external data bus	Internal lines A[25:2] mapped to external A[23:0] Internal A[1:0] yields NBL[3:0]
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