

Dimitrios Stamatios Bouras

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EDUCATION

- 09/2023 - Ongoing **University College London**, London, United Kingdom
- MSc Software Systems Engineering
- 09/2017 - 02/2023 **National Technical University of Athens**, Athens, Greece
- MEng, BSc in Electrical and Computer Engineering (5-year integrated masters degree; 300 ECTS)
 - Total Grade: 8.69/10
 - Selected Flows: Computer Systems, Computer Software, Management and Decision Support Systems
- 09/2014 - 06/2017 **2nd General High School Kalyvion**, Athens, Greece (Grade: 20.0./20.0, “Distinction”, Top of 2017 class)

PROFESSIONAL EXPERIENCE

- 03/2023 - 09/2023 **DevOps Engineer**, IBM Hellas
- Enginner at the COMSOS project; the implementation of a new core banking system for the National bank of Greece, the dominant Greek banking services provider
 - Developed continuous integration testing and deployment pipelines
 - Created multipurpose release management web-tool
 - Supported the developer teams from Finacle Infosys with error identifying , handling and resolving
 - Created and managed docker containers with Kubernetes, Redhat Openshift, UCD, UCV, Jenkins
- 11/2022 - 02/2023 **Teaching Assistant**, National Technical University of Athens, School of Electrical and Computer Engineering
- Provided assistance to students of the microlab’s course Microcomputers Laboratory
 - Examined and graded students on semester exercises and final exam
- 10/2021 - 02/2023 **Undergraduate Research Assistant**, Microprocessors and Digital Systems Lab, NTUA
- Assisted in Energy efficiency and binary code lifting research
 - Performed LLVM related task, created custom scripts
- 07/2021 - 09/2021 **DevOps Integration Specialist Intern**, IBM Hellas
- Intern at the COMSOS project; the implementation of a new core banking system for the National bank of Greece, the dominant Greek banking services provider

HONORS & AWARDS

- **“Proud of Youth” Scholarship** by Helleniq Energy for postgraduate studies
- **Eugenides Foundation Scholarship** for postgraduate studies
- **National Chemistry Olympiad 2017**, 7th , selection as reserve to represent Greece at the Global Chemistry Olympiad
- **European Youth Parliament of Greece 2016**, 11th , selection as reserve to represent Greece at the 87th international Session

SKILLS

- C, C++, Python, Java, Groovy, Typescript, JavaScript, SQL, x86 – RISC-V Assembly, Standard ML, Prolog, PHP, Matlab, Verilog, UML, VHDL
- Jenkins, Openshift, Docker, Kubernetes, Azure DevOps, Maven, Terraform, Ansible, IaC, Prometheus, ELK, Android studio, LLVM, Xilinx Vivado, Machine Learning , Neural Networks, SVMs ,PyTorch, TensorFlow, Keras

PUBLICATIONS

- **Reliable Basic Block Energy Accounting** - International Conference on Embedded Computer Systems: Architectures, Modeling and Simulatios - SAMOS XXIII

DISSERTATION THESIS

- **Production of reliable C code energy dataset on Basic Block level**, used LLVM , intel-rapl , intel-perf, to correspond basic blocks of C source code to energy consumption . The dataset will be used to train energy predicting neural network with green computing and energy efficiency purposes.
- **Genetic improvement library for software optimization**: library for integrating different metrics in the fitness function during the automatic optimization of Software using Genetic Improvement (currently underway)

NOTABLE PROJECTS

- **LLVM Verification tool for C function autocompletions** using symbolic execution and static analysis
- **Parameter Optimization and Rightsizing Models** for the **Green Software Foundation** Project Impact Engine Framework (Team Leader), in collaboration with **NTT Data**
- **Custom OS implementation from scratch for Armv8-A** , implemented on Raspberry Pi 3
- **Distributed Neural Network Training Pipeline** , using Tensorflow
- **Beta prototype of smart phone app**, processing user data and reviews and calculating danger level of public spaces for COVID-19 transmission
- **Electric car charging Station system**, user interface, cli and backend development
- **Home Security system with password protection** , using EasyAVR6 board
- **Parametrized Debayering Filter design for ZYNQ SoC FPGA** , using Xilinx Vivado - ISE

LANGUAGES – OTHER CERTIFICATES

- **English** (fluent - C2), **French** (intermediate - B2), **Greek** (native)