

Format (little endian)	# bytes	# codes	description		detailed description	comments
xxx00000x	1	2	one byte instructions, 00000000 and 11111111 are illegal op-codes			
nnnnnnnnn nnn000xxx	2	6	Two byte instructions which take a 12-bit immediate value			CALLR, BR, BRZ, BRNZ, LDI, POSTFX
ssmddnnn rPRxxxxxx	2	64 - 8	generic two byte instruction			
1111nnnnn rPVxxxxxx	2		two byte instruction with immediate value			
xxxxddnnn ssmddnnn rPRxxxxxx	3	10	three byte instructions		no ssmm on 3rd byte, is used for inst modifiers	Three operand instructions and for accessing residue register
1111ffff ffsssss			Return stack entry for restored frame and stack pointers		A return address of 1111xxxx xxxxxx not allowed	RTNF same as RTN and requires double read of return stack