**IEEE Lone Star Computer chapter March meeting**

**The Small Chip Landscape:**

Abstract:

Talk will cover what’s available in “tiny” integrated circuits and other related components: in many cases smaller than a cubic millimeter.

Small is good: less material, lower power, compact and low cost.

A review of the standard chip footprints along with actual samples.

A review of the smallest chips in each category.

Some new aspects of circuit board assembly: replacement of solder masks by ink-jet printing, digital tweezers and the USB microscope.

Some comments on the number of transistors per square millimeter and the prospects for even smaller chips.

Speaker: Jim Brakefield

Biography:

Semi-retired with a career in instrumentation, embedded software development, circuit board design, FPGAs, architect and project lead.

Jim has a Bachelor’s degree in applied math and Masters degrees in Computer Science and Electrical Engineering all from University of Wisconsin Madison.