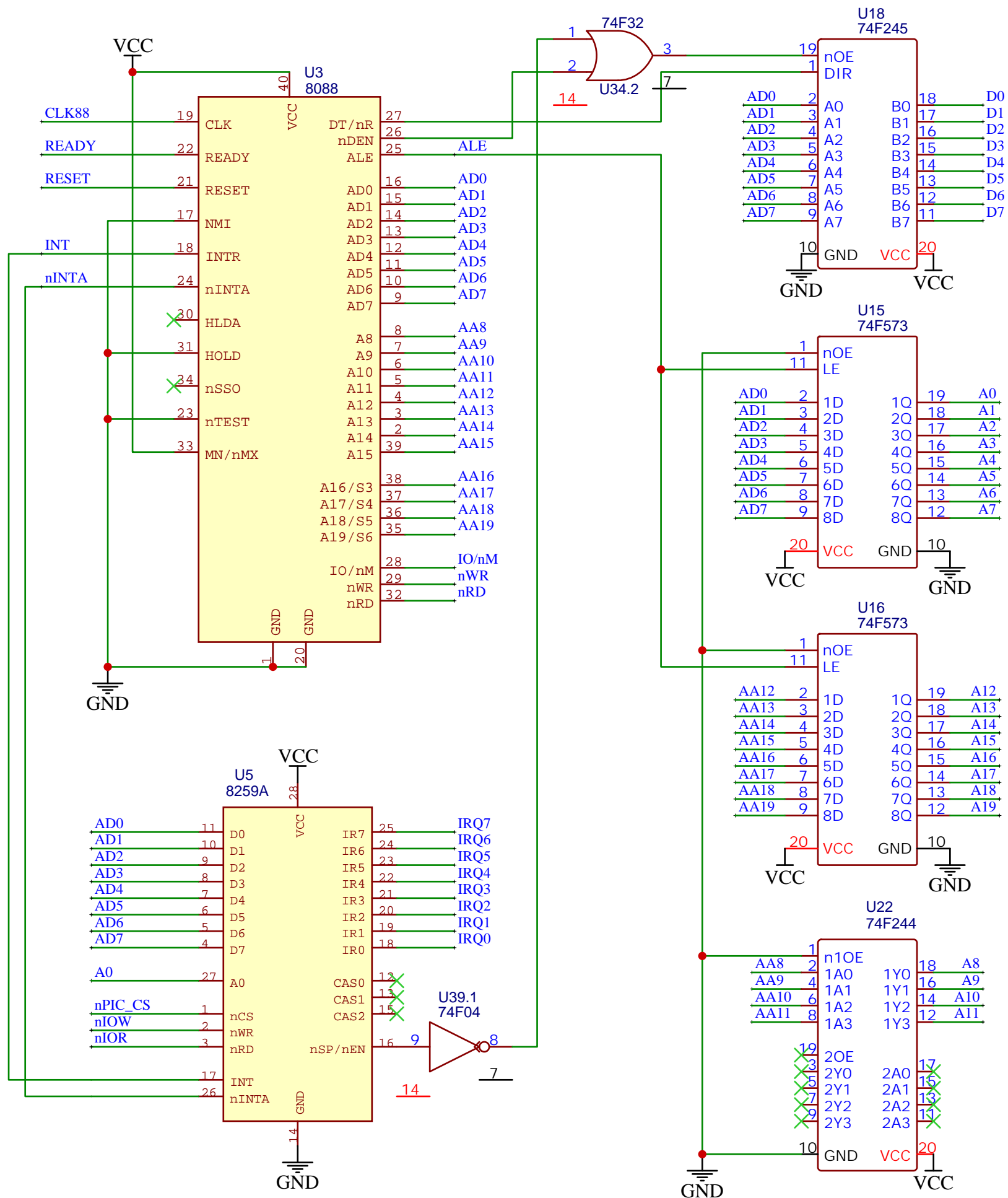
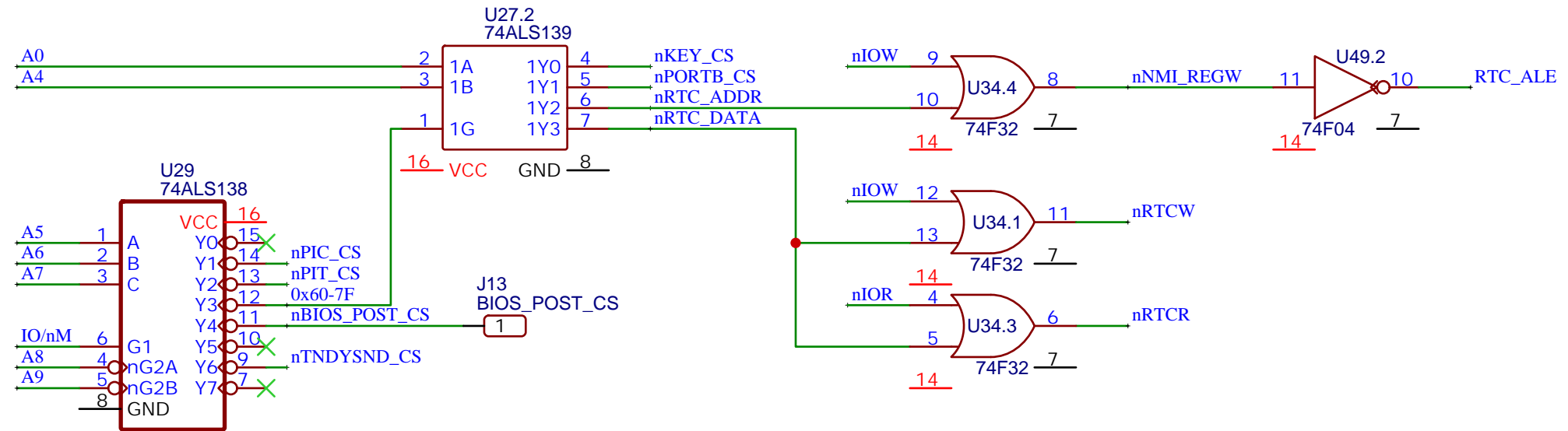


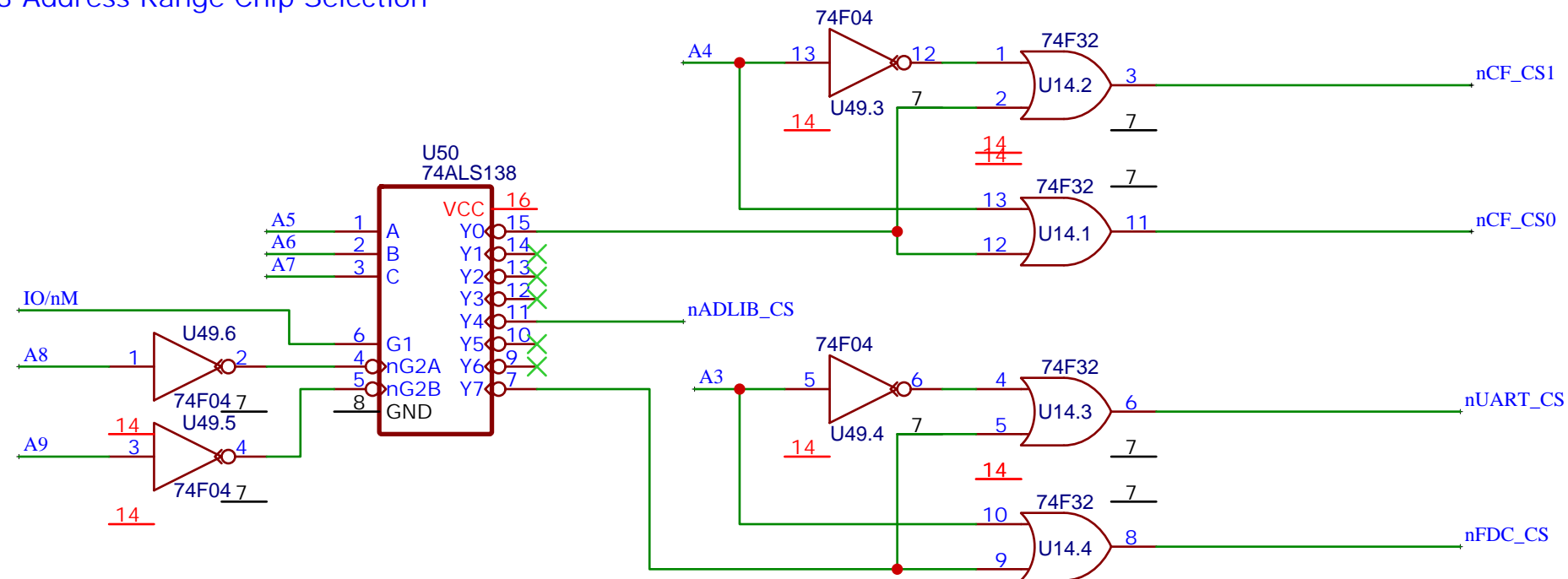
TITLE: XTjr 1. Clock and Reset Logic		REV: 1.0
EasyEDA	Company: JamSoft	Sheet: 1/1
	Date: 2021-01-27	Drawn By: Chris Jamieson

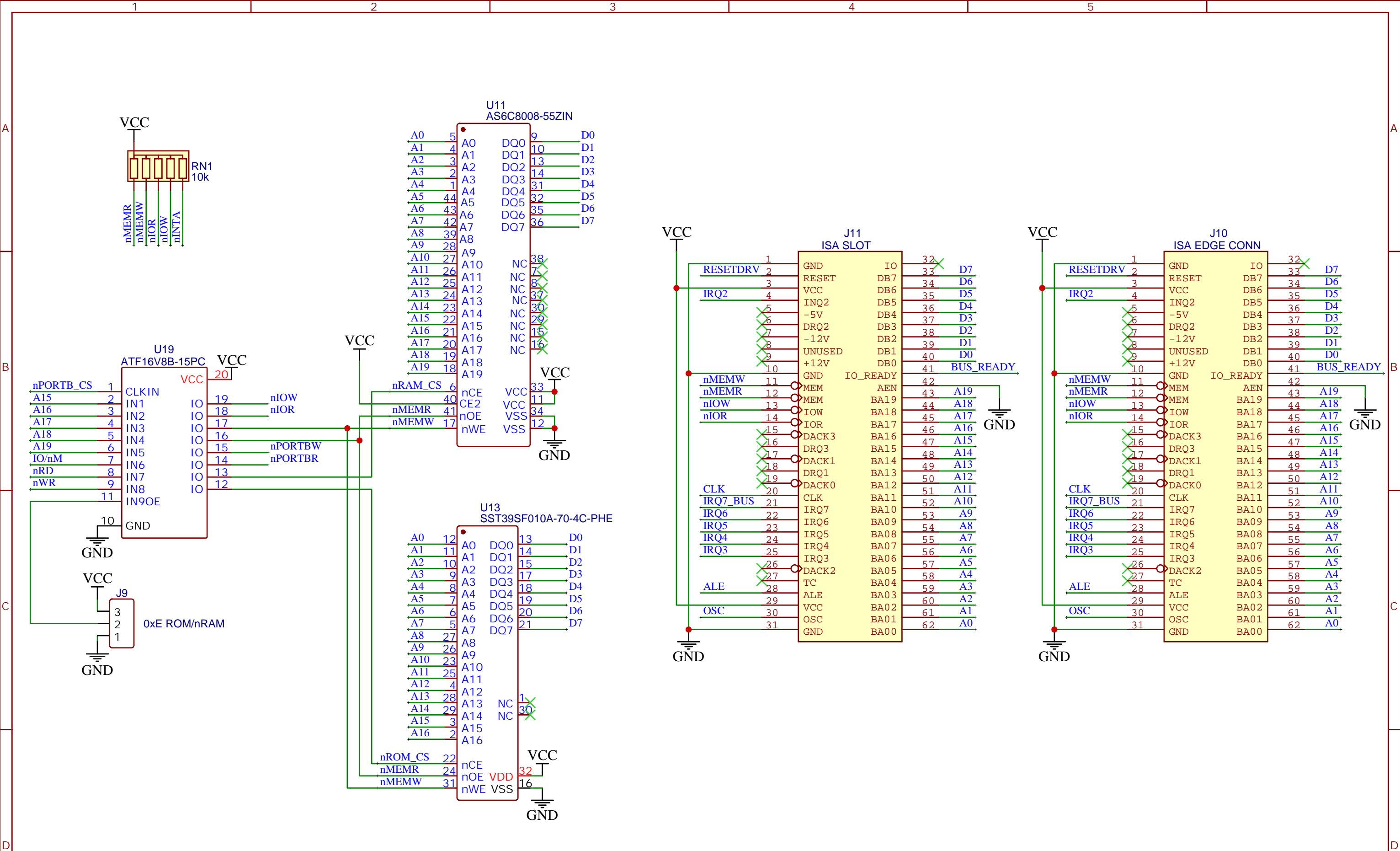


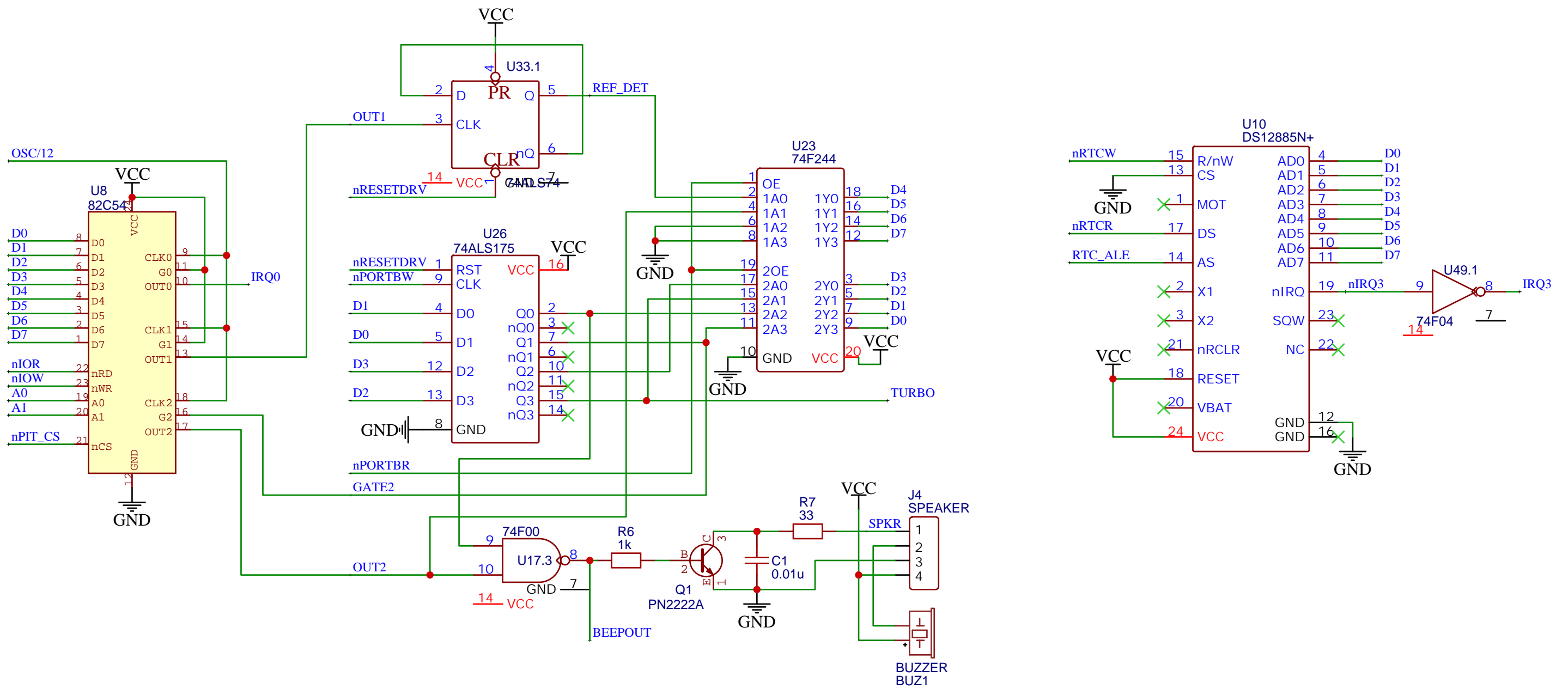
0x0 Address Range Chip Selection



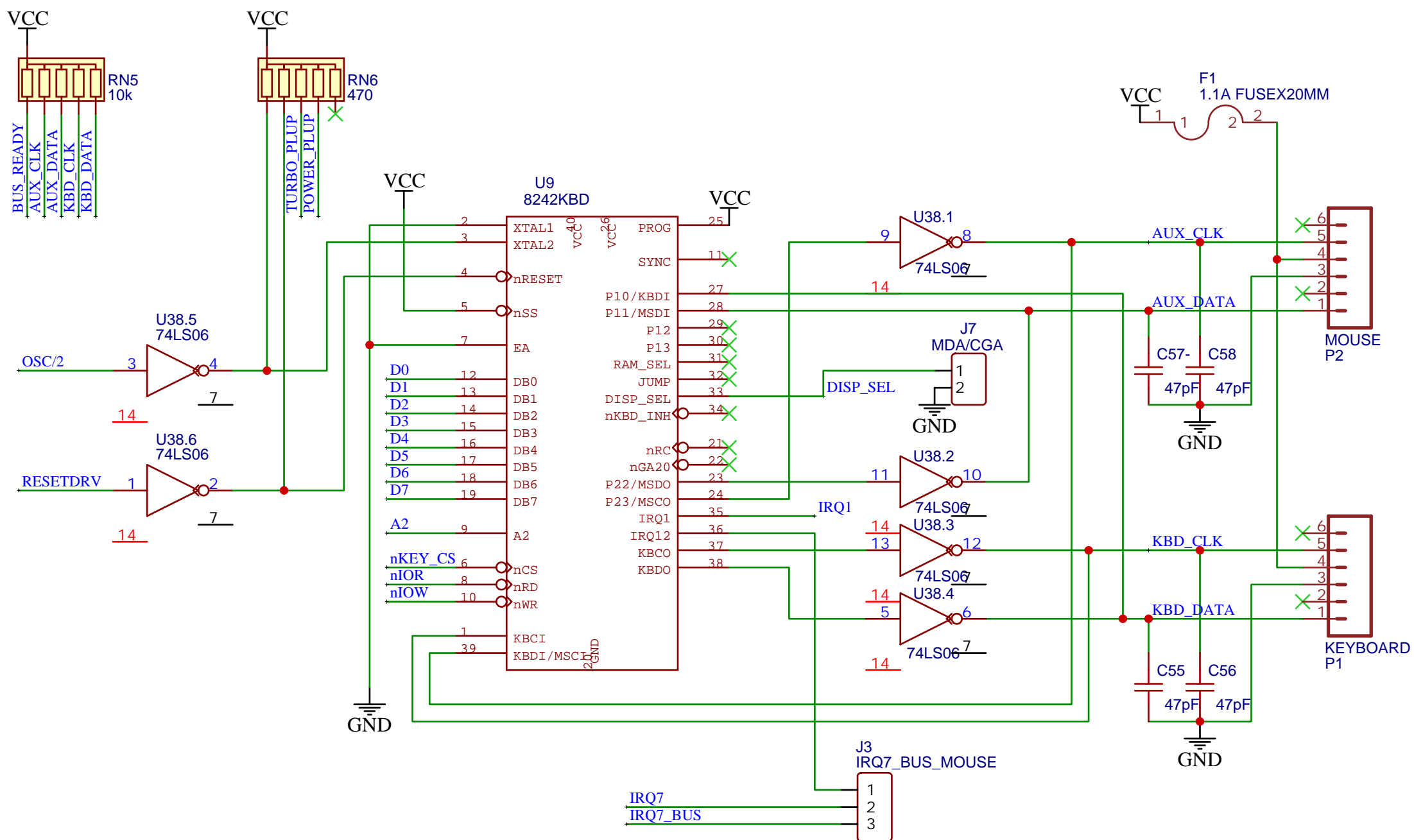
0x3 Address Range Chip Selection







TITLE: XTjr 5. Devices - PIT, PortB, RTC/NVRAM		REV: 1.0
EasyEDA	Company: Your Company	Sheet: 1/1
	Date: 2021-02-14	Drawn By: Chris Jamieson



TITLE: XTjr 6. Keyboard Controller		REV: 1.0
EasyEDA	Company: JamSoft	Sheet: 1/1
	Date: 2021-02-16	Drawn By: Chris Jamieson

