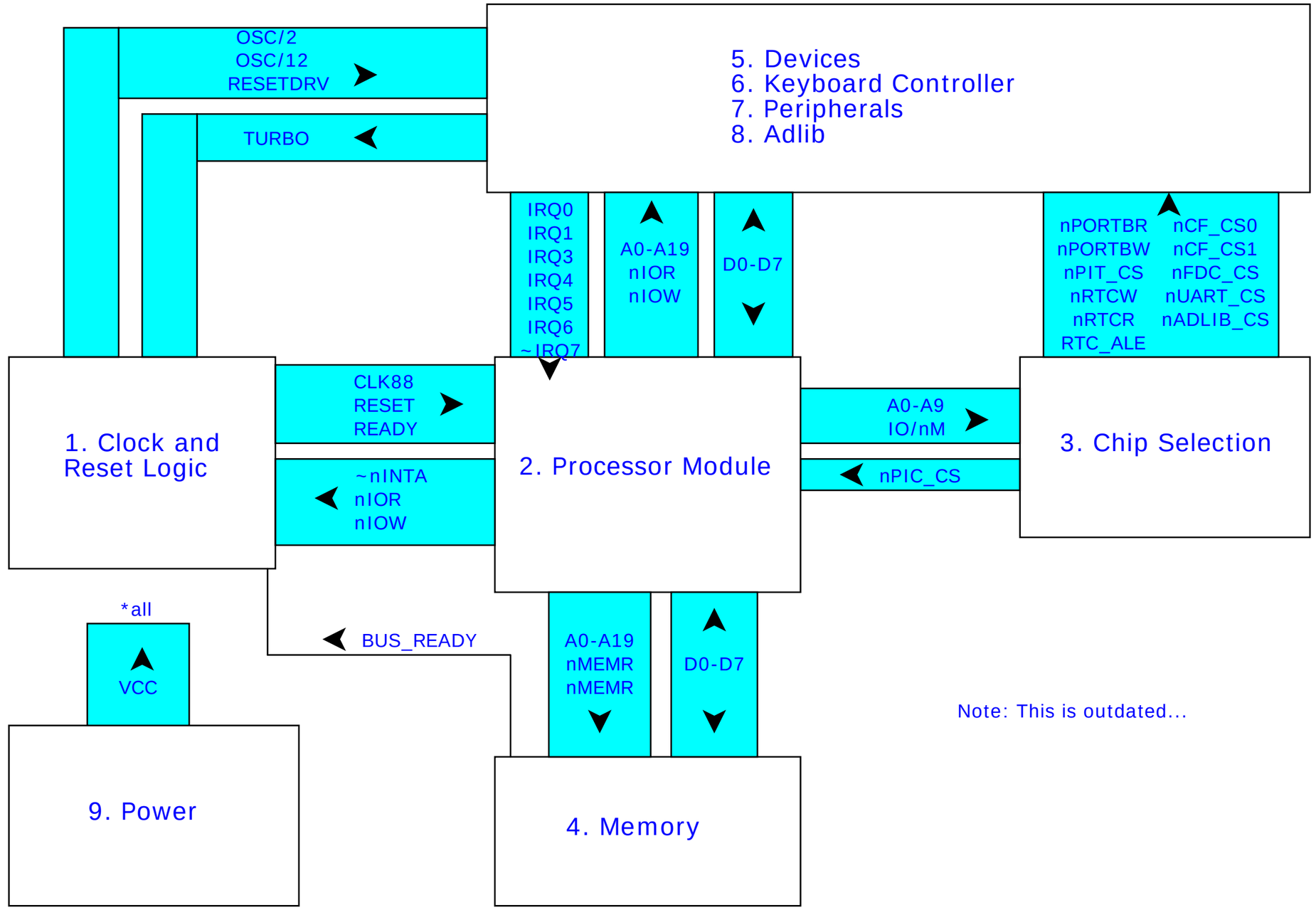
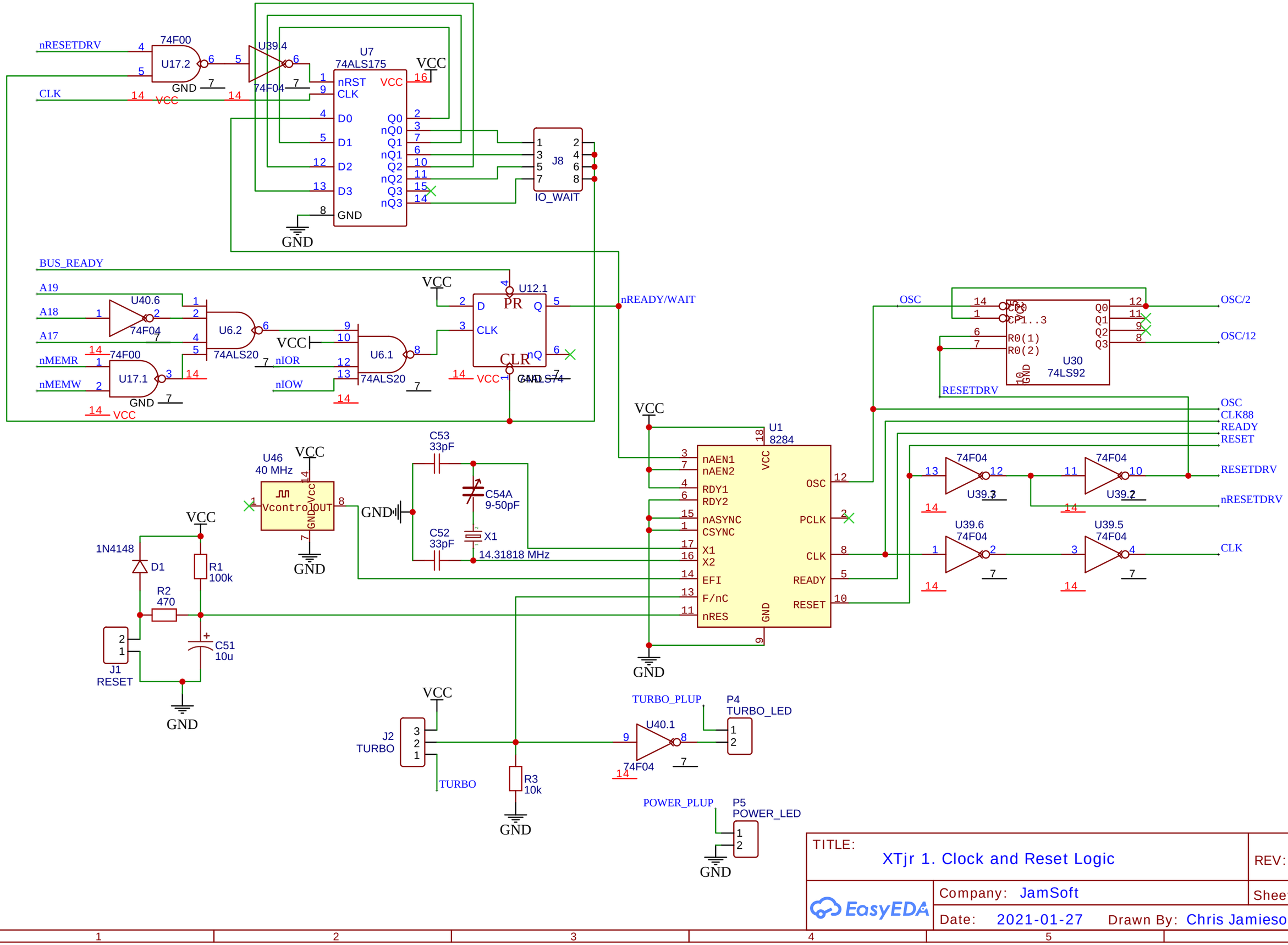


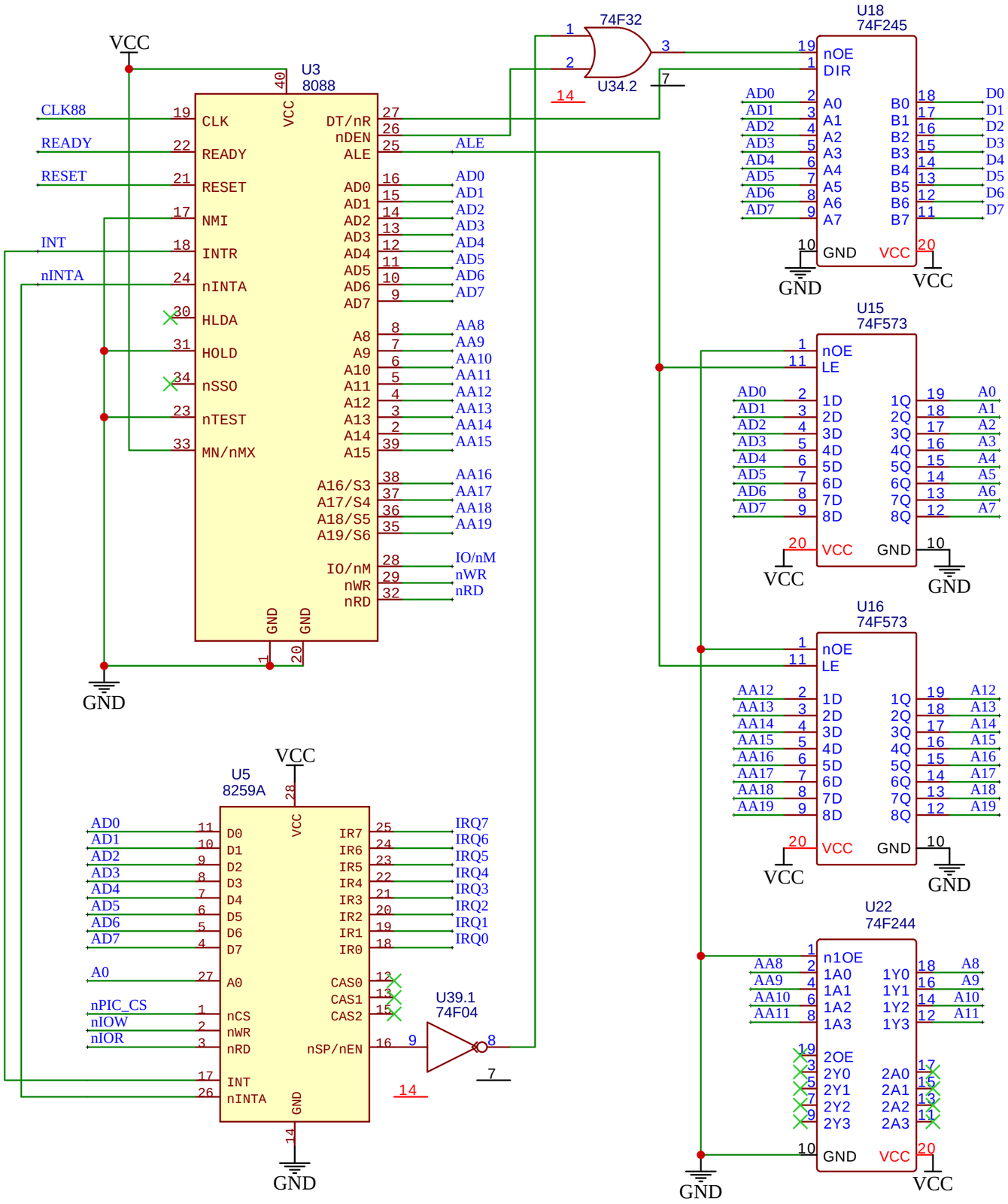
Note: This is outdated...

~ Optional

TITLE: System Block Diagram		REV: 1.0
EasyEDA	Company: JamSoft	Sheet: 1/1
	Date: 2021-02-02 Drawn By: Chris Jamieson	

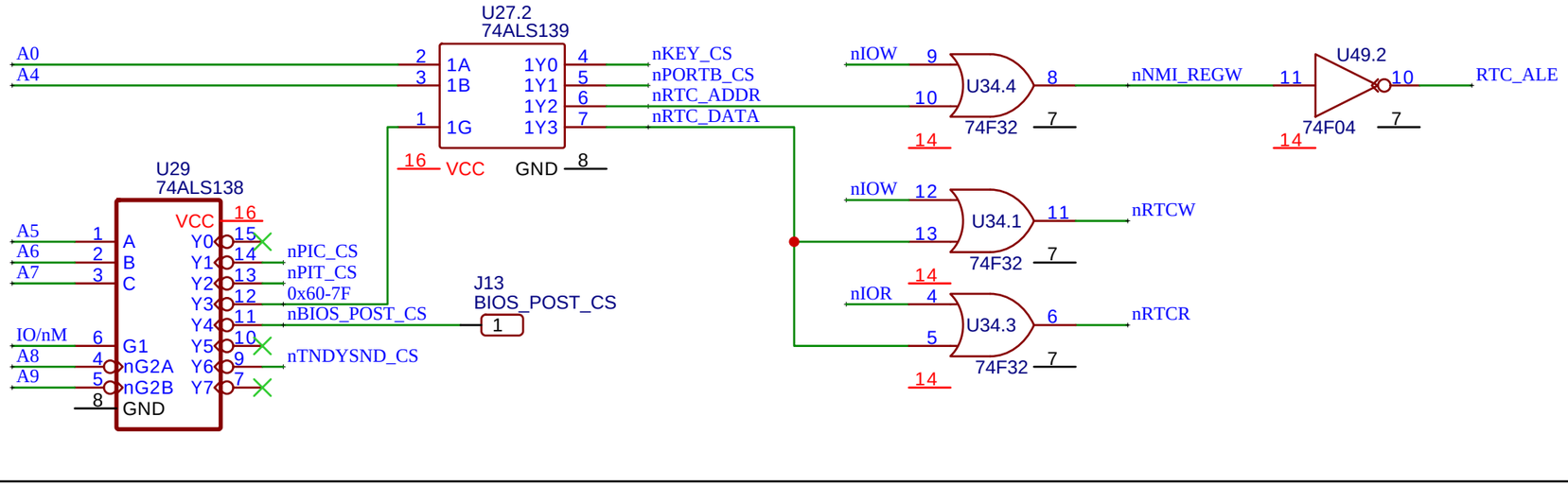


TITLE: XTjr 1. Clock and Reset Logic			REV: 1.0
EasyEDA	Company: JamSoft		Sheet: 1/1
	Date: 2021-01-27	Drawn By: Chris Jamieson	

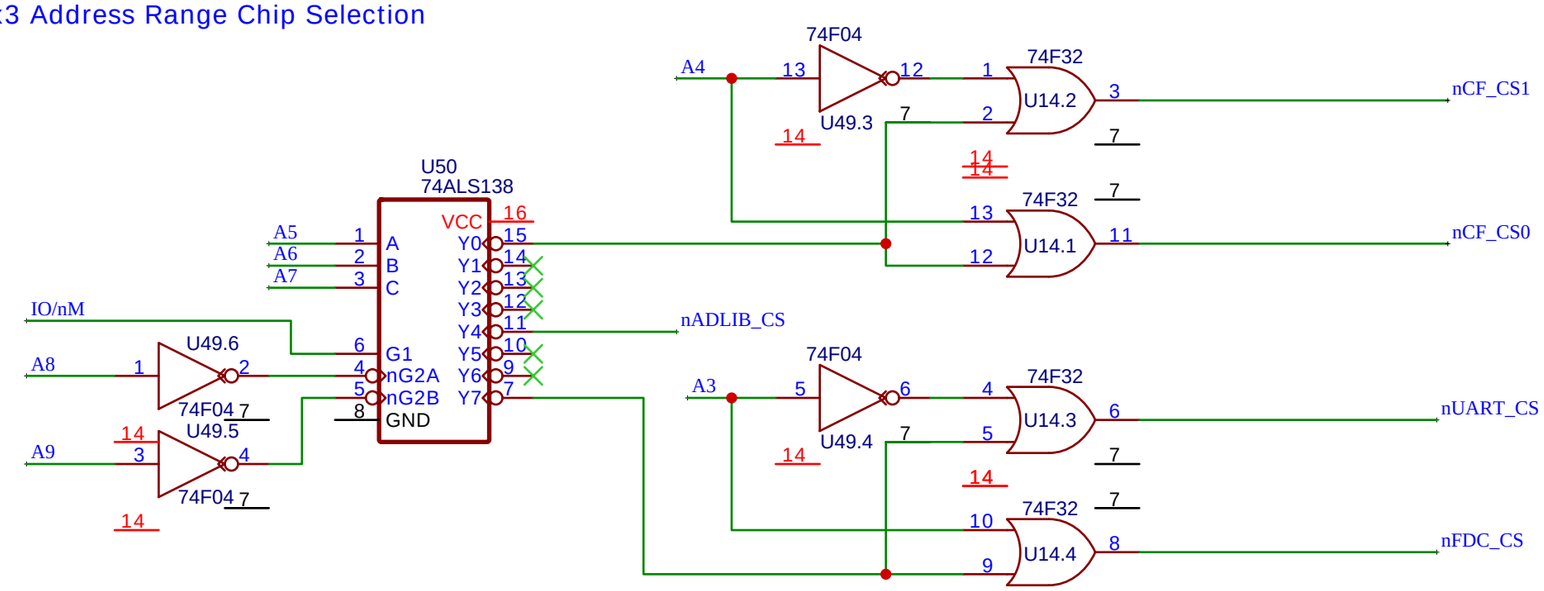


TITLE: XTjr 2. Processor and PIC		REV: 1.0
EasyEDA	Company: JamSoft	Sheet: 1/1
	Date: 2021-02-01 Drawn By: Chris Jamieson	

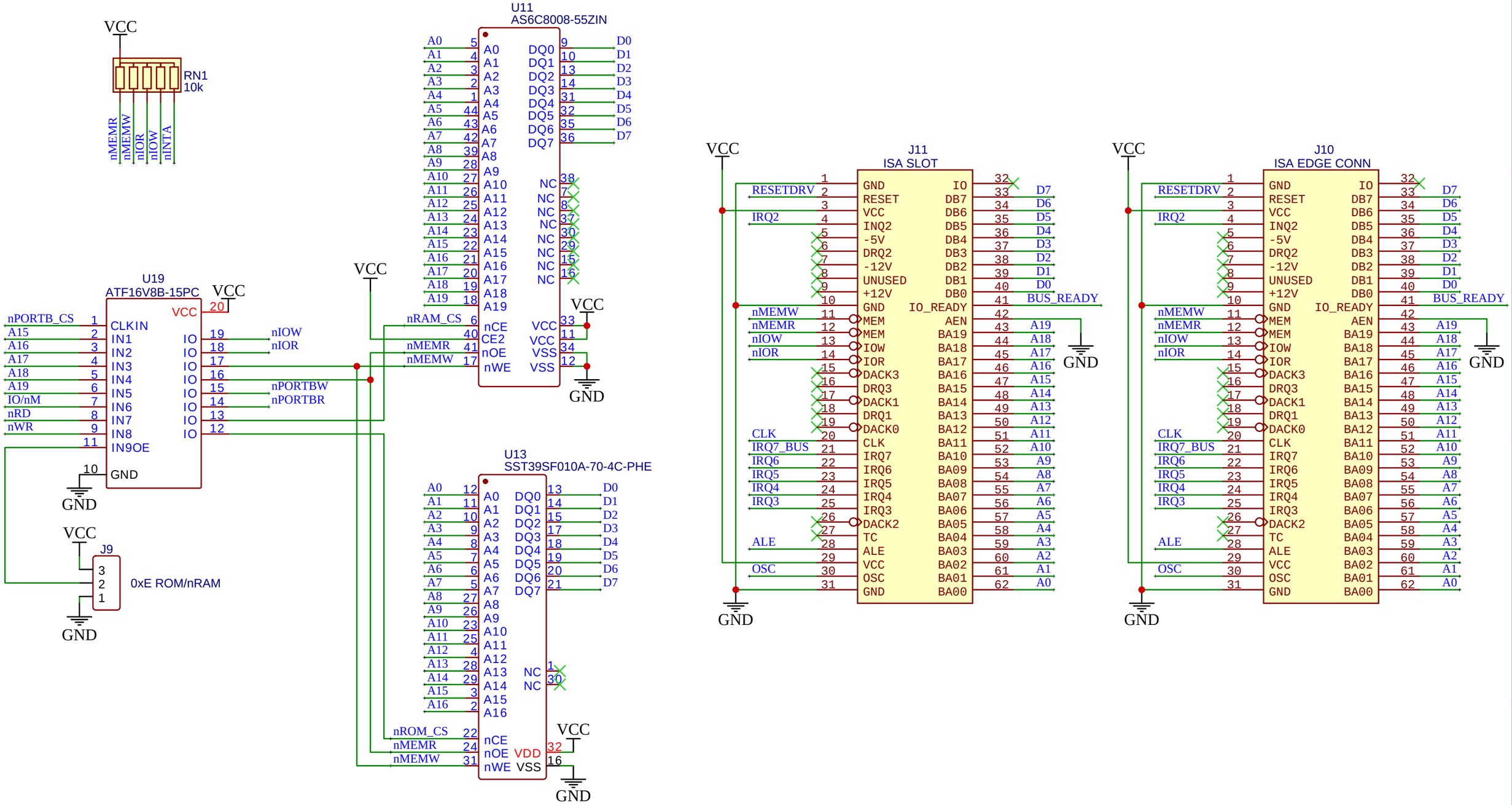
0x0 Address Range Chip Selection



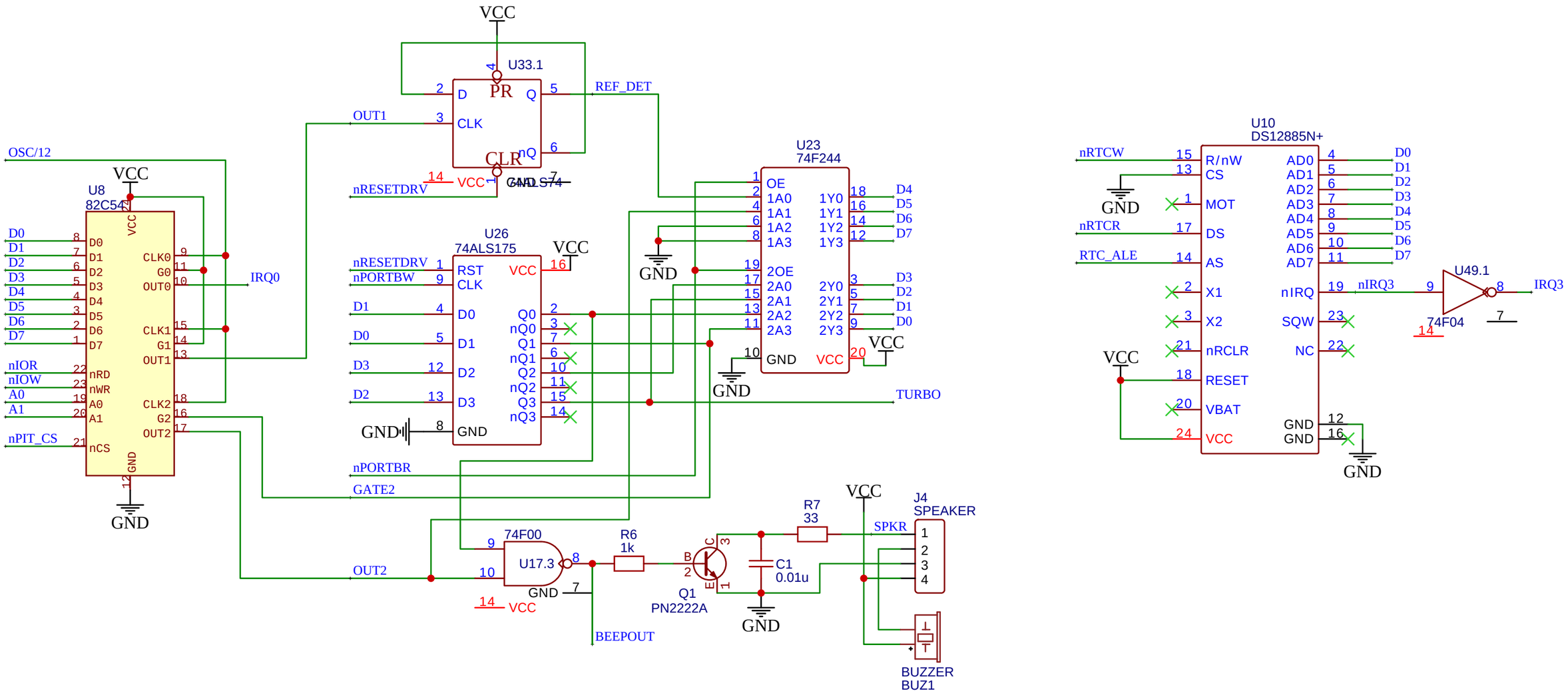
0x3 Address Range Chip Selection



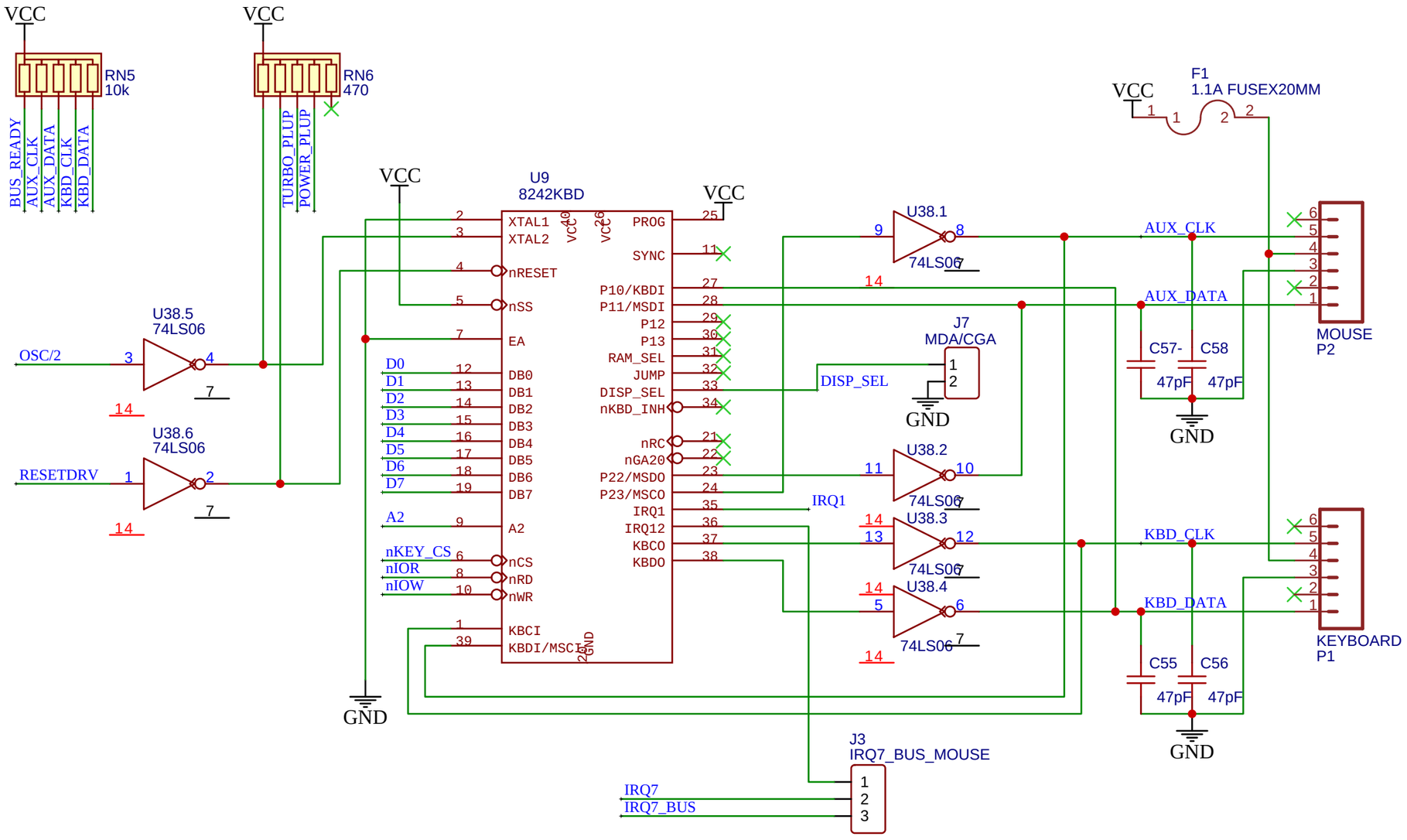
TITLE: XTjr 3. Chip Selection		REV: 1.0
EasyEDA	Company: JamSoft	Sheet: 1/1
	Date: 2021-02-09 Drawn By: Chris Jamieson	



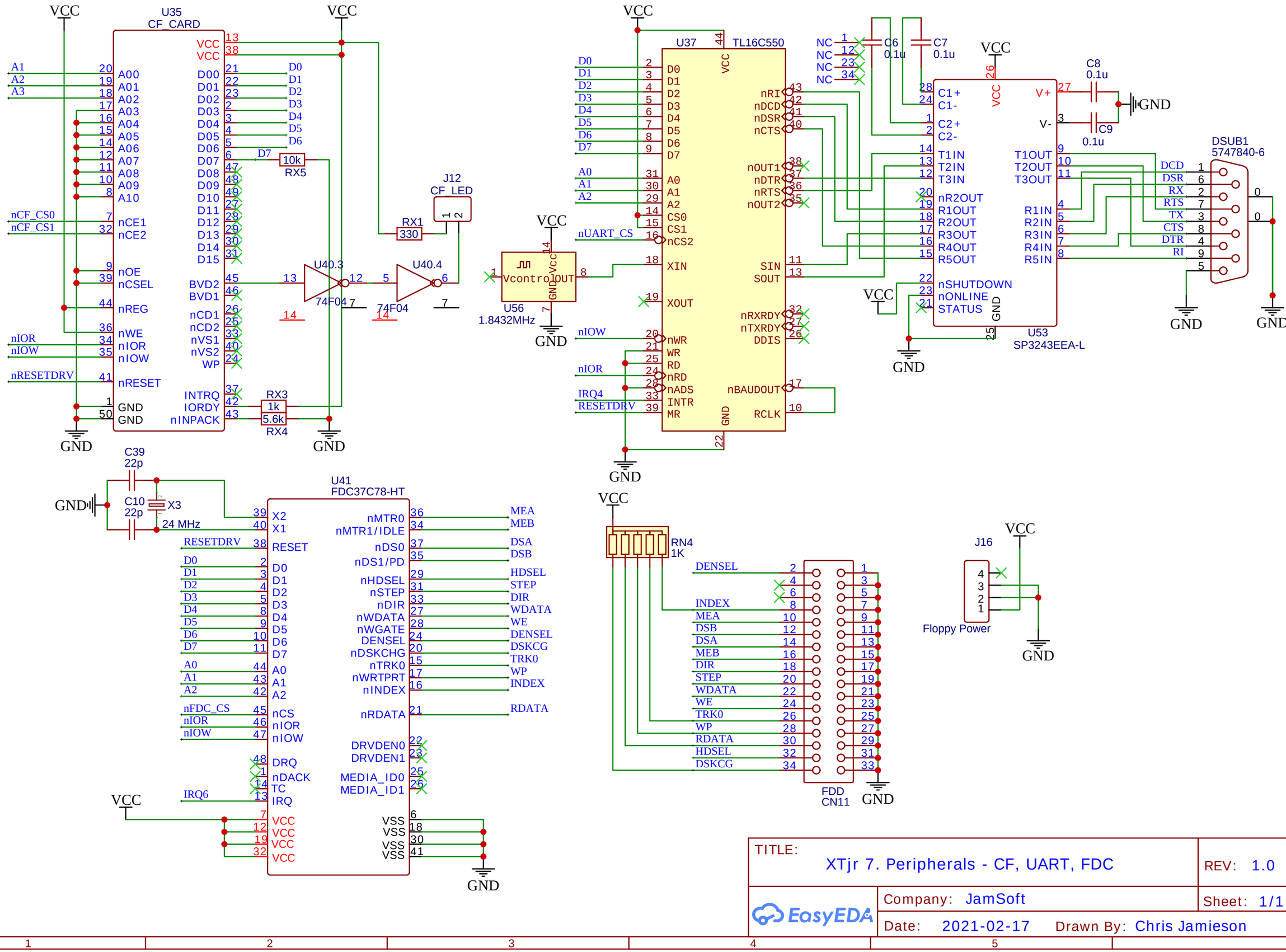
TITLE: XTjr 4. Memory and Bus Ports		REV: 1.0
EasyEDA	Company: JamSoft	Sheet: 1/1
	Date: 2021-02-13 Drawn By: Chris Jamieson	



TITLE: XTjr 5. Devices - PIT, PortB, RTC/NVRAM		REV: 1.0
EasyEDA	Company: Your Company	Sheet: 1/1
	Date: 2021-02-14 Drawn By: Chris Jamieson	

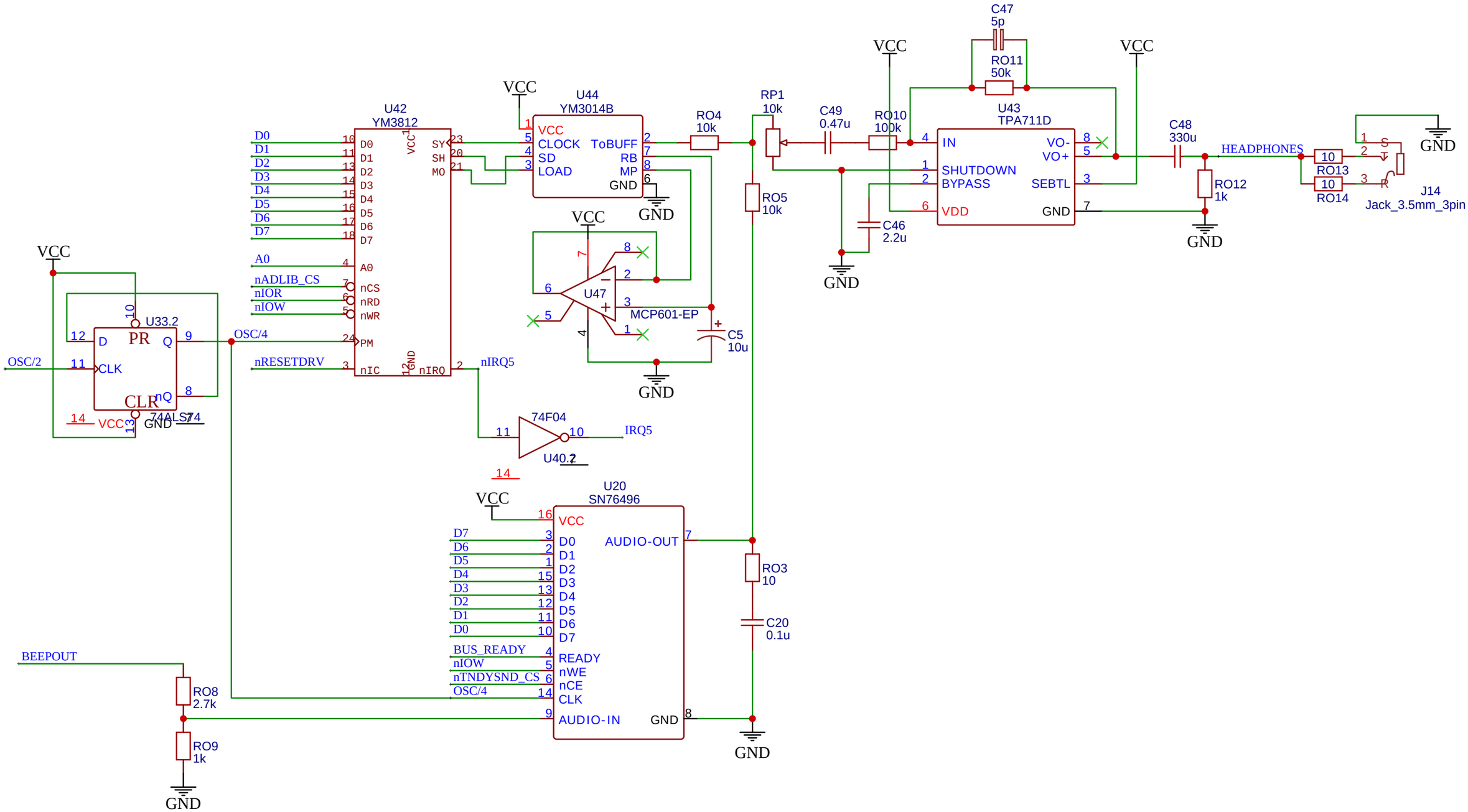


TITLE: XTjr 6. Keyboard Controller			REV: 1.0
EasyEDA	Company: JamSoft		Sheet: 1/1
	Date: 2021-02-16	Drawn By: Chris Jamieson	



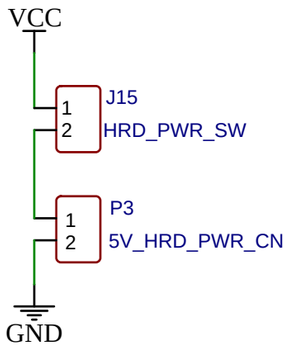
TITLE:	XTjr 7. Peripherals - CF, UART, FDC
REV:	1.0
Company:	JamSoft
Sheet:	1/1
Date:	2021-02-17
Drawn By:	Chris Jamieson

EasyEDA

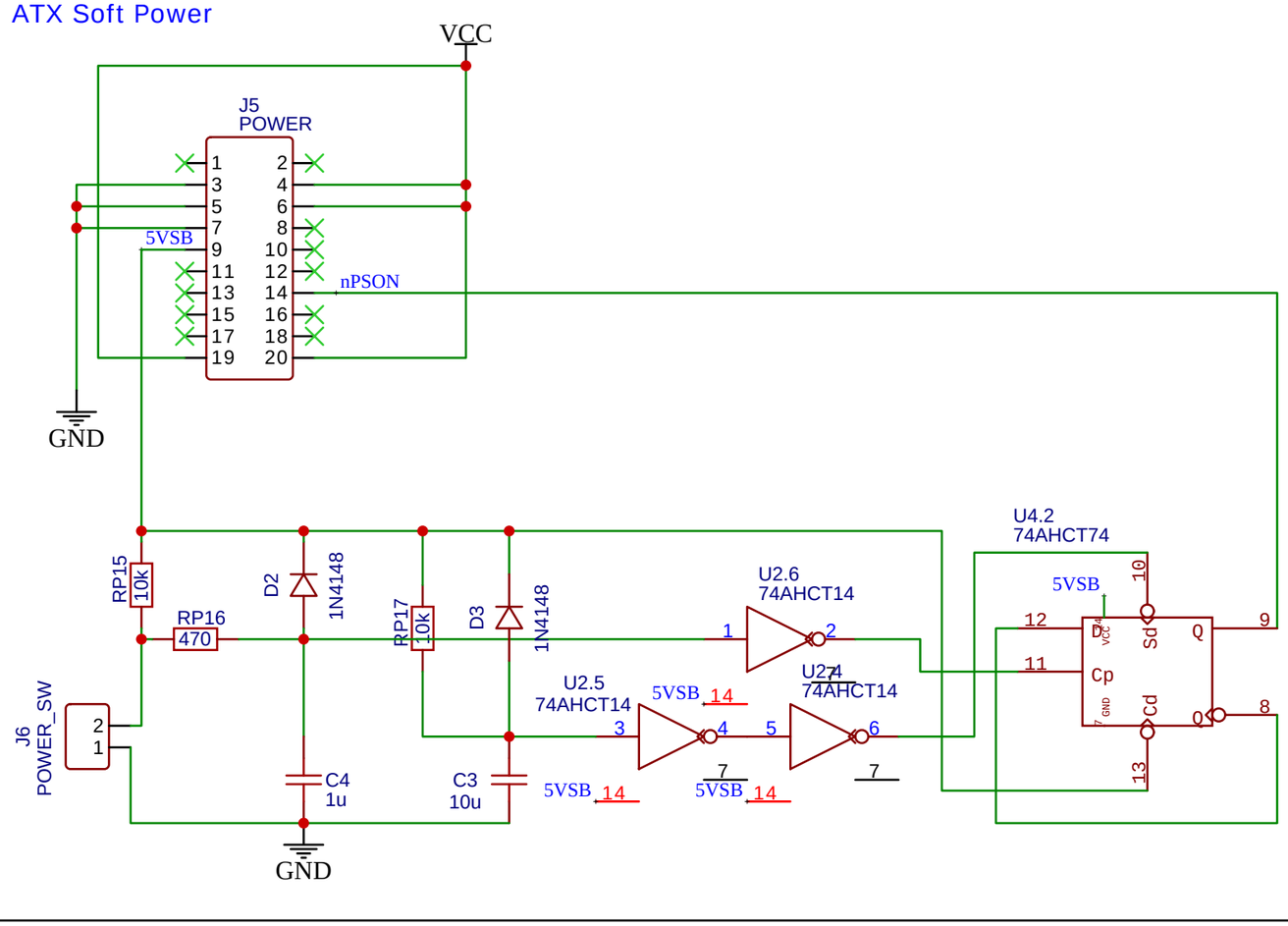


TITLE: XTjr 8. Audio - OLP2 & Tandy Sound			REV: 1.0
EasyEDA	Company: JamSoft		Sheet: 1/1
	Date: 2021-02-23	Drawn By: Chris Jamieson	

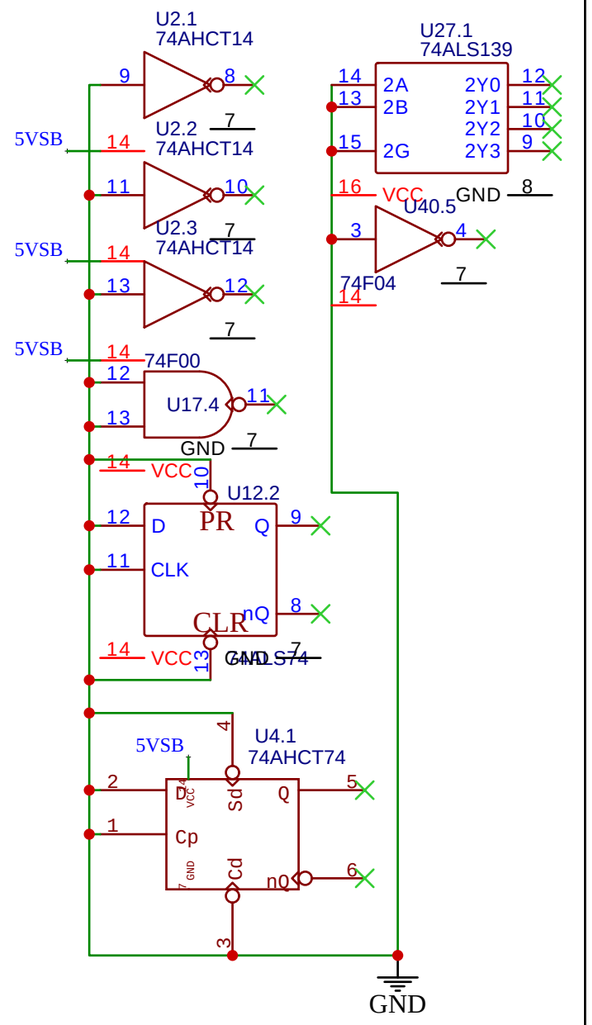
5V Hard Power

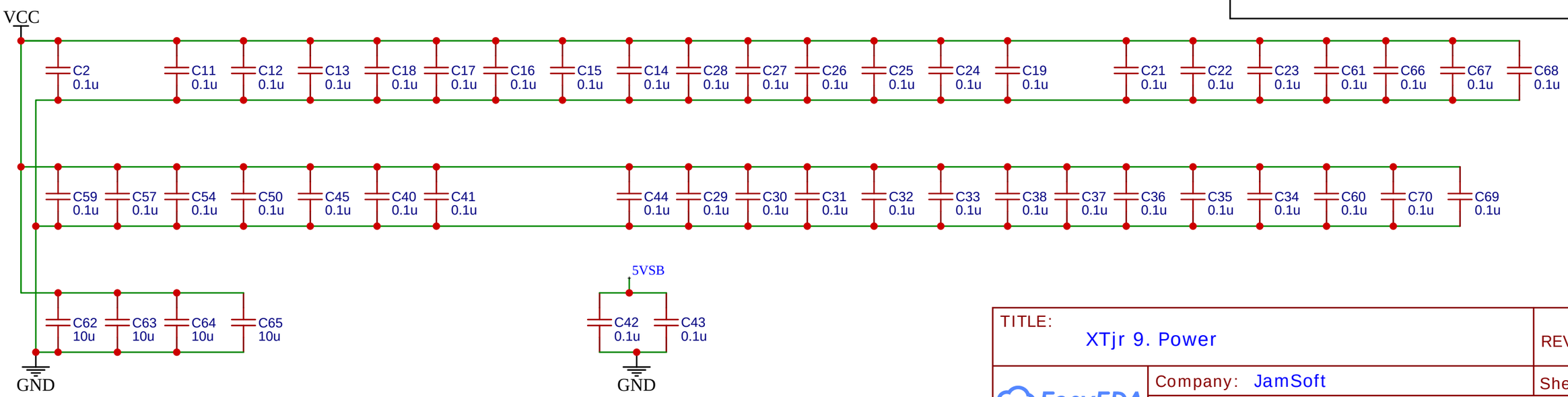


ATX Soft Power



Unused Gates





TITLE: XTjr 9. Power		REV: 1.0
EasyEDA	Company: JamSoft	Sheet: 1/1
	Date: 2021-02-25 Drawn By: Chris Jamieson	