|  |  |  |  |
| --- | --- | --- | --- |
| FSM name : | Example.v | Date: | Sept 2020 |
| Section : | 1 of 1 | Author | Jim Herd |
| Notes : | Word layout for FSM diagrams | | |

|  |  |  |  |
| --- | --- | --- | --- |
| **States** | **States** | **Outputs** | **Notes** |
|  |  |  | Initial state. |
|  |  | **handshake\_1 = 1** |  |
|  | **S\_RD1** |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |

**S\_RD1**

State Objects

|  |  |
| --- | --- |
| **States** | Notes |
| S\_RD1 |  |
| S\_RD2 |  |
| S\_RD3 |  |
| S\_RD4 |  |
|  |  |
|  |  |
|  |  |

Input objects

|  |  |  |
| --- | --- | --- |
| **Inputs** | Type | Notes |
| bus\_RW |  |  |
| IN\_a |  |  |
|  |  |  |
|  |  |  |
|  |  |  |
|  |  |  |
|  |  |  |

Output objects

|  |  |  |
| --- | --- | --- |
| **Outputs** | Type | Notes |
| handshake\_1 |  |  |
|  |  |  |
|  |  |  |
|  |  |  |
|  |  |  |
|  |  |  |
|  |  |  |