(0)	CODE -> VDECL CODE
(1)	CODE -> FDECL CODE
(2)	CODE -> CDECL CODE
(3)	CODE -> ' '
(4)	VDECL -> vtype id semi
(5)	VDECL -> vtype ASSIGN semi
(6)	ASSIGN -> id assign RHS
(7)	RHS -> EXPR
(8)	RHS -> literal
(9)	RHS -> character
(10)	RHS -> boolstr
(11)	EXPR -> EXPR addsub EXPR
(12)	EXPR -> EXPR multdiv EXPR
(13)	EXPR -> lparen EXPR rparen
(14)	EXPR -> id
(15)	EXPR -> num
(16)	FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN
(17)	rbrace
(18)	ARG -> vtype id MOREARGS
(19)	ARG -> ' '
(20)	MOREARGS -> comma vtype id MOREARGS
(21)	MOREARGS -> ' '
(22)	BLOCK -> STMT BLOCK
(23)	BLOCK -> ' '
(24)	STMT -> VDECL
(25)	STMT -> ASSIGN semi
(26)	STMT -> if lparen COND rparen lbrace BLOCK rbrace ELSE
(27)	STMT -> while <u>lparen</u> COND <u>rparen</u> <u>lbrace</u> BLOCK <u>rbrace</u>
(28)	COND -> COND comp COND
(29)	COND -> boolstr
(30)	ELSE -> else lbrace BLOCK rbrace
(31)	ELSE -> ' '
(32)	RETURN -> return RHS semi
(33)	CDECL -> class id lbrace ODECL rbrace
(34)	ODECL -> VDECL ODECL
(35)	ODECL -> FDECL ODECL
	ODECL -> ' '

FIRST / FOLLOW table											
Nonterminal	FIRST	FOLLOW									
CODE	{',vtype,class}	{\$}									
VDECL	{vtype}	{',vtype,class,id,if,while}									
ASSIGN	{id}	{semi}									
RHS	{literal,character,boolstr,lparen,id,num}	{semi}									
EXPR	{lparen,id,num}	{semi,addsub,multdiv,rparen}									
FDECL	{vtype}	{',vtype,class}									
ARG	{vtype,'}	{rparen}									
MOREARGS	{comma,'}	{rparen}									
BLOCK	{',vtype,id,if,while}	{return,rbrace}									
STMT	{vtype,id,if,while}	{',vtype,id,if,while}									
COND	{boolstr}	{rparen,comp}									
ELSE	{else,'}	{',vtype,id,if,while}									
RETURN	{return}	{rbrace}									
CDECL	{class}	{',vtype,class}									
ODECL	{vtype,'}	{rbrace}									

>			SLR closure table
	0, VDECL)	{CODE -> .VDECL CODE} {CODE -> VDECL.CODE}	Closure CODE -> .VDECL CODE; VDECL -> .vtype id semi; VDECL -> .vtype ASSIGN semi} CODE -> VDECL.CODE; CODE -> .VDECL CODE; CODE -> .CDECL CODE; CODE -> .vtype id semi; VDECL -> .vtype id sem
goto goto	1, CODE) 1, VDECL)	<pre>{VDECL -> vtype.id semi; VDECL -> vtype.ASSIGN semi} {CODE -> VDECL CODE.} {CODE -> VDECL.CODE}</pre>	<pre>{VDECL -> vtype.id semi; VDECL -> vtype.ASSIGN semi; ASSIGN -> .id assign RHS} {CODE -> VDECL CODE.}</pre>
goto goto	1, CDECL) 1, ')	{CODE -> FDECL.CODE} {CODE -> CDECL.CODE} {CODE -> '.'}	{CODE -> FDECL CODE; CODE -> .VDECL CODE; CODE -> .CDECL CODE; CODE -> .CDECL CODE; CODE -> .' '; VDECL -> .vtype id semi; VDECL -> .vtype id lparen ARG rparen lbrace BLOCK RETURN rbrace; CDECL -> .class id lbrace ODECL rbrace} {CODE -> CDECL CODE; CODE -> .VDECL CODE; CODE -> .CDECL CODE; CODE -> .' '; VDECL -> .vtype id semi; VDECL -> .vtype id lparen ARG rparen lbrace BLOCK RETURN rbrace; CDECL -> .class id lbrace ODECL rbrace} {CODE -> '.'} {CODE -> '.'}
goto	1, class)	<pre>{VDECL -> vtype.id semi; VDECL -> vtype.ASSIGN semi; FDECL -> vtype.id lparen ARG rparen lbrace BLOCK RETURN rbrace} [7] {CDECL -> class.id lbrace ODECL rbrace} {VDECL -> vtype id.semi; ASSIGN -> id.assign RHS}</pre>	{VDECL -> vtype.id semi; VDECL -> vtype.ASSIGN semi; FDECL -> vtype.id lparen ARG rparen lbrace; ASSIGN -> .id assign RHS} {CDECL -> class.id lbrace ODECL rbrace} {VDECL -> vtype id.semi; ASSIGN -> id.assign RHS}
goto	4, CODE)		<pre>{VDECL -> vtype ASSIGN.semi} CODE -> FDECL CODE.} </pre>
goto goto	4, FDECL) 4, CDECL)		
goto goto	4, vtype) 4, class)	{VDECL -> vtype.id semi; VDECL -> vtype.ASSIGN semi; FDECL -> vtype.id lparen ARG rparen lbrace BLOCK RETURN rbrace} {CDECL -> class.id lbrace ODECL rbrace}	CODE -> CDECL CODE.}
goto goto	5, VDECL) 5, FDECL)	{CODE -> VDECL.CODE} {CODE -> FDECL.CODE} {CODE -> CDECL.CODE}	
goto goto	5, ') 5, vtype)	{CODE -> '.'} {VDECL -> vtype.id semi; VDECL -> vtype.ASSIGN semi; FDECL -> vtype.id lparen ARG rparen lbrace BLOCK RETURN rbrace} {CDECL -> class.id lbrace ODECL rbrace}	
goto goto	6, ') 7, id)	{CODE -> ' '.}	{CODE -> ' '.} {VDECL -> vtype id.semi; FDECL -> vtype id.lparen ARG rparen lbrace BLOCK RETURN rbrace; ASSIGN -> id.assign RHS}
goto goto	8, id) 9, semi)	{CDECL -> class id.lbrace ODECL rbrace} {VDECL -> vtype id semi.}	CDECL -> class id.lbrace ODECL rbrace} [CDECL -> vtype id semi.] [VDECL -> vtype id semi.] [ASSIGN -> id assign.RHS; RHS -> .EXPR; RHS -> .literal; RHS -> .character; RHS -> .boolstr; EXPR -> .EXPR addsub EXPR; EXPR -> .laren EXPR rparen; EXPR -> .id; EXPR -> .num}
goto goto	10, semi) 14, semi)	{VDECL -> vtype ASSIGN semi.} {VDECL -> vtype id semi.}	Representation of the state o
goto goto	14, assign) 15, lbrace)	{ASSIGN -> id assign.RHS} {CDECL -> class id lbrace.ODECL rbrace}	CDECL -> class id lbrace.ODECL rbrace; ODECL -> .VDECL ODECL; ODECL -> .' '; VDECL -> .vtype id semi; VDECL -> .vtype id lparen ARG rparen lbrace BLOCK RETURN rbrace} [ASSIGN -> id assign RHS.]
goto goto	17, EXPR) 17, literal)	{RHS -> EXPR.; EXPR -> EXPR.addsub EXPR; EXPR -> EXPR.multdiv EXPR} {RHS -> literal.}	RHS -> EXPR.; EXPR -> EXPR.addsub EXPR; EXPR -> EXPR.multdiv EXPR} RHS -> literal.} RHS -> character.}
goto	17, boolstr) 17, lparen)	<pre>{RHS -> boolstr.} {EXPR -> lparen.EXPR rparen}</pre>	<pre>{RHS -> boolstr.} (EXPR -> lparen.EXPR rparen; EXPR -> .EXPR addsub EXPR; EXPR -> .lparen EXPR rparen; EXPR -> .id; EXPR -> .num} (EXPR -> id.)</pre>
goto goto	17, num) 19, ARG)	{EXPR -> num.} {FDECL -> vtype id lparen ARG.rparen lbrace BLOCK RETURN rbrace}	<pre>EXPR -> num.} EXPR -> num.} E</pre>
goto goto	19, ') 20, ODECL)	{ARG -> '.'} {CDECL -> class id lbrace ODECL.rbrace}	ARG -> '.'} [ARG -> '.'] [CDECL -> class id lbrace ODECL.rbrace] [ODECL -> VDECL.ODECL; ODECL -> .FDECL ODECL; ODECL -> .' '; VDECL -> .vtype id semi; FDECL -> .vtype id lparen ARG rparen lbrace BLOCK RETURN rbrace]
goto	20, FDECL) 20, ')	{ODECL -> FDECL.ODECL}	{ODECL -> FDECL.ODECL; ODECL -> .VDECL ODECL; ODECL -> .' '; VDECL -> .vtype id semi; VDECL -> .vtype id lparen ARG rparen lbrace BLOCK RETURN rbrace} {ODECL -> '.'}
goto	22, addsub) 22, multdiv)	{EXPR -> EXPR addsub.EXPR} {EXPR -> EXPR multdiv.EXPR}	EXPR -> EXPR addsub.EXPR; EXPR -> .EXPR addsub EXPR; EXPR -> .lparen EXPR rparen; EXPR -> .id; EXPR -> .num} EXPR -> EXPR multdiv.EXPR; EXPR -> .EXPR addsub EXPR; EXPR -> .EXPR multdiv EXPR; EXPR -> .id; EXPR -> .num} EXPR -> lparen EXPR.rparen; EXPR -> EXPR.addsub EXPR; EXPR -> EXPR.multdiv EXPR}
goto goto	26, lparen) 26, id)	{EXPR -> lparen.EXPR rparen} {EXPR -> id.}	RESTRICTION OF THE EXPR> EXPR. addsub EXPR; EXPR -> EXPR. multdly EXPR} RESTRICTION OF THE EXPR> EXPR. addsub EXPR; EXPR -> EXPR. multdly EXPR} RESTRICTION OF THE EXPR> EXPR. addsub EXPR; EXPR -> EXPR. multdly EXPR} RESTRICTION OF THE EXPR> EXPR. addsub EXPR; EXPR -> EXPR. multdly EXPR} RESTRICTION OF THE EXPR> EXPR. addsub EXPR; EXPR -> EXPR. multdly EXPR} RESTRICTION OF THE EXPR> EXPR. addsub EXPR; EXPR -> EXPR. multdly EXPR} RESTRICTION OF THE EXPR> EXPR. addsub EXPR; EXPR -> EXPR. multdly EXPR} RESTRICTION OF THE EXPR> EXPR. addsub EXPR; EXPR -> EXPR. multdly EXPR} RESTRICTION OF THE EXPR> EXPR. addsub EXPR; EXPR -> EXPR. multdly EXPR} RESTRICTION OF THE EXPR> EXPR. addsub EXPR; EXPR -> EXPR. multdly EXPR} RESTRICTION OF THE EXPR> EXPR. addsub EXPR; EXPR -> EXPR. multdly EXPR}
goto goto	29, rparen) 30, id)	{FDECL -> vtype id lparen ARG rparen.lbrace BLOCK RETURN rbrace} {ARG -> vtype id.MOREARGS}	FDECL -> vtype id lparen ARG rparen.lbrace BLOCK RETURN rbrace
goto	32, rbrace) 33, ODECL)	{CDECL -> class id lbrace ODECL rbrace.}	CDECL -> class id lbrace ODECL rbrace.} ODECL -> VDECL ODECL.}
goto goto	33, FDECL) 33, ')	{ODECL -> VDECL.ODECL} {ODECL -> FDECL.ODECL} {ODECL -> '.'} {VDECL -> vtype.id semi; VDECL -> vtype.ASSIGN semi; FDECL -> vtype.id lparen ARG rparen lbrace BLOCK RETURN rbrace} 7	4
goto goto	34, ODECL) 34, VDECL)	{ODECL -> FDECL ODECL.}	4 {ODECL -> FDECL ODECL.} 3
goto goto	34, ') 34, vtype)	{ODECL -> '.'} {VDECL -> vtype.id semi; VDECL -> vtype.ASSIGN semi; FDECL -> vtype.id lparen ARG rparen lbrace BLOCK RETURN rbrace}	5 (ODECL -> ' '.)
goto	36, EXPR) 36, lparen)		EXPR -> EXPR addsub EXPR; EXPR -> EXPR.addsub EXPR; EXPR -> EXPR.multdiv EXPR} Column
goto goto	36, num) 37, EXPR)	{EXPR -> num.}	EXPR -> EXPR multdiv EXPR.; EXPR -> EXPR.addsub EXPR; EXPR -> EXPR.multdiv EXPR}
goto goto	37, id) 37, num)	{EXPR -> id.} {EXPR -> num.}	27 Carlo Control Con
goto goto	38, addsub) 38, multdiv)	{EXPR -> EXPR addsub.EXPR} {EXPR -> EXPR multdiv.EXPR}	FDECL -> vtype id lparen ARG rparen lbrace.BLOCK RETURN rbrace; BLOCK -> .' '; STMT -> .if lparen COND rparen lbrace BLOCK rbrace; VDECL -> .vtype id semi; VDECL -> .vtype
goto	40, comma)	{ARG -> vtype id MOREARGS.} {MOREARGS -> comma.vtype id MOREARGS}	{ARG -> vtype id MOREARGS.} MOREARGS -> comma.vtype id MOREARGS} MOREARGS -> '.'}
goto	46, multdiv)	{EXPR -> EXPR addsub.EXPR} {EXPR -> EXPR multdiv.EXPR} {EXPR -> EXPR addsub.EXPR}	36 37 36
goto	49, BLOCK)		FDECL -> vtype id lparen ARG rparen lbrace BLOCK.RETURN rbrace; RETURN -> .return RHS semi} [BLOCK -> STMT_BLOCK; BLOCK -> .STMT_BLOCK; BLOCK -> .' '; STMT -> .VDECL; STMT -> .if lparen COND rparen lbrace BLOCK rbrace; VDECL -> .vtype id semi; VDECL -> .vtype id semi; VDECL -> .vtype id semi; ASSIGN -> .id assign RHS}
goto	49, VDECL)	{STMT -> VDECL.}	SIMT -> VDECL.
goto	49, while) 49, vtype)	{STMT -> while.lparen COND rparen lbrace BLOCK rbrace} {VDECL -> vtype.id semi; VDECL -> vtype.ASSIGN semi}	{STMT -> if.lparen COND rparen lbrace BLOCK rbrace ELSE} {STMT -> while.lparen COND rparen lbrace BLOCK rbrace}
goto goto	51, vtype) 52, ')	{MOREARGS -> comma vtype.id MOREARGS} {MOREARGS -> ' '.}	{ASSIGN -> id.assign RHS} {MOREARGS -> comma vtype.id MOREARGS} {MOREARGS -> ' '.}
goto goto	53, return) 54, BLOCK)	{RETURN -> return.RHS semi} {BLOCK -> STMT BLOCK.}	FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN.rbrace FDECL -> vtype id lparen ARG rparen EXPR rparen FDECL -> vtype id lparen ARG rparen EXPR rparen FDECL -> vtype id lparen ARG rparen EXPR rparen FDECL -> vtype id lparen ARG rparen EXPR rparen FDECL -> vtype id lparen ARG rparen EXPR rparen FDECL -> vtype id lparen ARG rparen EXPR rparen FDECL -> vtype id lparen ARG rparen EXPR rparen FDECL -> vtype id lparen ARG rparen FDECL -> vtype id lp
goto goto	54, ') 54, VDECL)	{BLOCK -> STMT.BLOCK} {BLOCK -> '.'} {STMT -> VDECL.} {STMT -> ASSIGN.semi}	
goto goto	54, if) 54, while)		
goto goto	54, id) 55, ')	{ASSIGN -> id.assign RHS} {BLOCK -> ' '.}	STMT -> ASSIGN semi.}
goto goto	58, lparen) 59, lparen)	{STMT -> if lparen.COND rparen lbrace BLOCK rbrace ELSE}	STMT -> ASSIGN Semi.
goto goto	61, id) 63, rbrace)	{MOREARGS -> comma vtype id.MOREARGS} {FDECL -> vtype id lparen ARG rparen lbrace BLOCK RETURN rbrace.}	MOREARGS -> comma vtype id.MOREARGS; MOREARGS -> .comma vtype id MOREARGS; MOREARGS -> .' '} [MOREARGS -> comma vtype id.MOREARGS; MOREARGS -> .' '} [MOREARGS -> comma vtype id.MOREARGS; MOREARGS -> .' '} [MOREARGS -> comma vtype id.MOREARGS; MOREARGS -> .' '} [MOREARGS -> comma vtype id.MOREARGS; MOREARGS -> .' '} [MOREARGS -> comma vtype id.MOREARGS; MOREARGS -> .' '} [MOREARGS -> comma vtype id.MOREARGS; MOREARGS -> .' '} [MOREARGS -> comma vtype id.MOREARGS; MOREARGS -> .' '} [MOREARGS -> comma vtype id.MOREARGS; MOREARGS -> .' '} [MOREARGS -> comma vtype id.MOREARGS; MOREARGS -> .' '} [MOREARGS -> comma vtype id.MOREARGS; MOREARGS -> .' '} [MOREARGS -> comma vtype id.MOREARGS; MOREARGS -> .' '} [MOREARGS -> comma vtype id.MOREARGS; MOREARGS -> .' '}
goto goto	64, EXPR) 64, literal)	{RHS -> EXPR.; EXPR -> EXPR.addsub EXPR; EXPR -> EXPR.multdiv EXPR} {RHS -> literal.} {RHS -> character.}	22
goto goto	64, boolstr) 64, lparen)	<pre>{RHS -> boolstr.} {EXPR -> lparen.EXPR rparen} {EXPR -> id.}</pre>	25 26 27 27 28 29 29 29 29 29 29 29
goto goto goto	64, num) 68, COND) 68, boolstr)	{EXPR -> num.} {STMT -> if lparen COND.rparen lbrace BLOCK rbrace ELSE; COND -> COND.comp COND} {COND -> boolstr.}	STMT -> if lparen COND.rparen lbrace BLOCK rbrace ELSE; COND -> COND.comp COND} COND -> boolstr.}
goto goto	69, boolstr) 70, MOREARGS)	{COND -> boolstr.} {MOREARGS -> comma vtype id MOREARGS.}	STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp COND} [STMT -> while lparen COND.rparen lbrace BLOCK rbrace; COND -> COND.comp cond
goto goto	70, ') 72, semi)		RETURN -> return RHS semi.}
goto goto	73, comp) 75, rparen)	{COND -> COND comp.COND} {STMT -> while lparen COND rparen.lbrace BLOCK rbrace}	STMT -> if lparen COND rparen.lbrace BLOCK rbrace ELSE COND -> COND comp.COND; COND -> .COND comp COND; COND -> .boolstr
goto goto	78, lbrace) 79, COND)	{COND -> COND comp COND.; COND -> COND.comp COND}	SIMIT -> if lparen COND rparen lbrace.BLOCK rbrace ELSE; BLOCK -> .vtype ASSIGN semi; ASSIGN -> .id assign RHS} [2 {COND -> COND comp COND.; COND -> COND.comp COND}
goto goto	80, lbrace) 81, BLOCK)	{STMT -> while lparen COND rparen lbrace.BLOCK rbrace}	STMT -> while lparen COND rparen lbrace.BLOCK rbrace; BLOCK -> .vtype ASSIGN semi; ASSIGN -> .id assign RHS} STMT -> if lparen COND rparen lbrace BLOCK rbrace; VDECL -> .vtype id semi; ASSIGN -> .id assign RHS} STMT -> if lparen COND rparen lbrace BLOCK.rbrace ELSE; STMT -> .while lparen COND rparen lbrace BLOCK.rbrace ELSE; STMT -> .vtype id semi; VDECL -> .vtype id semi; ASSIGN -> .id assign RHS}
goto goto	81, ') 81, VDECL)	{BLOCK -> STMT.BLOCK} {BLOCK -> '.'} {STMT -> VDECL.} {STMT -> ASSIGN.semi}	55
goto goto	81, if) 81, while)	{STMT -> if.lparen COND rparen lbrace BLOCK rbrace ELSE} {STMT -> while.lparen COND rparen lbrace BLOCK rbrace} {VDECL -> vtype.id semi; VDECL -> vtype.ASSIGN semi}	
goto goto	81, id) 82, comp)	{ASSIGN -> id.assign RHS} {COND -> COND comp.COND}	STMT -> while lparen COND rparen lbrace BLOCK.rbrace}
goto goto	83, STMT) 83, ')	{BLOCK -> STMT.BLOCK} {BLOCK -> '.'} {STMT -> VDECL.}	34 35 36
goto goto goto	83, ASSIGN) 83, if) 83, while)	{STMT -> ASSIGN.semi} {STMT -> if.lparen COND rparen lbrace BLOCK rbrace ELSE} {STMT -> while.lparen COND rparen lbrace BLOCK rbrace}	77
goto goto goto	83, vtype) 83, id) 84, rbrace)	<pre>{VDECL -> vtype.id semi; VDECL -> vtype.ASSIGN semi} {ASSIGN -> id.assign RHS} {STMT -> if lparen COND rparen lbrace BLOCK rbrace.ELSE}</pre>	STMT -> if lparen COND rparen lbrace BLOCK rbrace.ELSE; ELSE -> .else lbrace BLOCK rbrace; ELSE -> .' '}
goto goto goto	85, rbrace) 86, ELSE) 86, else)	{STMT -> while lparen COND rparen lbrace BLOCK rbrace.} {STMT -> if lparen COND rparen lbrace BLOCK rbrace ELSE.} {ELSE -> else.lbrace BLOCK rbrace}	STMT -> while lparen COND rparen lbrace BLOCK rbrace.} STMT -> if lparen COND rparen lbrace BLOCK rbrace ELSE.} STMT -> else.lbrace BLOCK rbrace}
goto goto goto	86, ') 89, lbrace) 90, ')	{ELSE -> '.'} {ELSE -> else lbrace.BLOCK rbrace} {ELSE -> ' '.}	{ELSE -> '.'} {ELSE -> else lbrace.BLOCK rbrace; BLOCK -> .STMT BLOCK; BLOCK -> .' '; STMT -> .VDECL; STMT -> .if lparen COND rparen lbrace BLOCK rbrace; VDECL -> .vtype id semi; VDECL -> .vtype
goto goto	91, BLOCK) 91, STMT) 91, ')	{ELSE -> else lbrace BLOCK.rbrace} {BLOCK -> STMT.BLOCK} {BLOCK -> '.'}	{ELSE -> else lbrace BLOCK.rbrace} {ELSE -> else lbrace BLOCK.rbrace}
goto goto	91, ASSIGN) 91, if)	{STMT -> VDECL.} {STMT -> ASSIGN.semi} {STMT -> if.lparen COND rparen lbrace BLOCK rbrace ELSE}	66
goto goto	91, vtype) 91, id)	{STMT -> while.lparen COND rparen lbrace BLOCK rbrace} {VDECL -> vtype.id semi; VDECL -> vtype.ASSIGN semi} {ASSIGN -> id.assign RHS}	
goto	os, rbrace)	{ELSE -> else lbrace BLOCK rbrace.}	4 {ELSE -> else lbrace BLOCK rbrace.}
Sta	e 't i d	LR table ACTION semi assign literal character boolstr addsub multdiv lparen rparen num lbrace rbrace comma if while comp	GOTO Input (tokens): id + id * id Maximum number of steps: 100

State	vtvpe	ACTION GOTO type id semi assign literal character boolstr addsub multdiv lparen rparen num lbrace rbrace comma if while comp else return class \$ CODE VDECL ASSIGN RHS EXPR FDECL ARG MOREARGS BLOCK STMT COND ELSE RETURN CDECL ODECL											Maximum number of steps: 100													
0	s2				3001501	uuusus									COP C15			1								PARSE
2 86		s ₉															s8	3 1	10		4				5	_
3																	acc									Step Stack Input Action Tre
4 s6 5 s6																	s8 s8	12 1			4				5	1 0 id + id * id \$
6 s13		s14																	10							
8		s15																								
10			s17																							
11																	r_1									
13																	r_2									
14		s16	s17					s19																		
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	r_4	$ \mathbf{r}_4 $								s20			$\begin{vmatrix} \mathbf{r}_4 & \mathbf{r}_4 \end{vmatrix}$				r_4									
17		s27	s23	s24 s	s25			s26		s28										21 22	2					
$ \begin{array}{c c} 18 & r_5 \\ \hline 19 & s31 \end{array} $	r ₅	$ \mathbf{r}_5 $											$ \mathbf{r}_5 \mathbf{r}_5 $				r ₅				29					
20 s35 21																		33			34				32	
22		r_6				s36	s37																			
23		r ₈																								
25		r_9																								
26		s27						s26		s28										38	8					
27		r_{14}	-ii			r ₁₄	r_{14} r_{15}		r_{14} r_{15}																	
29									s39																	
30 31 s 41		s40																								
32 33 s35	[]										s42							33			34				43	
34 S35	S /																	33			34				44	
35 s45 36		s27						s26		s28										46	6					
37	<u> </u>	s27						s26		s28										47						
39						s36	s37		s48	s49																
40 s52 41									r			s51									50					
42 r ₃₂	 r ₃₂								r ₁₈								r ₃₂									
43											r ₃₃															
45											r_{34} r_{35}															
46		r ₁₁				•s36 / or ₁₁ •s36 / or ₁₂	•s37 / or ₁₁		r ₁₁																	
47		r_{12}					s37 / r ₁₂		r ₁₂																	
49 s55		s60											s58 s5	9				56	57			53 54				
50	s61								r ₁₇																	
52 s62																										
54 s55	s2	s60											s58 s5	9		s64		56	57			65 54		63		
55 s66 56 r ₂₃	<u> </u>	r ₂₃											r_{23} r_{23}	2												
57	23	s67											23 - 23	3												
58 59								s68 s69																		
60			s17																							
62		s70							r ₂₀																	
63		27		24	-25			26		220	s71									72 22	2					
65		s27	s23	s24 s	s25			s26		s28	r_{21}					r ₂₁				72 22	2					
66											r ₂₂					r ₂₂										
67 F ₂₄	r ₂₄	r ₂₄		s	s74								$\begin{vmatrix} \mathbf{r}_{24} & \mathbf{r}_{24} \end{vmatrix}$	4									73			
69 70 s52				s	574							s51									76		75			
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$												851					r ₁₆									
72		s77							s78						s79											
74									r ₂₈					-	r ₂₈											
75 76									s80 r ₁₉					s	s79											
77									19		r ₃₁															
78 79					574					s81													82			
80										s83																
81 s55 82	s2	s60							r ₂₇				s58 s5		•s79 / Or ₂₇			56	57			84 54				
83 s55	s2	s60											s58 s5					56	57			85 54				
85											s86 s87															
86 s90		roc											rac	6	s89								88			
	$\begin{vmatrix} \mathbf{r}_{26} \\ \mathbf{r}_{25} \end{vmatrix}$												$\begin{vmatrix} \mathbf{r}_{26} & \mathbf{r}_{26} \\ \mathbf{r}_{25} & \mathbf{r}_{25} \end{vmatrix}$													
89 90 s92										s91																
91 s55	s2	s60											s58 s5					56	57			93 54				
92 r ₃₀ 93		r ₃₀									s94		$\begin{vmatrix} \mathbf{r}_{30} & \mathbf{r}_{30} \end{vmatrix}$	0												
	r ₂₉	r ₂₉											$\begin{vmatrix} \mathbf{r}_{29} & \mathbf{r}_{29} \end{vmatrix}$	9												
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