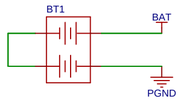
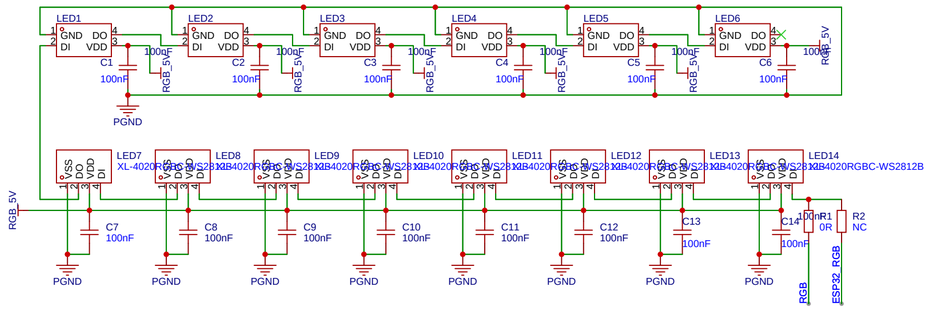
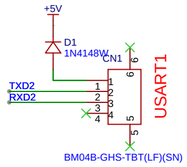
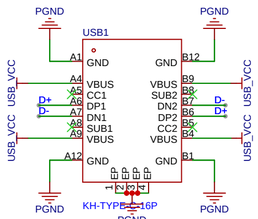
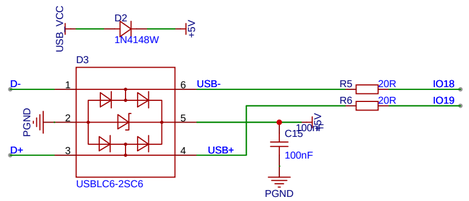


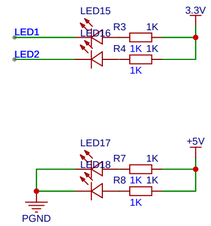


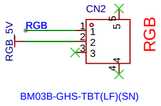


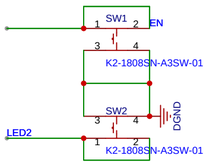
注意：元件名称（值）为 NC 的不用贴，留空即可！

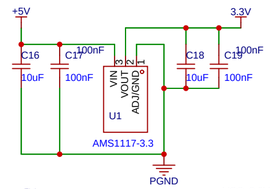


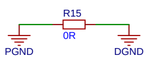


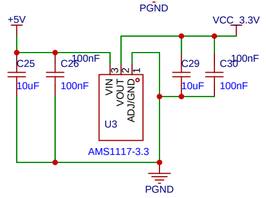


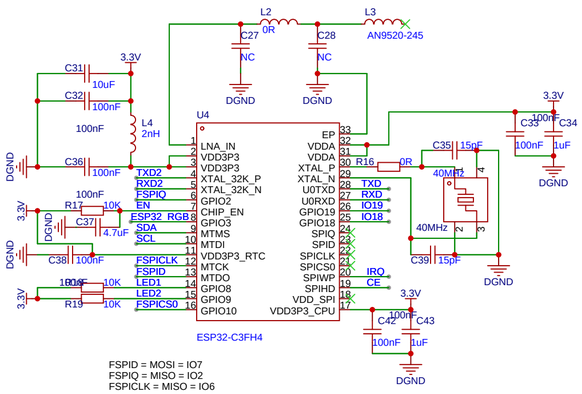


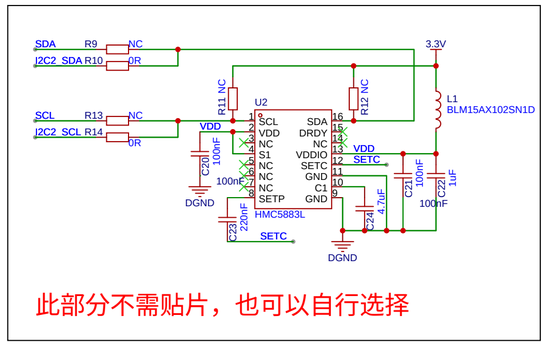


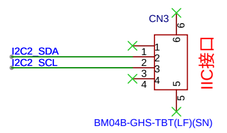


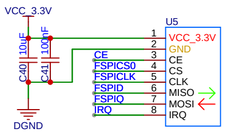












LNA_IN 1 I/O — 射频输入和输出
VDD3P3 2 PA — 模拟电源
VDD3P3 3 PA — 模拟电源
XTAL_32K_P 4 I/O/T VDD3P3_RTC GPIO0, ADC1_CH0, XTAL_32K_P
XTAL_32K_N 5 I/O/T VDD3P3_RTC GPIO1, ADC1_CH1, XTAL_32K_N
GPIO2 6 I/O/T VDD3P3_RTC GPIO2, ADC1_CH2, FSPIQ
CHIP_EN 7 I VDD3P3_RTC
高电平：芯片使能；
低电平：芯片关闭；
注意不能让 CHIP_EN 管脚浮空。
GPIO3 8 I/O/T VDD3P3_RTC GPIO3, ADC1_CH3
MTMS 9 I/O/T VDD3P3_RTC GPIO4, ADC1_CH4, FSPIHD, MTMS
MTDI 10 I/O/T VDD3P3_RTC GPIO5, ADC2_CH0, FSPIWP MTDI
VDD3P3_RTC 11 PD — RTC 电源输入
MTCK 12 I/O/T VDD3P3_CPU GPIO6, FSPICLK, MTCK
MTDO 13 I/O/T VDD3P3_CPU GPIO7, FSPID, MTDO
GPIO8 14 I/O/T VDD3P3_CPU GPIO8
GPIO9 15 I/O/T VDD3P3_CPU GPIO9
GPIO10 16 I/O/T VDD3P3_CPU GPIO10, FSPICS0
VDD3P3_CPU 17 PD — CPU IO 电源输入
VDD_SPI 18 I/O/T/PD VDD3P3_CPU GPIO11, flash 电源输出
SPIHD 19 I/O/T VDD3P3_CPU GPIO12, SPIHD
SPIWP 20 I/O/T VDD3P3_CPU GPIO13, SPIWP
SPICS0 21 I/O/T VDD3P3_CPU GPIO14, SPICS0
SPICLK 22 I/O/T VDD3P3_CPU GPIO15, SPICLK
SPID 23 I/O/T VDD3P3_CPU GPIO16, SPID
SPIQ 24 I/O/T VDD3P3_CPU GPIO17, SPIQ
GPIO18 25 I/O/T VDD3P3_CPU GPIO18, USB_DGPIO19 26 I/O/T VDD3P3_CPU GPIO19, USB_D+
U0RXD 27 I/O/T VDD3P3_CPU GPIO20, U0RXD
U0TXD 28 I/O/T VDD3P3_CPU GPIO21, U0TXD
XTAL_N 29 — — 外部主晶振输出
XTAL_P 30 — — 外部主晶振输入
VDDA 31 PA — 模拟电源
VDDA 32 PA — 模拟电源
GND 33 G — 接地

Schematic	BAT_ESP32_Eval_V1.1	Update Date	2023-09-29
		Create Date	2023-09-29
Page	P1	Part Number	
Drawed		FOC无刷平衡车，复刻后无需调参数即可丝滑稳定！	
Reviewed			
		VER	SIZE	PAGE 1 OF 1
嘉立创EDA			