

Pines de los puertos del conector J1

PUERTO B:

Table 13-6. Port B Pins Alternate Functions

Port Pin	Alternate Functions
PB7	OC0A/OC1C/PCINT7 (Output Compare and PWM Output A for Timer/Counter0, Output Compare and PWM Output C for Timer/Counter1 or Pin Change Interrupt 7)
PB6	OC1B/PCINT6 (Output Compare and PWM Output B for Timer/Counter1 or Pin Change Interrupt 6)
PB5	OC1A/PCINT5 (Output Compare and PWM Output A for Timer/Counter1 or Pin Change Interrupt 5)
PB4	OC2A/PCINT4 (Output Compare and PWM Output A for Timer/Counter2 or Pin Change Interrupt 4)
PB3	MISO/PCINT3 (SPI Bus Master Input/Slave Output or Pin Change Interrupt 3)
PB2	MOSI/PCINT2 (SPI Bus Master Output/Slave Input or Pin Change Interrupt 2)
PB1	SCK/PCINT1 (SPI Bus Serial Clock or Pin Change Interrupt 1)
PB0	\overline{SS} /PCINT0 (SPI Slave Select input or Pin Change Interrupt 0)

PUERTO D:

Table 13-12. Port D Pins Alternate Functions

Port Pin	Alternate Function
PD7	T0 (Timer/Counter0 Clock Input)
PD6	T1 (Timer/Counter1 Clock Input)
PD5	XCK1 (USART1 External Clock Input/Output)
PD4	ICP1 (Timer/Counter1 Input Capture Trigger)
PD3	$\overline{INT3}$ /TXD1 (External Interrupt3 Input or USART1 Transmit Pin)
PD2	$\overline{INT2}$ /RXD1 (External Interrupt2 Input or USART1 Receive Pin)
PD1	$\overline{INT1}$ /SDA (External Interrupt1 Input or TWI Serial Data)
PD0	$\overline{INT0}$ /SCL (External Interrupt0 Input or TWI Serial Clock)

PUERTO K:

Table 13-30. Port K Pins Alternate Functions

Port Pin	Alternate Function
PK7	ADC15/PCINT23 (ADC Input Channel 15 or Pin Change Interrupt 23)
PK6	ADC14/PCINT22 (ADC Input Channel 14 or Pin Change Interrupt 22)
PK5	ADC13/PCINT21 (ADC Input Channel 13 or Pin Change Interrupt 21)
PK4	ADC12/PCINT20 (ADC Input Channel 12 or Pin Change Interrupt 20)
PK3	ADC11/PCINT19 (ADC Input Channel 11 or Pin Change Interrupt 19)
PK2	ADC10/PCINT18 (ADC Input Channel 10 or Pin Change Interrupt 18)
PK1	ADC9/PCINT17 (ADC Input Channel 9 or Pin Change Interrupt 17)
PK0	ADC8 /PCINT16 (ADC Input Channel 8 or Pin Change Interrupt 16)

PUERTO L:

Table 13-33. Port L Pins Alternate Functions

Port Pin	Alternate Function
PL7	–
PL6	–
PL5	OC5C (Output Compare and PWM Output C for Timer/Counter5)
PL4	OC5B (Output Compare and PWM Output B for Timer/Counter5)
PL3	OC5A (Output Compare and PWM Output A for Timer/Counter5)
PL2	T5 (Timer/Counter5 Clock Input)
PL1	ICP5 (Timer/Counter5 Input Capture Trigger)
PL0	ICP4 (Timer/Counter4 Input Capture Trigger)