

Cost & Complexity

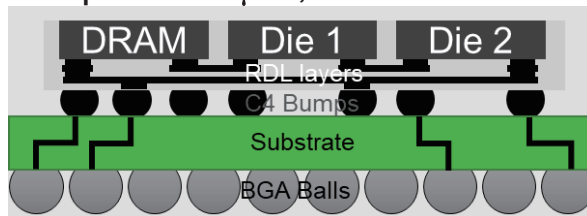


Organic Substrate

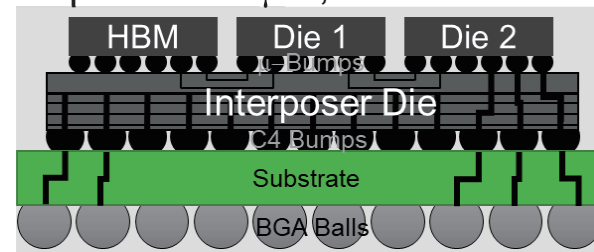
Data Rate: 112Gbps

Line Space: $>10\mu\text{m}$

Integrated Fanout (InFO)
Post Fab RDL (Fanout)
Data Rate: 56Gbps
Line Space: $>2\mu\text{m}$, 2500 Pin Count



Silicon Interposer
Data Rate: 3.2 \rightarrow 6.4Gbps
Line Space: $>0.4\mu\text{m}$, 4000 Pin Count



Data Rates

Die/Package Size, Density & IO Count