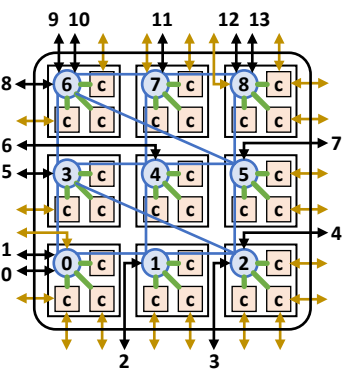
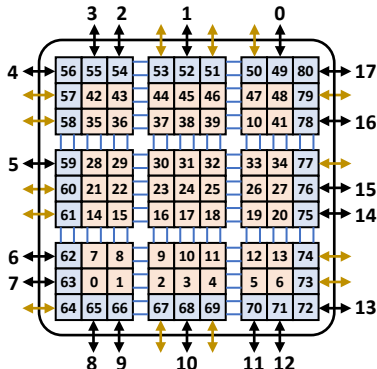


↔ I/O Port for Interconnection

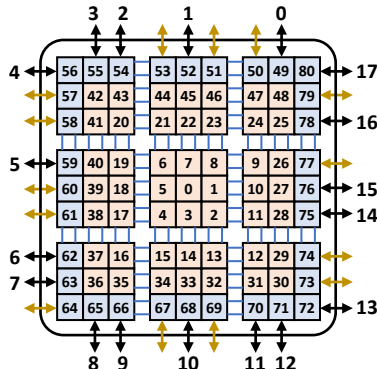
I/O Port for other Functions (e.g. Memory, PCIe)



(a)



(b)



(c)