

config_output

ff_yadif_init_x86

ff_yadif_filter_line
_16bit_mmxext

```
graph LR; A[config_output] --> B[ff_yadif_init_x86]; B --> C[ff_yadif_filter_line_16bit_mmxext];
```

The diagram illustrates a three-step process flow. It begins with a box labeled 'config_output', which points via a blue arrow to a box labeled 'ff_yadif_init_x86'. This second box then points via another blue arrow to a final box labeled 'ff_yadif_filter_line_16bit_mmxext'. The final box is shaded gray, while the others are white.