


common_vt_8t_24w_msa



```
graph LR; A[common_vt_8t_24w_msa] --> B[common_vt_8t_8w_msa]
```

A diagram showing a transformation from a source state to a target state. The source state is represented by a white rectangular box with a black border containing the text 'common_vt_8t_24w_msa'. A blue arrow points from this box to a target state, which is represented by a gray rectangular box with a black border containing the text 'common_vt_8t_8w_msa'.

common_vt_8t_8w_msa