

pred8x16_mad_cow_dc_l0t

pred8x8_mad_cow_dc_l0t

pred4x4_dc

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graph LR; A[pred8x16_mad_cow_dc_l0t] --> D[pred4x4_dc]; B[pred8x8_mad_cow_dc_l0t] --> D;
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The diagram illustrates a data flow where two separate input components, 'pred8x16_mad_cow_dc_l0t' and 'pred8x8_mad_cow_dc_l0t', are combined to produce a single output component, 'pred4x4_dc'. The input boxes are white with black borders, while the output box is gray with a black border. Blue arrows indicate the direction of the flow from the inputs to the output.