

config\_output

ff\_yadif\_init\_x86

ff\_yadif\_filter\_line  
\_mmxext

```
graph LR; A[config_output] --> B[ff_yadif_init_x86]; B --> C[ff_yadif_filter_line_mmxext];
```

The diagram illustrates a three-step process flow. It begins with a box labeled 'config\_output', followed by an arrow pointing to a box labeled 'ff\_yadif\_init\_x86'. A second arrow then points from 'ff\_yadif\_init\_x86' to a final box labeled 'ff\_yadif\_filter\_line\_mmxext'. The final box is shaded gray, while the others are white.