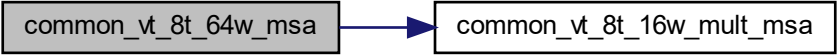


common_vt_8t_64w_msa



```
graph LR; A[common_vt_8t_64w_msa] --> B[common_vt_8t_16w_mult_msa]
```

A diagram showing a transformation from a source state to a target state. The source state is represented by a gray rectangular box on the left containing the text 'common_vt_8t_64w_msa'. A dark blue arrow points from the right side of this box to the left side of a white rectangular box on the right. The target state is represented by the white box containing the text 'common_vt_8t_16w_mult_msa'. Both boxes have a black border.

common_vt_8t_16w_mult_msa