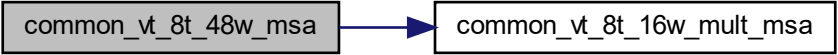


common_vt_8t_48w_msa



```
graph LR; A[common_vt_8t_48w_msa] --> B[common_vt_8t_16w_mult_msa]
```

common_vt_8t_16w_mult_msa