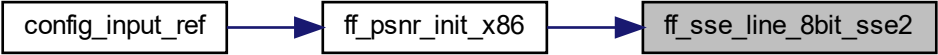


config_input_ref

ff_psnr_init_x86

ff_sse_line_8bit_sse2



```
graph LR; A[config_input_ref] --> B[ff_psnr_init_x86]; B --> C[ff_sse_line_8bit_sse2];
```