

config_output

ff_yadif_init_x86

ff_yadif_filter_line
_10bit_ssse3

```
graph LR; A[config_output] --> B[ff_yadif_init_x86]; B --> C[ff_yadif_filter_line_10bit_ssse3];
```

The diagram illustrates a three-step process flow. It begins with a box labeled 'config_output', followed by an arrow pointing to a box labeled 'ff_yadif_init_x86'. A second arrow then points from this box to a final box labeled 'ff_yadif_filter_line_10bit_ssse3'. The final box is shaded gray, while the others are white.