

pred8x16_mad_cow_dc_0l0

pred8x16_mad_cow_dc_l00

pred8x16_left_dc

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graph LR; A[pred8x16_mad_cow_dc_0l0] --> C[pred8x16_left_dc]; B[pred8x16_mad_cow_dc_l00] --> C;
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The diagram illustrates a data flow where two separate inputs, 'pred8x16_mad_cow_dc_0l0' and 'pred8x16_mad_cow_dc_l00', are combined into a single output, 'pred8x16_left_dc'. The output box is shaded gray, while the input boxes are white with black borders. Blue arrows indicate the direction of the flow from the inputs to the output.