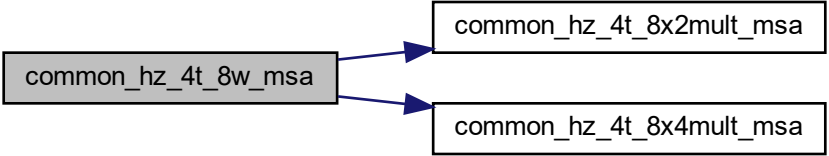


common\_hz\_4t\_8w\_msa



```
graph LR; A[common_hz_4t_8w_msa] --> B[common_hz_4t_8x2mult_msa]; A --> C[common_hz_4t_8x4mult_msa];
```

common\_hz\_4t\_8x2mult\_msa

common\_hz\_4t\_8x4mult\_msa