

ZeBu-Server Modules

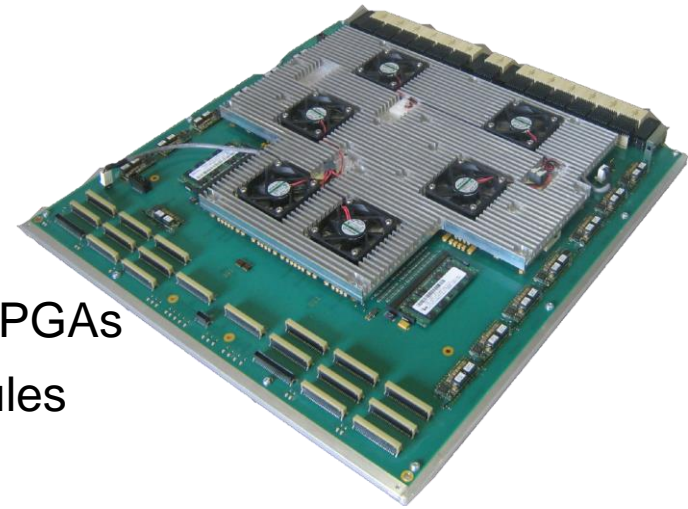
- Three Module Types:
 - 5 Million, 10 Million (w/ ICE), 20 Million ASIC gates



Virtex 5 to map DUT	Design Capacity [ASIC gates]	Local Memory (RLDRAM)	System Memory (DDR2)	Distributed Memories (BRAM: 36kb)	LUT Memories (RAMLUT: 64b)	ICE	Trace Memory (DDR2)
4 LX330	5M	4x256MB	2x2GB	5MB	1.6MB	No	2x2GB
8 LX330	10M	8x128MB	1x2GB*	10MB	3.3MB	1200 I/Os	1x2GB*
16 LX330	20M	No	2x2GB	20MB	6.6MB	No	2x2GB

* Could be upgraded to 4GB upon customer request

ZeBu-Server2 Modules



- Three types of module: 5 FPGAs, 9 FPGAs, 17 FPGAs
- ICE available on both 5-FPGA and 9-FPGA modules
- ICE connectors are not compatible with ZSE:
 - Footprint is similar
 - Pin out is different

Module Name	DUT LX-760 FPGA	Max Capacity [ASIC gates]	Memories			ICE
			Local (DUT)	System (DUT)	Trace (debug)	
5F	5	~ 12M	4x128MB RLDRAM	2x2GB DDR2	2x4GB DDR2	600 I/Os
9F	9	~ 21M	4x512MB DDR2	2x2GB DDR2	2x4GB DDR2	600 I/Os
17F	17	~ 40M	-	2x2GB DDR2	2x4GB DDR2	-

Notes:

1. Above max capacity is nominal, actual capacity may vary
2. Local Memories and System Memories can be configured into any of a large number of memory configurations, including DDR, DDR2, DDR3, GDDR5, etc., via ZeBu Memory Models