|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| 型号代号 | ${modelCode} | 单机名称 | ${productName} | 图纸代号 | ${drawingCode} | 阶段 | ${stage} |

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| ${isInheritArr0} | 继承分机名称 | ${inheritExtensionName} | | 单板名称 | ${inheritSingleBoardName} | 图纸代号 | ${inheritDrawingCode} | 阶段 | ${inheritStage} |
| ${isInheritArr1} |  | | | | | | | | | |
| ${isInheritArr2} | 相对于同型号前阶段设计 : | | ${changeSection} | | | | | | | |
| 变化内容: | | ${changeContent} | | | | | | | |
| 变化原因: | | ${changeReason} | | | | | | | |
| 三单落实情况: | | ${specialProblem} | | | | | | | |

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| 填写说明：设计、校对、审核需要对表内所有项目进行检查；填表时不能用符号代替“正确/错误”或“是/否” |

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| --- | --- | --- | --- |
| **检查项目** | **设计检查结果及说明** | **校对检查结果** | **审核检查结果** |
| **状态** | | | |
| 1. ${checkDemand0} | ${firstCheck0} | ${secondCheck0} | ${thirdCheck0} |
| **外部接口** | | | |
| 2. ${checkDemand1} | ${firstCheck1} | ${secondCheck1} | ${thirdCheck1} |
| **走线与连线** | | | |
| 3. ${checkDemand2} | ${firstCheck2} | ${secondCheck2} | ${thirdCheck2} |
| 4. ${checkDemand3} | ${firstCheck3} | ${secondCheck3} | ${thirdCheck3} |
| **元器件的使用** | | | |
| 5. ${checkDemand4} | ${firstCheck4} | ${secondCheck4} | ${thirdCheck4} |
| 6. ${checkDemand5} | ${firstCheck5} | ${secondCheck5} | ${thirdCheck5} |
| 7. ${checkDemand6} | ${firstCheck6} | ${secondCheck6} | ${thirdCheck6} |
| 8. ${checkDemand7} | ${firstCheck7} | ${secondCheck7} | ${thirdCheck7} |
| 9. ${checkDemand8} | ${firstCheck8} | ${secondCheck8} | ${thirdCheck8} |
| 10. ${checkDemand9} | ${firstCheck9} | ${secondCheck9} | ${thirdCheck9} |
| 11. ${checkDemand10} | ${firstCheck10} | ${secondCheck10} | ${thirdCheck10} |
| 12. ${checkDemand11} | ${firstCheck11} | ${secondCheck11} | ${thirdCheck11} |
| **内部接口** | | | |
| 13. ${checkDemand12} | ${firstCheck12} | ${secondCheck12} | ${thirdCheck12} |
| 14. ${checkDemand13} | ${firstCheck13} | ${secondCheck13} | ${thirdCheck13} |
| 15. ${checkDemand14} | ${firstCheck14} | ${secondCheck14} | ${thirdCheck14} |
| 16. ${checkDemand15} | ${firstCheck15} | ${secondCheck15} | ${thirdCheck15} |
| **静电防护** | | | |
| 17. ${checkDemand16} | ${firstCheck16} | ${secondCheck16} | ${thirdCheck16} |
| **新工艺** | | | |
| 18. ${checkDemand17} | ${firstCheck17} | ${secondCheck17} | ${thirdCheck17} |
| **新材料** | | | |
| 19. ${checkDemand18} | ${firstCheck18} | ${secondCheck18} | ${thirdCheck18} |
| **ALC电路设计** | | | |
| 20. ${checkDemand19} | ${firstCheck19} | ${secondCheck19} | ${thirdCheck19} |
| **安全间距** | | | |
| 21. ${checkDemand20} | ${firstCheck20} | ${secondCheck20} | ${thirdCheck20} |
| 22. ${checkDemand21} | ${firstCheck21} | ${secondCheck21} | ${thirdCheck21} |
| 23. ${checkDemand22} | ${firstCheck22} | ${secondCheck22} | ${thirdCheck22} |
| 24. ${checkDemand23} | ${firstCheck23} | ${secondCheck23} | ${thirdCheck23} |
| 25. ${checkDemand24} | ${firstCheck24} | ${secondCheck24} | ${thirdCheck24} |

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| 单板设计师/日期: | ${submitInfo0} | 校 对/日期: | ${submitInfo1} |
| 审 核/日期: | ${submitInfo2} | 批 准/日期: | ${submitInfo3} |