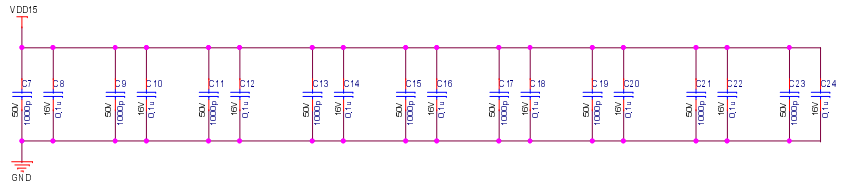
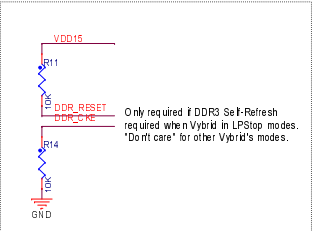
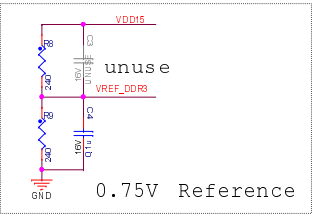
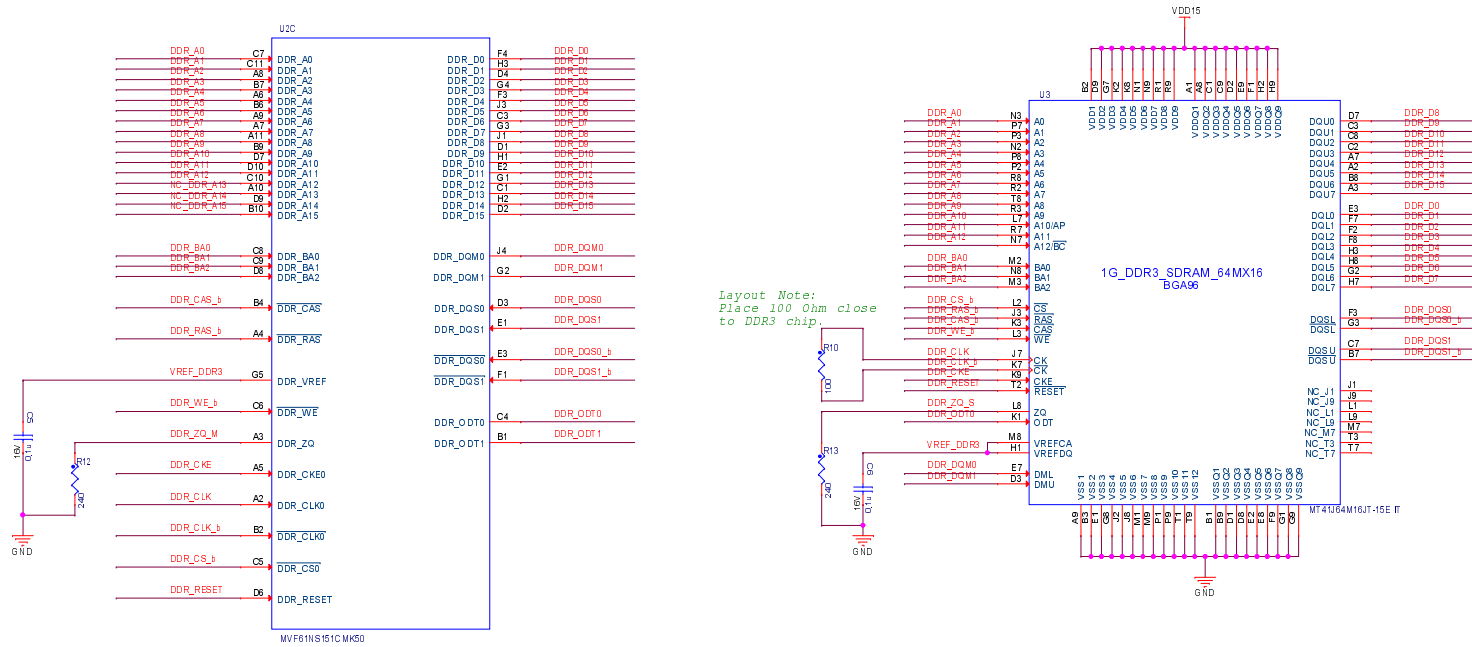
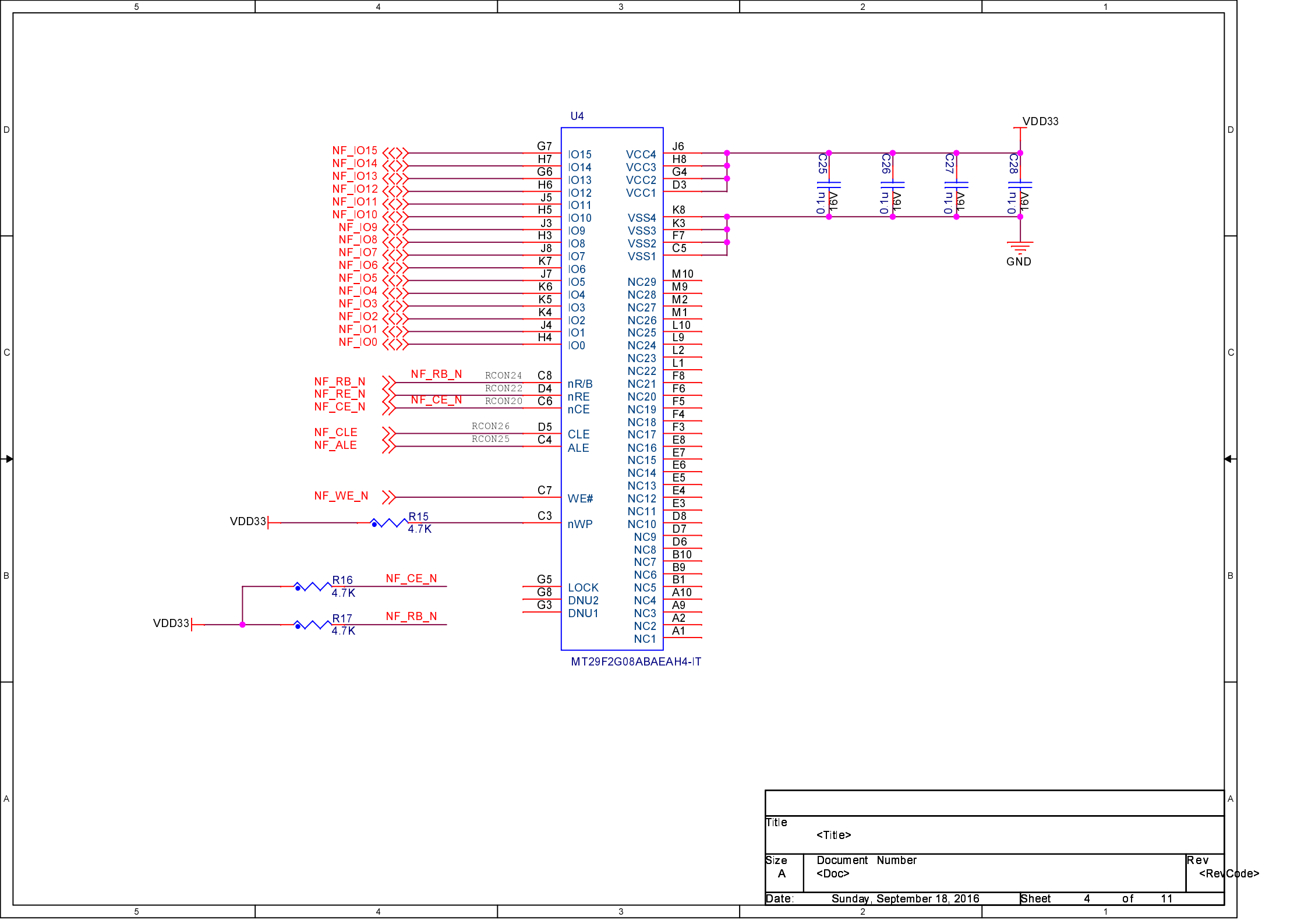
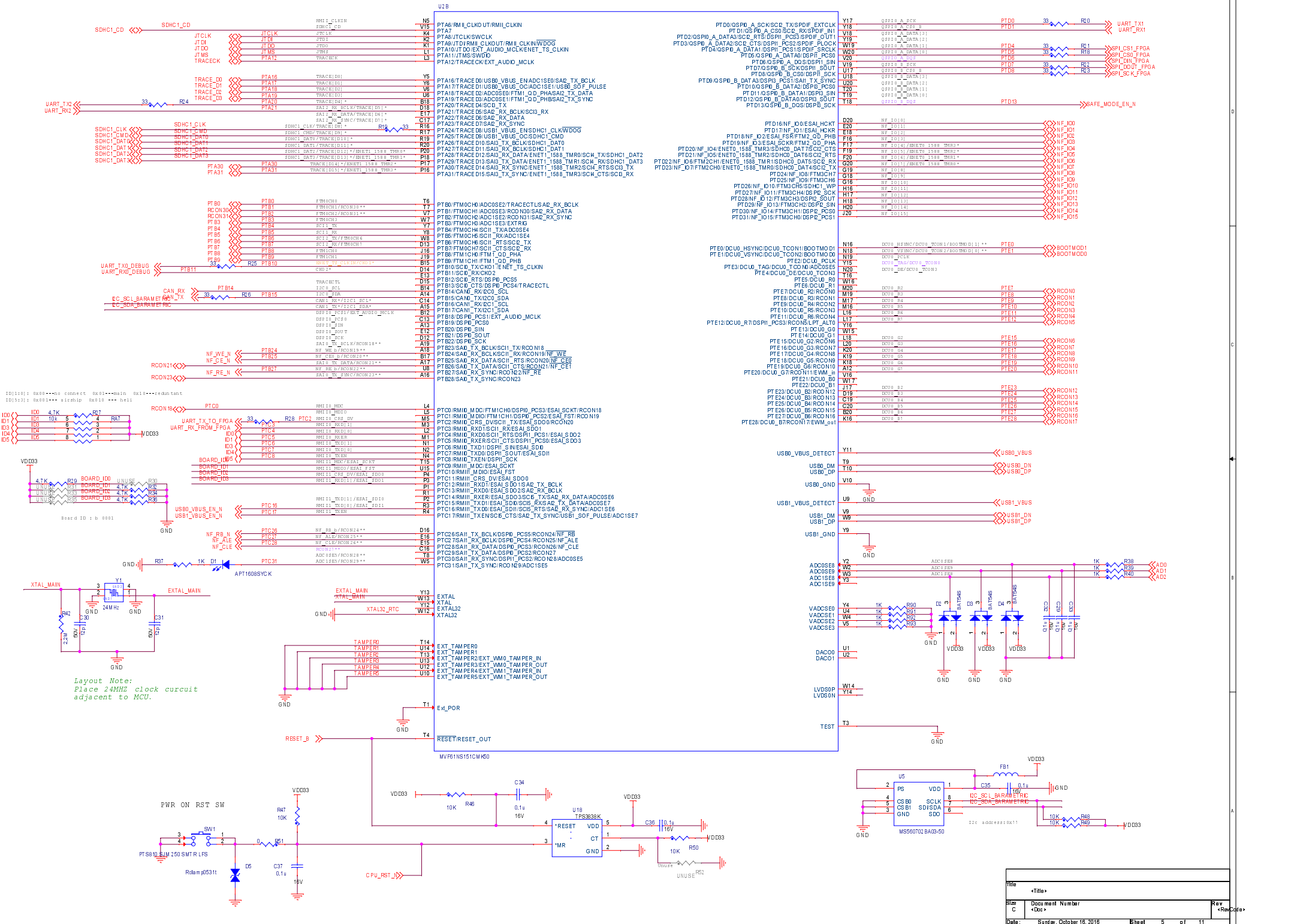


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DDR3 MEMORIES AND TERMINATIONS

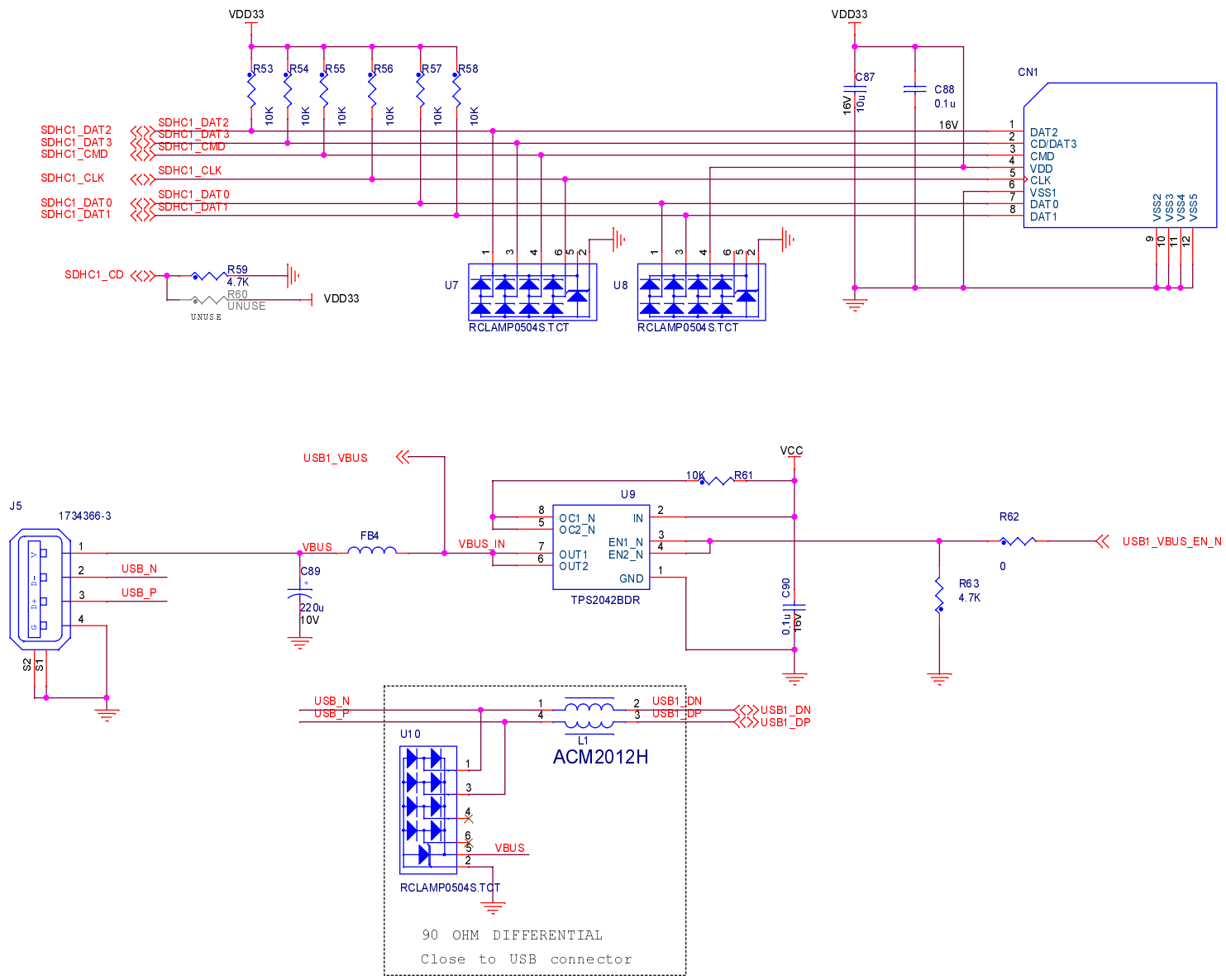






Layout Note:
Place 24MHz clock circuit
adjacent to MCU.

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