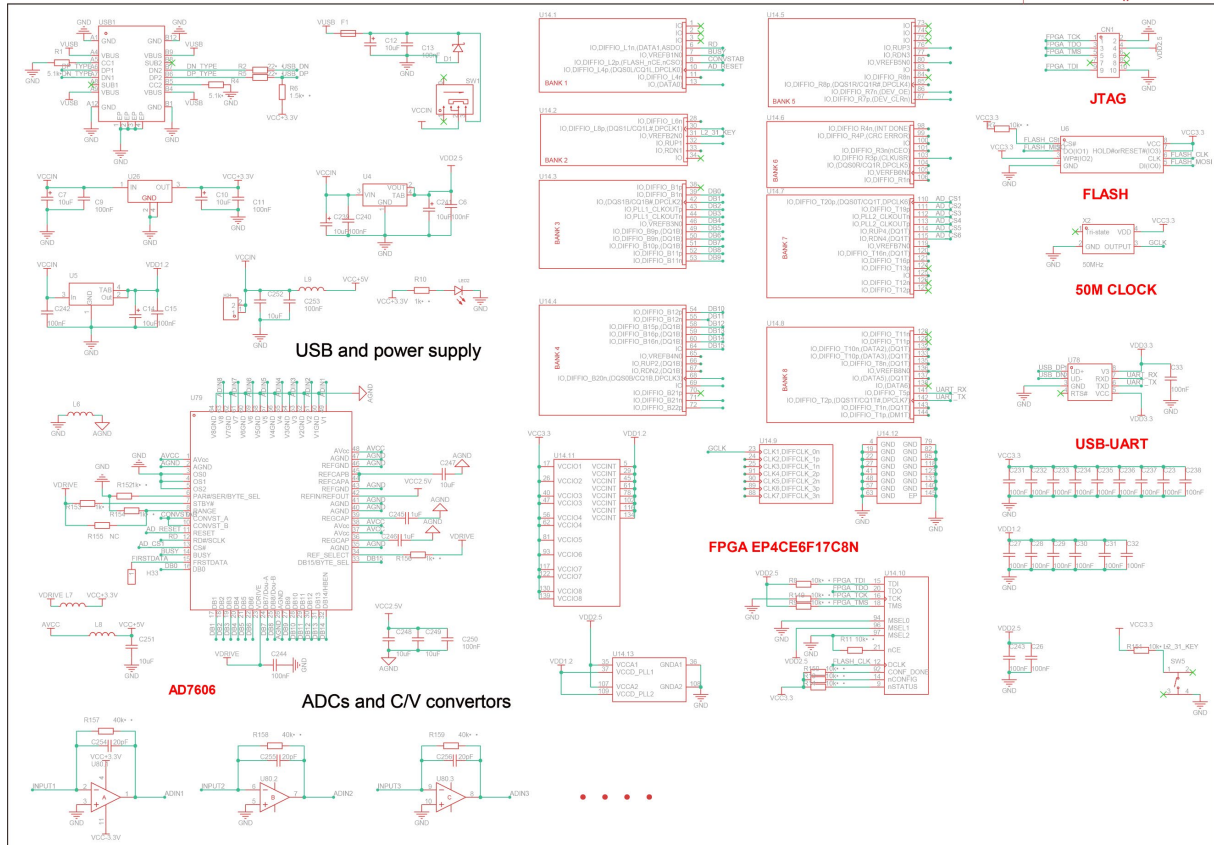
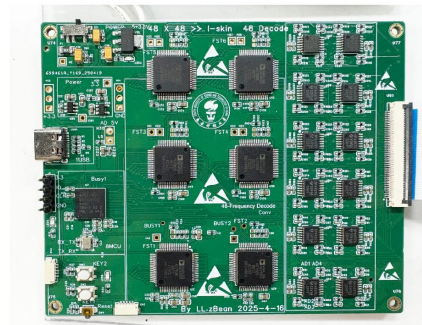




A



B



1

2 **The design of the decoding circuit and computation block. (A) Schematic diagram of core**  
 3 **components in the decoding circuit. (B) Physical PCB layout of the decoding circuit board.**

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