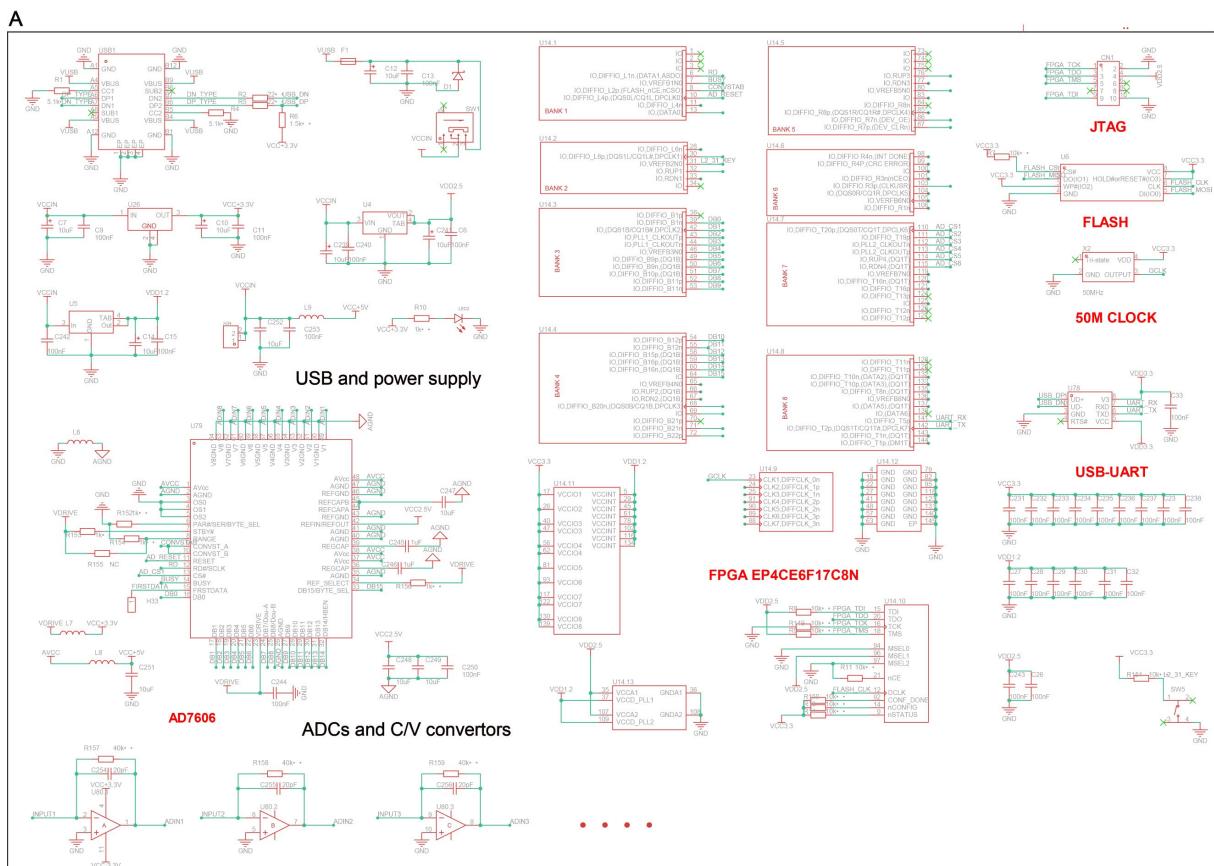


1

2      **The design of the encoding circuit.** (A) Schematic diagram of core components in the  
3      encoding circuit. (B) Physical PCB layout of the encoding circuit board.



1

**2 The design of the decoding circuit and computation block.** (A) Schematic diagram of core  
**3** components in the decoding circuit. (B) Physical PCB layout of the decoding circuit board.

4

2