

## Registro 0

		1	2	3	4	5	6	7	8	BIT MODE
Default	007d0000	0000	0000	0111	1101	0000	0000	0000	0000	00000000011111010000000000000000
31	0	FRAC MODE								
30:15	00000000011111010	0000	0000	1111	1010	N DIVIDER				
	HEX 0	0	F	A						
	DEC	250								
14:03	000000000000	0000	0000	0000	FRAC VALUE					
	HEX	0	0	0	0					
	DEC	0								
2:0	00	ADDRES								

## Registro 1

		1	2	3	4	5	6	7	8	BIT MODE
Default	2000FFF9	0010	0000	0000	0000	1111	1111	1111	1001	00100000000000001111111111111001
31	0	CLAMP OUTPUT DISABLE								
30:29	01	01	CP LINEARITY (FRAC MODE)							
	HEX	1								
	DEC	1								
28:27	00	00	CHARGE PUMP NORMAL							
	HEX	0								
	DEC	0								
26:15	000000000001	0000	0000	0001	PHASE VALUE					
	HEX	0	0	1						
	DEC	1								
14:3	111111111111	1111	1111	1111	MOD VALUE					
	HEX	F	F	F						
	DEC	4095								
2:0	001									

Registro 2

			1	2	3	4	5	6	7	8	BIT MODE
Default	00004042		0000	0000	0000	0000	0100	0000	0100	0010	00000000000000000100000001000010
31	0										LOCK DETECT SPED
30:29	00		00								LOW NOISE
		HEX	0								
		DEC	0								
28:26	000		000								MUX_OUT THREE STATE
		HEX	0								
		DEC	0								
25	0		0								DOUBLER DISA
		HEX	0	0	0						
		DEC	0								
24	0		0								DIVDER-2 DISA
		HEX	0	0	0						
		DEC	0								
23:14	0000000001		0000	0000	01						DIVIDER VALUE
		HEX	0	0	1						
		DEC	1								
13	0		0								DOBLE BUFFER DIS
		HEX	0								
		DEC	0								
12:9	0000		0000								CHARGE PUMP CURRENT = 0.32mA
		HEX	0								
		DEC	0								
8	0		0								FRAC-N LOCK DETECT
7	0		0								LOCK DETEC PRES = 10nS
6	1		1								PHASE DETECTOR POSITIVE
5	0		0								POWER MODE NORMAL
4	0		0								CHARGE PUMP THREE STATE DISA
3	0		0								COUNTER RESET MODE = NORMAL

### Registro 3

[illegible]

## Registro 4

		1	2	3	4	5	6	7	8	BIT MODE
Default	6180B23C	0110	0001	1000	0000	1011	0010	0011	1100	01100001100000001011001000111100
31:26	011000	0110	00	RESERVED. PROGRAM TO 0110000						
	HEX	6	0							
	DEC	96								
25:24	01	01	BAND SELECT MSB							
	HEX	1								
	DEC	1								
23	1	VCO FEDBACK MODE = FUNDAMENTAL								
22:20	000	000	RF OUTPUT DIVIDER = 1							
	HEX	0								
	DEC	0								
19:12	00001011	0000	1011	BAND SELECT CLK DIVIDER VALUE = 267						
	HEX	0	B							
	DEC	11								
11	0	RESERVED PUT IN 0								
10	0	RESERVED PUT IN 0								
9	1	RF_B VCO DIVIDED								
8	0	RF_B OUTPUT DIS								
7:6	00	00	RF_B OUTPUT POWER = -4dbm							
	HEX	0								
	DEC	0								
5	1	RF_A OUTPUT EN								
4:3	11	11	RF_A OUTPUT POWER = +5dbm							
	HEX	3								
	DEC	3								

## Registro 5

			1	2	3	4	5	6	7	8	BIT MODE
Default	00400005		0000	0000	0100	0000	0000	0000	0000	0101	00000000010000000000000000000101
31:25	0000000		0000	000	RESERVED. PROGRAM TO 00000000						
		HEX	0	0							
		DEC	0								
24	0	AUTO SET OF INTEGER MODE IF F = 0 EN									
23:22	01		01	LOCK DETECT MODE = DIGITAL							
		HEX	1								
		DEC	1								
21:19	000		000	RESERVED. PROGRAM TO 000							
		HEX	0								
		DEC	0								
18	0	MUX_OUT MSB									
17:3	0000000000000000		0000	0000	0000	000	RESERVED PROGRAM TO 0000 0000 0000 000				
		HEX	0								
		DEC	0								