

Czech Technical University in Prague

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Faculty of Electrical Engineering Department of measurement

Manual for Raspberry Pi Pico diagnostic logic probe

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Specialization:

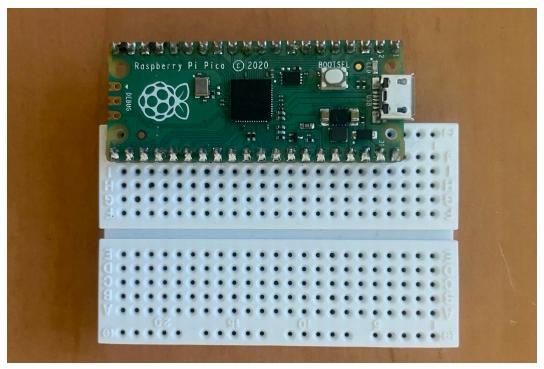
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Contents

1	Build	2
	1.1 Firmware setup	2
2	Terminal mode	3
	2.0.1 Activation	3
	2.1 Pinout	4
	2.2 Universal keys	4
	2.3 MENU	5
	2.4 Basic Mode	5
	2.4.1 Page Voltmeter/Ohmmeter	5
	2.4.2 Page Signal detection	8
	2.4.3 Page Signal generator	10
	2.4.4 Page Logic Level	12

Section 1

Build



1. Put Raspberry Pi Pico to edge of the breadboard with GPIO pins supporting ADC facing inside

1.1 Firmware setup

- 1. Connect RPI Pico to the the PC via USB
- 2. Check if new external storage was added
- 3. Move firmware file (logic_probe_rpi.u2f) to external storage
- 4. Reconnect device

Section 2

Terminal mode

This mode enables deeper circuit diagnostic with PC control. It offers the following features:

- Voltage measure
- · Logic Level measure
- · Resistence measure
- Manual logic Level Control
- · Frequency measure
- · Pulse detection

2.0.1 Activation

Terminal mode is enabled when the device is powered with UART computer connection. To activate it:

- 1. Raspberry Pi Pico to PC via USB cable.
- 2. Led will flash green.
- 3. Connect to device via serial communication. Open PuTTY app and connect to specific COM (WIN) or /dev/ttyUSBx (Linux) with 115200 baudrate 8N1 (Figure 1)
- 4. After connection press **R** to start

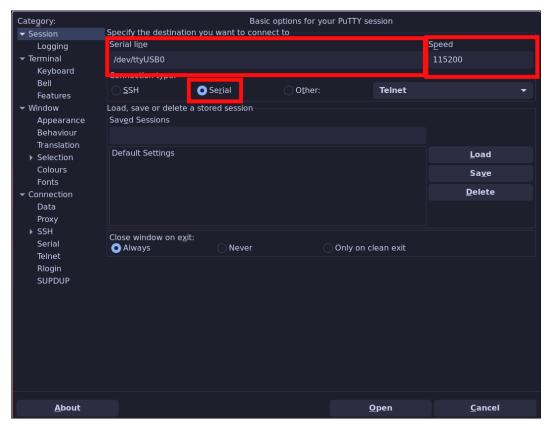


Figure 1: PuTTY app

2.1 Pinout

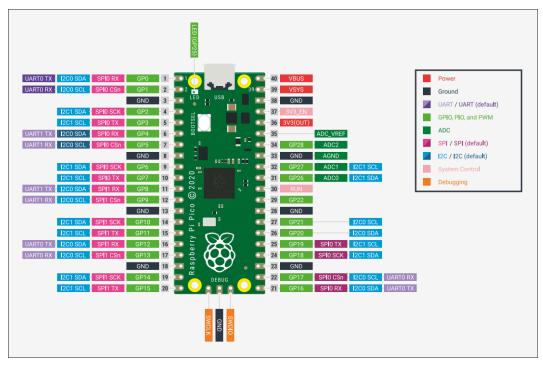


Figure 2: Picture from RPI manual (https://datasheets.raspberrypi.com/pico/pico-datasheet.pdf)

2.2 Universal keys

- ullet [R] reload page to fix broken ansi sequences
- [Q] return to menu

2.3 MENU

By pressing letter on keyboard written next to name, function can be selected.



Figure 3: Basic menu

2.4 Basic Mode

2.4.1 Page Voltmeter/Ohmmeter

■ Voltmeter and Logic Levels

Pinout

- (GP26/31): Measures Channel 1
- (GP27/32): Measures Channel 2
- (GP28/34): Measures Channel 3

Control

- [1-3] Used to turn the respective measurement channel on or off.
- [S] -Confirms (saves) the current on/off settings for the channels.
- [M] Switches between Voltmeter and Ohmmeter mode.

Description

The voltmeter allows measurements on multiple channels. Different device versions may vary in the number of available measurement channels.

In addition to the measured value, an indicator of its logic level is displayed for each channel:

- If **H** is displayed: The voltage on the channel corresponds to a High level.
- If L is displayed: The voltage on the channel corresponds to a Low level.
- If ? is displayed: The channel status is indeterminate (e.g., the input is floating, or in a transitional state).
- If the symbol \mathbf{x} is displayed instead of a numerical voltage value for a channel, it means that the channel is currently turned off.

Example: To turn off channel 1, press the [1] key. To confirm and save this change, subsequently press the [S] key



Figure 4: Voltmeter page

Ohmmeter

Pinout

• (GP26/31): Measures Channel 1

Control

- [E] Enter or exit edit mode for the base resistor (R1) value.
- [0-9] Enter digits for the R1 value.
- [X] Delete the last entered digit for R1 / Clear R1 value.

• [M] - Change mode to Voltmeter.

Description

Pressing the [M] key changes the current mode to **Ohmmeter mode**. The Ohmmeter page illustrates how to connect a resistor to perform the measurement. The reference voltage (**Vdd**) used for the calculation is displayed on this page.

A known base resistor (R1) is used for the calculation of the unknown measured resistance. The value of this base resistor R1 can be changed by first pressing the [E] key to enter edit mode. The numerical value can then be entered using the number keys [0-9] and can be corrected using the [X] key (to delete digits or clear the input). Pressing [E] again exits edit mode.

The calculated value of the measured resistor is displayed in a green field on the screen.



Figure 5: Ohmmeter page

2.4.2 Page Signal detection

Frequency

Pinout

• (GP21/27): Measures frequency

Control

- [T] Change gate time (measurement period).
- [M] Changes the mode (e.g., to Rise edge, Fall edge, or the next main function).

Description

In this mode, the device measures several characteristics of an incoming signal:

- Frequency
- Reciprocal frequency¹
- · Pulse width
- Duty cycle (or active pulse time)

The [T] key allows the user to cycle through different gate time settings, which defines the period over which the frequency or other time-related parameters are measured. A longer gate time can provide more accurate readings for lower frequencies but takes longer, while a shorter gate time is faster and better for higher frequencies.



Figure 6: Frequency page

¹The difference between frequency and reciprocal frequency lies in the calculation method and resulting unit (Hz vs. seconds).

Pulse Detection

Pinout

• (GP21/27): Measures frequency

Control

- $[\mathbf{D}]$ Clears the flag indicating a caught signal.
- [M] Changes the mode (e.g., to Rise edge, Fall edge, or the next main function).

Description

Pressing the [M] key cycles between Rise edge mode, Fall edge mode and back to Frequency.

- **Rise edge** mode is designed to catch a rising edge of a signal.
- Fall edge mode is designed to catch a falling edge of a signal.

If the selected type of edge is detected on the input pin, a "Flag" will be set to TRUE on the display. This flag indicates that an event has occurred. To reset or clear this flag (e.g., to prepare for catching a new pulse), press the [D] key.



Figure 7: Pulse page

2.4.3 Page Signal generator

Pinout

- (GP17/22): Generates signal
- (GP16/21): Generates permanent frequency

Control

- [E] Enter or exit edit mode for pulse width.
- [T] Toggle signal generation mode (Pulse Up / Pulse Down).
- [S] Send the configured signal.
- [1-9] Enter digits for the value currently being edited (pulse width or repeat count).
- [X] Delete the last entered digit for the value currently being edited.
- [D] Send periodic signal.

Description

This page allows for the generation of digital signals on the output pin. The user can define the pulse width and the number of times the signal should repeat.

The signal generation has two modes, which can be toggled using the [T] key:

- Pulse Up Mode: In this mode, the output pin (PA0) defaults to a LOW LEVEL. When the signal is triggered using the [S] key, the pin will switch to a HIGH LEVEL for the duration specified by the "pulse width" setting.
- Pulse Down Mode: In this mode, the output pin (PA0) defaults to a HIGH LEVEL. When the signal is triggered using the [S] key, the pin will switch to a LOW LEVEL for the duration specified by the "pulse width" setting.

To set the pulse width, press the [E] key to enter edit mode, then use the number keys [1-9] to input the desired value. The [X] key can be used to delete digits. Press [E] again to confirm or exit edit mode. S After configuring the desired parameters, pressing the [S] key will send the signal sequence.

After pressing **[D]** key, signal is generated permanetly with frequency showed on display. Frequency can be edit via period.

Figure 8: GPIO SET Levels page

2.4.4 Page Logic Level

Pinout

(GP18/24): Toggle output (GP19/25): Toggle output

• (GP20/26): Toggle output

Control

• [1-3] - Toggle output

Description

This page allows to toggle output pin.

Figure 9: Generate signal page