

Czech Technical University in Prague

F3

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Manual for STM32 diagnostic logic probe

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Section 1

Build

1.1 STM32G030 SOP8

TODO: doplnit podrobnosti

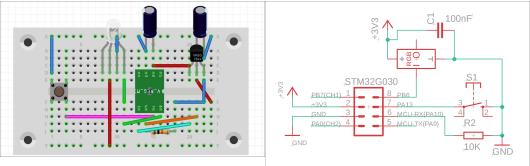


Figure 1: Diagram SOP8 ve Fritzingu

Figure 2: Diagram SOP8

■ 1.2 STM32G030 TSSOP20

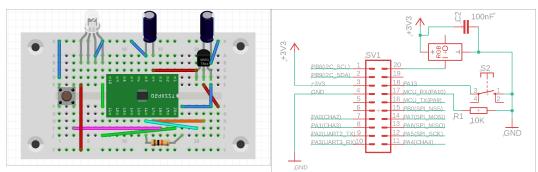


Figure 3: Diagram TSSOP20 ve Fritzingu

Figure 4: Diagram TSSOP20

Section 2

Local mode

This mode is used to quickly identify the problem without having to connect to a desktop computer. It allows the following actions:

- · analysing circuit by simple logic probe
- · detecting rise or fall edge
- · setting logic levels on channels

By holding the button for a long time (approx. 500 ms) it is possible to switch between modes. The modes are differentiated by RGB LEDs, where the colour of the mode is displayed when the button is released.

2.0.1 How to enable

Local mode is enabled by the user connecting the assembled circuit only to the power supply. This means that the mode is activated when after switching on the device it detects a **logic zero** on the **recieve pin**, which is intended for communication with the computer. When the communication with the computer is connected, the default is logic one.

The activation of the mode can be detected so that when switched on, a sequence of colours on the RGB LEDs is triggered.

2.1 Logic probe mode

- LED Color: AQUA
- Channels are switched with button press. After press, LED shows color of the selected channel.
 - ► Channel 1: PURPLE
 - ► Channel 2: YELLOW
- LED indicates if state is HIGH by GREEN color, LOW state by RED color, and interstate by led turned off.
- **SOP8** logic probe can be use on PB7(CH1) and PA0(CH2) pins.

2.2 Logic levels

- LED Color: ORANGE
- Channels are switched with button double press. After double press, LED shows color
 of the selected channel.
 - ► Channel 1: PURPLE
 - ► Channel 2: YELLOW
- Logic levels are changed with **button press**. After press, level on channel is toggled. State of the channel is indicated by LED.
- **SOP8** logic levels can be toggled on PB7(CH1) and PA0(CH2) pins.

2.3 Generating periodic pulses

- LED Color: WHITE
- PA0 is used for generating pulses
- after **button press** generation is turned on, another press generation is turned off. While on LED is WHITE.

2.4 Rise edge detection

- LED Color: BLUE
- PA0 is used for detection of rising edge
- if edge is detected, Green color is indicated. After one second led is turned off.

2.5 Falling edge detection

- LED Color: Purple
- PA0 is used for detection of falling edge
- if edge is detected, Green color is indicated. After one second led is turned off.

Section 3

Terminal mode

This mode is used for deeper identification of the problem.

3.1 Universal keys

- [R] reload page to fix broken ansi sequences
- [Q] return to menu

3.2 How to enable

Terminal mode is enabled by connecting device through converter. After connecting device to USB, open terminal which supports ANSI sequences. Recommended application is **PUTTY**.

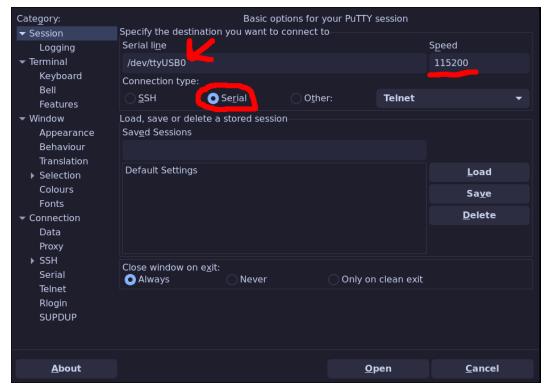


Figure 5: Putty settings

Select Serial as connection type. Enter **115200** as Speed of the serial communication and enter correct type of serial line. Usually on linux /dev/ttyUSB0.

After connection, press letter R on keyboard and menu appear.

3.3 PINOUT

Colors of wires show location of connect devices. If some text is colored, like PAO, that is pin 4(SOP8)/pin 7(TSSOP20).

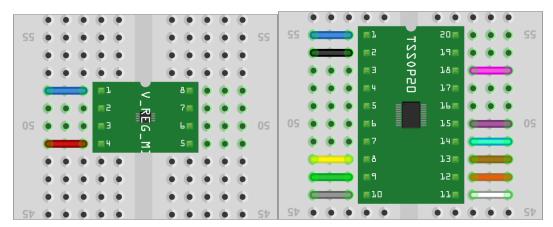


Figure 6: Voltage channels SOP8

Figure 7: Voltage channels TSSOP20

3.4 MENU

The menu is divided into basic functions and advanced functions. In the **SOP8** version only the basic function is available. By pressing letter on keyboard written next to name, function can be selected.

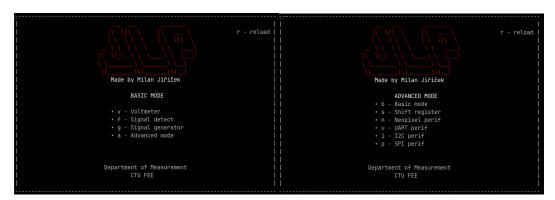


Figure 8: Basic menu

Figure 9: Advanced menu

3.5 Basic Mode

3.5.1 Voltmeter/Ohmmeter

Voltmeter

Voltmeter shows different channels. Versions differs in the number of channels. Page shows **Reference voltage**, which is used for calibrating channel voltage.

• In paranthesses is shown which pin is used as channel.

- [1-4] Toggle channel on or off
- [S] Apply settings of toggling channels
- [M] switch between Voltmeter and ohmmeter Red arrow points to the logic level of voltage.
- If shows H, channel is in high level
- If shows L, channel is in low level
- If shows?, channel is in interstate

If channel has \mathbf{x} instead of value, it is turned off. To turn off or turn on the channel, it is nesessary to press key of the number.

Example: For turn off the channel 1 press **1** on the keyboard and then **S** for saving the value.



Figure 10: Voltmeter page

Figure 6 and Figure 7 shows, which pins are used for channels. SOP8 has channel 1(PB7) and channel 2(PA0), but TSSOP20 has also channel 3(PA1) and channel 4(PA2).

Ohmmeter

By pressing **M**, in mode is changed to **Ohmmeter mode**. Ohmmeter page, illustrate, how to connect resistor to measure Ohms. Next to **Vdd** is showed, what reference voltage is used. R1 resistor is based. this resistor can be changed by pressing **E**. Value can be entered with number keys and delete with **X**. In green field is calculated value of measured resistor. Resistance is measured on **Channel 1(PB7)**.

- [E] edit base resistor and exit edit mode
- [0-9] changing value
- [X] delete number
- [M] change mode to voltmeter



Figure 11: Ohmmeter page

3.5.2 Signal detect

Frequency

Frequency is measured on pin PAO. Device can measure, frequency, reciprocial frequency, width of pulse and duty time. By key T, user can change gate time.

- [T] change gate time
- [M] change mode



Figure 12: Frequency page

¹Frequency and reciprocial frequency difference is in way of calculating.

3.5.2.1.1 Pulse

Pressing **M** will change mode to pulse up or down mode. Pulse up will catch rising edge and pulse down will catch falling edge. If edge is catched, Flag will be changed to TRUE. To delete flag, press **D**. Pulse is catched on **PA0**.

- [D] delete flag of catched signal
- [M] change mode



Figure 13: Pulse page

3.5.3 Signal generator

- [E] edit pulse width or end edit mode
- [Y] edit repeat count or end edit mode
- [T] toggle mode from pulse up to pulse down
- [S] send signal
- [1-9] change pulse width
- [X] delete pulse width number

On PAO pin can be send signal with specific pulse width. Width can be edited by E. There are two modes. When pulse up is choosed, default pin setup is LOW LEVEL. When signal is sent HIGH LEVEL is set for specified time. When pulse down is choosed, default pin setup is HIGH LEVEL and when signal is sent LOW LEVEL is set for specific time. By changing repeat counter, user can generate signal x times.

Figure 14: Generate signal page

■ 3.6 Advanced Mode

This function is only on TSSOP20!

3.6.1 SHIFT Register

Pin PA0 is used for send data and pin PB7 is used as clock signal. In case of 74HC595 pin PA1 is used as Output enable.

User can change sequence for shift register which will be sent.

- [1-8] Toggle 1 or 0 on specified position
- [S] send sequence



Figure 15: Neopixel page

3.6.2 NEOPIXEL

Neopixel perif, can be read or wrote. Modes can be changed by pressing **M**. Neopixel is read on pin PAO. When signal is detected. Value is showed on the display. Data can be send to Neopixel by pin PA13.

On write, user can choose color from 0 to 255 for every color element. By pressing **S** selected signal will be sent.

- [0-9] Change value
- [X] Delete value
- [M] Change mode
- [K] Edit values
- [L] Move cursor
- [S] Send neopixel data



Figure 16: Neopixel page

3.6.3 UART

UART peripheral, can be read and write. Modes can be changed by pressing **M**. MCU **TX** and MCU **RX** is connected to PA2 and PA3. Read mode shows letters which are accepted. Write mode has 10 bytes and parameter SEND BYTES set how many bytes are sent. In settings mode, user can change word length, parity, number of stopbits and baudrate. In value mode and settings mode can be edit by numkeys and letter X. By pressing **S** selected signal will be sent.

- [0-9] Change value
- [X] Delete value
- [M] Change mode
- [K] Edit values
- [L] Move cursor
- [S] Send data

- [T] Edit settings
- [O] Count bytes to send
- [Y] Word length
- [U] Switch between parity
- [I] Number of stopbit

Figure 17: UART page

3.6.4 I2C

I2C peripheral can be read and write. Modes can be changed by pressing M. SCL use PB8 and SDA use PB9.

First mode is address scan, where probe shows, which adress on I2C is active. Second mode is slave mode, where probe is behaving as slave. User can set I2C address of the slave. Third mode is master mode, where probe is behaving as master. To selected adress, probe send entered data by user. Fourth mode is testing **SSD1306** display, where after pressing **S**, display set all pixels active.

- [0-F] Change hex value
- [X] Delete value
- [M] Change mode
- [K] Edit values
- [L] Move cursor
- **[S]** Send data
- [T] Edit settings
- [Y] Change bytes to send
- [U] Switch read write

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$\frac{12C SCAN ADDRESS}{5} & $\frac{12C SCAN ADDRESS}{5} & $\frac{1}{5} & $\frac{12C SCAN ADDRESS}{5} & $\frac{1}{5} & $\frac
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Figure 18: I2C Scan address page

Figure 19: I2C Slave page

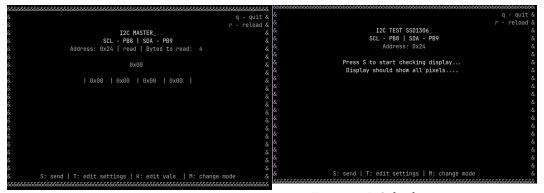


Figure 20: I2C Master page

Figure 21: I2C display testing page

3.6.5 SPI

SPI peripheral can be read and write. Modes can be changed by pressing M. SCK use PA5, MISO use PA6, MOSI use PA7 a NSS use PB0.

First mode is slave mode, where probe is behaving as slave. second mode is master mode, where probe is behaving as master. Fourth mode is testing **SSD1306** display, where after pressing **S**, display set all pixels active.

- [0-F] Change hex value
- [X] Delete value
- [M] Change mode
- [K] Edit values
- [L] Move cursor
- [S] Send data
- [T] Edit settings
- [Y] Switch Phase
- [U] switch Polarity
- [P] read/write
- [I] Change bytes to send

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q - quit % y q - quit % y q - quit % y press Spi TeST SSD1366 y preload % y polarity: 0 | Phase: 0 | MSB | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read: 5 % y polarity: 0 | Phase: 0 | MSB | read | bytes to read
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Figure 22: SPI Slave page

Figure 23: SPI Display test page

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Figure 24: SPI Master page