DGA，延迟，flops，双限制

[[('conv\_5\_LeakyReLU', 0), ('conv\_5\_LeakyReLU', 1), ('conv\_5\_LeakyReLU', 1), ('conv\_5\_LeakyReLU', 2)], range(1, 5), [('dilg\_conv\_5\_RELU', 0), ('dilg\_conv\_3\_LeakyReLU', 1), ('max\_pool\_3', 1), ('conv\_5\_RELU', 3)], range(1, 5), [('skip\_connect', 0), ('skip\_connect', 0), ('avg\_pool\_3', 0), ('conv\_5\_ELU', 0)], range(1, 5), [('skip\_connect', 0), ('lp\_pool\_3', 0), ('skip\_connect', 2), ('avg\_pool\_3', 2)], range(1, 5), [('avg\_pool\_3', 0), ('skip\_connect', 0), ('skip\_connect', 2), ('skip\_connect', 3)], range(1, 5), [('g\_conv\_3\_RELU', 0), ('g\_conv\_5\_LeakyReLU', 0), ('dilg\_conv\_3\_ELU', 1), ('dilg\_conv\_5\_ELU', 3)], range(1, 5), [('conv\_5\_RELU', 0), ('skip\_connect', 0), ('max\_pool\_3', 2), ('skip\_connect', 3)], range(1, 5), [('skip\_connect', 0), ('max\_pool\_3', 0), ('skip\_connect', 2), ('skip\_connect', 2)], range(1, 5), [('avg\_pool\_3', 0), ('avg\_pool\_3', 0), ('avg\_pool\_3', 0), ('skip\_connect', 2)], range(1, 5), [('avg\_pool\_3', 0), ('max\_pool\_3', 0), ('skip\_connect', 0), ('max\_pool\_3', 3)], range(1, 5)]

[[('conv\_5\_LeakyReLU', 0), ('conv\_5\_LeakyReLU', 1), ('conv\_5\_LeakyReLU', 1), ('conv\_5\_LeakyReLU', 2)], range(1, 5)]

[[('conv\_5\_LeakyReLU', 0), ('conv\_5\_LeakyReLU', 1), ('conv\_5\_LeakyReLU', 1), ('conv\_5\_ELU', 1)], range(1, 5)]

[[('conv\_5\_LeakyReLU', 0), ('conv\_5\_LeakyReLU', 1), ('conv\_5\_LeakyReLU', 1), ('conv\_5\_LeakyReLU', 2)], range(1, 5)]

[[('conv\_5\_LeakyReLU', 0), ('conv\_5\_LeakyReLU', 1), ('conv\_5\_LeakyReLU', 1), ('conv\_5\_LeakyReLU', 2)], range(1, 5)]

DNS，延迟，flops，双限制

[[('conv\_5\_ELU', 0), ('conv\_5\_LeakyReLU', 0), ('avg\_pool\_3', 2), ('conv\_5\_ELU', 3)], range(1, 5)]

（第一层全skip-connect）

[[('skip\_connect', 0), ('skip\_connect', 1), ('skip\_connect', 2), ('skip\_connect', 3)], range(1, 5), [('max\_pool\_3', 0), ('max\_pool\_3', 0), ('max\_pool\_3', 1), ('max\_pool\_3', 3)], range(1, 5)]

[('dilg\_conv\_5\_RELU', 0), ('max\_pool\_3', 1), ('max\_pool\_3', 2), ('avg\_pool\_3', 2)], range(1, 5)]

(差不多全是skip-connect)

[[('max\_pool\_3', 0), ('skip\_connect', 1), ('skip\_connect', 2), ('skip\_connect', 3)], range(1, 5), [('lp\_pool\_3', 0), ('max\_pool\_3', 1), ('max\_pool\_3', 2), ('skip\_connect', 3)], range(1, 5)]

IOT，延迟，flops，双限制

[[('dilg\_conv\_5\_LeakyReLU', 0), ('conv\_5\_LeakyReLU', 1), ('conv\_5\_LeakyReLU', 2), ('conv\_5\_LeakyReLU', 3)], range(1, 5), [('dilg\_conv\_5\_RELU', 0), ('lp\_pool\_3', 1), ('lp\_pool\_3', 2), ('lp\_pool\_3', 2)], range(1, 5), [('lp\_pool\_3', 0), ('lp\_pool\_3', 1), ('conv\_5\_RELU', 2), ('conv\_5\_RELU', 2)], range(1, 5), [('conv\_3\_ELU', 0), ('conv\_5\_LeakyReLU', 1), ('max\_pool\_3', 2), ('max\_pool\_3', 3)], range(1, 5), [('lp\_pool\_3', 0), ('conv\_5\_RELU', 1), ('conv\_3\_LeakyReLU', 0), ('conv\_3\_RELU', 0)], range(1, 5), [('conv\_5\_RELU', 0), ('lp\_pool\_3', 1), ('lp\_pool\_3', 1), ('lp\_pool\_3', 1)], range(1, 5), [('conv\_5\_RELU', 0), ('conv\_5\_RELU', 1), ('conv\_5\_LeakyReLU', 1), ('lp\_pool\_3', 2)], range(1, 5), [('conv\_5\_ELU', 0), ('g\_conv\_5\_RELU', 0), ('g\_conv\_5\_ELU', 2), ('conv\_5\_LeakyReLU', 3)], range(1, 5)]

[[('conv\_5\_LeakyReLU', 0), ('conv\_5\_LeakyReLU', 1), ('conv\_5\_RELU', 2), ('conv\_5\_RELU', 3)], range(1, 5)]

[[('conv\_5\_LeakyReLU', 0), ('conv\_5\_RELU', 1), ('conv\_5\_RELU', 2), ('conv\_5\_RELU', 3)], range(1, 5)]

[[('conv\_5\_LeakyReLU', 0), ('conv\_5\_LeakyReLU', 1), ('conv\_5\_RELU', 2), ('conv\_5\_RELU', 3)], range(1, 5)]