

INDIAN INSTITUTE OF TECHNOLOGY

DATE 4.1.234

Exp 1. Introduction to Microchip Studio, SimulIDE and Hardware Programming Tool with LED blink

SHEET NO. 1

• Sim: To program ATmega32 to control the blinking of an LED.

• Apparatus reqd:

	Name.	Specification	Quantity
1.	AT Mega 32 microcontroller.	—	1.
2.	Resistor	100Ω.	1.
3.	LED.	—	1.

• Pseudo code:

1. set pin C0 as output.

2. main:

set pin C0 high.

call delay routine

set pin C0 low.

call delay routine

goto main.

3. delay: # count from $(7FFFF)_{16} \rightarrow (0)_{16}$

$r_{18} \leftarrow (7)_{16}$

$L_3: r_{17} \leftarrow (FF)_{16}$

$L_2: r_{16} \leftarrow (FF)_{16}$

$L_1: r_{16} --$

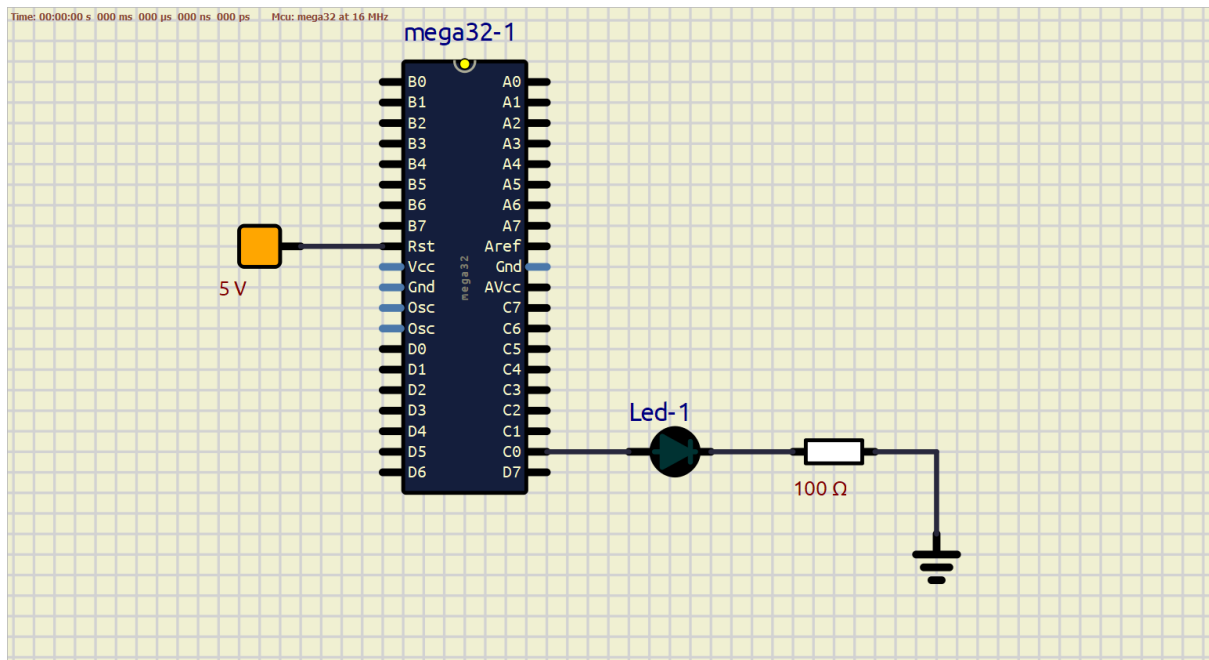
if $(z \neq 0)$ goto L_1 .

$r_{17} --$

if $(z \neq 0)$ goto L_2 .

~~Dec~~ $r_{18} --$

if $(z \neq 0)$ goto L_3
return.



Circuit Diagram

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1) Cooli.

. INCLUDE "M32DEF.INC"

. ORG 0

SBI DDRC, PINCO

LDI R16, HIGH(RAMEND).

OUT. SPH, R16.

LDI R16, LOW(RAMEND)

OUT SPL, R16

~~MA~~

MAIN:

SBI PORTC, PINCO

CALL DELAY

CBI PORTC, PINCO

CALL DELAY

DELAY:

LDI R18, 0x07.

N3: LDI R17, 0xFF

N2: LDI R16, 0xFF

N1: DEC R16. **

* *

BRNE N1.

DEC R17

BRNE N2.

DEC R18.

BRNE N3

RET.

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» Explanation:

To blink the LED we needed a to turn it on and off indefinitely with a delay in between.

The delay block is basically a counter that counts from $(7FFFF)_{16}$ to $(0)_{16}$. It does so by concatenating (hypothetically) three 8 bit registers $\{r_{18}, r_{17}, r_{16}\}$. where during init $r_{18} \leftarrow 07F$; $r_{17} \leftarrow FF$; $r_{16} \leftarrow FF$. We decrease the least significant register (r_{16}), until it becomes 0, further decrement will incur a carry. Thus r_{17} is decr. Thus r_{17} will also go zero at some point, hence a carry from r_{18} is incurred. Finally when r_{18} ~~stop~~ becomes zero counter stops and transfers execution to main block.

$$\therefore f_{clk} = 16 \text{ MHz.}$$

$$\text{and counter} = (7FFFF)_{16} \text{ steps } (\times 2).$$

$$\therefore f_{\text{delay}} = \frac{16 \text{ M.}}{2^{19} \times 2} \approx 32 \text{ Hz. } 16 \text{ Hz.}$$

$$\Rightarrow t_{\text{delay}} = \frac{1}{32768} \approx 0.3 \text{ sec. } 0.03 \text{ sec}$$

$$\Rightarrow t_{\text{delay}} = \frac{1}{16} \approx 62.5 \text{ ms.}$$